



Dr. D. Y. Patil Group of Institutions' Technical Campus  
**Dr. D. Y. PATIL SCHOOL OF ENGINEERING**  
Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune – 412 105.  
Department of Electronics and Telecommunication Engineering  
Form No. IQAC/23 (f)

Date:16/ 09 /2019

### Event Report

**Academic Year: 2019-20**

**Semester-I**

Name of the event: PCB Design Workshop

<b>Date and Time</b>	13/09/2019-14/09/2019
<b>Event Venue</b>	DYPSOE (E&TC Dept.) PROTOTECH COMPANY(Narhe,Pune)
<b>Organized by</b>	E&TC Department
<b>Targeted Audience</b>	Third Year Students
<b>Resource Person</b>	<b>Mr. Mubarak Tamboli</b> <b>Senior Hardware Engineer ,TCS , Pune</b>

Event Contents:

1. PCB Manufacturing Process
2. PCB Design Flow
3. Visit to PCB manufacturing plant

Details of the event:

Two days PCB knowledge transfer session for students along with actual PCB manufacturing process plant visit.

Day 1:(13 th Sept.2019)

1) Theory of PCB,PCB Design and Its Importance : Explanation of PCB from schematic design to actual PCB design flow and implementation

What is importance of PCB and how schematic and PCB design is done is explained.  
Basics and industry level points, experiences discussed by Mr. Mubarak Tamboli  
Followed with Students Questions and thoughts session.

Timings: Day 1: (10:30 AM to 4:00 PM)

Day 2:(14 Sept. 2019)

Industrial Visit to PCB manufacturing Plant located in Pune.

Timing: ( 10.30 to 2.00)

Photos:





Name and Sign of Coordinator: Prof. Yogesh Y. Shinde

Departmental Event Coordinator: Prof. Prajakta Khairner

HoD: Dr. S.M. Koli