

AICTE ID - 1-3847411 AISHE Code: C-46648 DTE Code: EN6732

SPPU PUN Code: CEGP015720

(Approved by AICTE, Recognized by Govt. of Maharashtra, Affiliated to Savitribai Phule Pune University)
(Accredited by NAAC, NABL & ISO 9001:2015 & 21001:2018 Certified Institute)

Criteria 1

1.1: Curricular Planning and Implementation

1.1.1: The Institution ensures effective curriculum planning and delivery through a well-planned and documented process including Academic calendar and conduct of continuous internal Assessment.

List of supporting documents

Sr. No.	Name of the Document
1.	SPPU academic calendar
2.	Institute academic calendar
3.	Departmental academic calendar
4.	Subject Choice for faculty
5.	Load Distribution
6.	Academic Progress Report
7.	Industrial visit Report
8.	Seminar/Guest Lecture Report
9.	Result Analysis
10.	Course File



Principal
Ajeenkya DY Patil School of
Engineering, Lohegaen, Pune

Savitribai Phule Pune University

(Formerly University of Pune)

Circular No.484of 2022

Dates of Commencement and Conclusion of terms for the Academic Year 2022-23 for University Department / Affiliated Colleges / Recognised Institutes.

It is hereby informed that, the dates of commencement and conclusion of the First and Second term of University Courses, under various faculties, for the academic year 2022-23 shall be as under as per DTE & CET Cell admission notification:

			2022	- 2023								
Sr No	Name of the Courses , Faculties & Year	First'	Term	Second Term								
		Commencement	Conclusion	Commencement	Conclusion							
	Science & Technology	Science & Technology										
1	B.E./B.Tech : Ist Year	04/11/2022	20/02/2023	20/03/2023	15/07/2023							
	M.E./M.Tech : Ist Year	27/10/2022	20/02/2023	20/03/2023	15/07/2023							
	Commerce & Management											
2	M.B.A. : Ist Year	10/11/2022	03/03/2023	20/03/2023	15/07/2023							
	M.C.A. : Ist Year	09/11/2022	03/03/2023	20/03/2023	15/07/2023							
	Humanities											
3	L.L.B. : Ist Year (3 Years)	13/12/2022	04/04/2023	24/04/2023	12/08/2023							
3	L.L.B. : Ist Year (5 Years)	13/12/2022	04/04/2023	24/04/2023	12/08/2023							
	L.L.M. : Ist & IInd Year	03/10/2022	28/01/2023	20/02/2023	17/06/2023							
	Inter-disciplinary Studies											
	B.Ed. : Ist Year	19/12/2022	12/04/2023	24/04/2023	12/08/2023							
4	B.P.Ed. : Ist Year	12/12/2022	04/04/2023	24/04/2023	12/08/2023							
	M.Ed. /M. P. Ed.	14/12/2022	04/04/2023	18/04/2023	05/08/2023							
	Fine Arts	12/12/2022	04/04/2023	24/04/2023	12/08/2023							

NOTE:

 In case, the Principal of the Affiliated Colleges require to give additional holiday in exceptional circumstances, he/she may do so by compensating the same by keeping the College working on Sunday.

Ganeshkhind, Pune-07 Ref. No. PGS/6275 Date:29/12/2022

Principal
Ajeenkya DY Patil School of
Engineering, Lohegaon, Pune

Assistant Registrar
(P.G.Admission)

Lohegaon

Pune



"Empowerment through Quality Technical Education"

AJEENKYA DY Patil School of Engineering

D Y Patil Knowledge City, Charoli (Bk), Via Lohegaon, Pune 412105

Institute Academic Calendar 2022-23 [Tentative] Form No:IQAC/01(a)

			JUNE :	2022	×			a. Admission Committee Meeting with Principal b. Staff identification, advertisement, recruitment
SUN	MON	TUE	WED	THU	FRI	SAT	1000	c. Meeting for preparation of annual budget
			1	2	3	4	a-b	d. First Meeting of College Development Committee [CDC] e. A. Y. 2021-22 Teacher diary submission
					1074			f. Staff performance appraisal
5	6	7	8	9	10	11	c-d-e	g. Dead stock verification h. 360 Degree Feedback of Institute Staff
12	13	14	15	16	17	18	f-g-h	i. Service book updation
19	20	21	22	23	24	25	i-j-k	j. A. Y. 2022-23 IQAC plan of action k. AQAR 2022-23 Activity Calendar
25	27	28	29	30			1	1.5 Days (30 hours) Faculty development Program /Workshop for Teaching from department of E&TC Engginering
			JULY :	2022				a. IQAC Meeting No. 1 for Annual Planning.
SUN	MON	TUE	WED	THU	FRI	SAT		b. NAAC & AQAR Criterion Chairpersons Meeting with Principal & IQAC Coordinator for execution of AQAR/NAAC activities Calendar
NAMES OF TAXABLE					1	2	a-b	c. Department Academic Calendar
			-		•		20	 d. Cell Coordinator Meeting with Principal & IQAC Coordinator e. Meeting of Section Incharges (TPO, CEO, Librarian, SDO) with Principal
3	4	5	6	7	8	9	c-d-e	f. Statutory & Non-Statutory Committee Meeting with Principal
10	11	12	13	14	15	16	f-g	g. Load Distribution, Course File, Staff Portfolio & Time table
17	18	19	20	21	22	23	h-i-j	h. Commencement of Teaching: SEM-I (18/07/2022) i. Induction program for Staff & students
					-			j. Identification of Slow learners & advanced learners
24	25	26	27	28	29	30	k-l-m	k.5 Days (30 hours) Faculty development Program/Workshop for Teachin from department of Mechanical Engineering
31			1					LBridge course for Slow Learners
			AUGUST	r 2022				m. Approval Process for Staff a. Administrative Training Program for Non-Teaching Staff from IQAC
SUN	MON	TUE	WED	THU	FRI	SAT		 b. Notice for Student Eligibility & Scholarship Form Submission c. University Affiliation
SUN	1 1	2	3	4	5	6	a	d.15th August - Independence Day Celebration
7	8	9 Mohram		11	12	13	b-c	e. Student's Feedback – I, Feedback Analysis & Action Taken Report f. Academic Progress Report - I
14	15-Aug	16-Parsi	17	18	19	20	d-e-f	g. Student's attendance, defaulter List-I & Action Taken Report
21	22	23	24	25	26	27	g-h	h. Unit Test -1 & Result Analysis i. Remedial Teaching & Retest
28	29	30	31- Ganesh	23	20	27	j-j	j. Department Advisory Board Meeting-I
28	29	30	31-Ganesn				H	Principal Ajeenkya DY Patil Sch Engineering, Lohegao





			Septemb	er 2022	A 18 PHA MARIANANA			a. Student Notice for Exam form Submission					
SUN	MON	TUE	WED	THU	FRI	SAT		b. SPPU INSEM EXAM, Invigilation & CAP duty c. Parents Teacher's Meet-1 (PTM-I)					
				1	2	3	a-b	d. Field Projects/Industrial Visits					
4	5	6	7	8	9	10	c	e. Academic Progress Report - II (APR-II)					
11	12	13	14	15	16	17	d	f. Student's attendance & defaulter List-II g. Review Meeting of AQAR chairpersons, Cell coordinators, Committee					
18	19	20	21	22	23	24	e-f	Coordinators with Principal & IQAC					
25	26	27	28	29	30	2000	g-h	h. IQAC Meeting No. 2					
			October	r 2022									
SUN	N MON TU		WED	THU	FRI	SAT		a. Student's Feedback - II, Feedback Analysis & Action Taken Report					
		F				1		b.Unit Test -II (UT-II) & Result Analysis of UT-II					
2	3	4	5-Dasra	6	7	8	а	c Remedial Teaching & Retest					
9	10	11	12	13	14	15	b	d. Audits, Review Meeting & Submission of AQAR 2021-22 to NAAC					
16	17	18	19	20	21	22	c-d-e	e. Review Meeting & Submission of NIRF 2023 to MHRD f. Diwali Holidays					
23	24 Diwali	25	26-Bhaubij	27	28	29	f	in Direction and State of the S					
30	31												
			Novembe	er 2022									
SUN	MON	TUE	WED	THU	FRI	SAT	0.00	a. Mock Examination (OR/PR), Course Exit Survey, Student Submission					
		1	2	3	4	5	a-b	b.Conclusion of Teaching:SEM-I (5/11/2022) c. SPPU OR / PR EXAM					
6	7	8 - Gurunanak	9	10	11	12	С	d. SPPU ENDSEM EXAM - Invigilation, CAP duty & Vacation e.5 Days (30 hours) Faculty development Program/Workshop for Teachi					
13	14	15	16	17	18	19	d	from department of Civil Engineering					
20	21	22	23	24	25	26	е	f. ISO: Surveillance Audit					
27	28	29	30				f	1					



Principal
Ajeenkya DY Patil School of
Engineering, Lohegaon, Pune

		Decemb	er 2022				a. Second Meeting of College Development Committee [CDC]					
UN MON	TUE	WED	THU	FRI	SAT		 b. Meeting with Principal to Organize International Conference c. Meeting with Principal to Organize Techfest/Project Competition 					
			1	2	3	a	d. 5 Days (30 hours) Faculty development Program/Workshop for					
4 5	6	7	8	9	10	b-c	Teaching from department of Computer Engineering e. Meeting for planning of Industrial Visits /Field Projects					
11 12	13	14	15	16	17	d	f. Review Meeting of NAAC/AQAR Chairpersons, Cell coordinators, and					
18 19	20	21	22	23	24	e	Committees with Principal & IQAC Coordinator g. Load Distribution, Staff Portfolio & Time-Table for SEM-II					
25 26	27	28	29	30	31	f-g	g. Load Distribution, Staff Portfolio & Time-1 able for SEM-II					
		January	2023				TABLE STATE OF THE					
UN MON	TUE	WED	THU	FRI	SAT		a. Commencement of Teaching: SEM-II (02/01/2023) b. Identification of Slow learners & Advanced learners					
1 2	3	4	5	6	7	a	c. Bridge course for Slow Learners					
8 9	10	11	12	13	14	b-c	d. 5 Days (30 hours) Professional development Program for Teaching fro IQAC					
15 16	17	18	19	20	21	d	e. 26th January - Republic Day Celebration					
22 23	24	25	26	27	28	е	f. Administrative Training Program for Non-Teaching Staff from IQAC g.Department Advisory Board Meeting-II					
29 30	31		- 1		A Property	f-g	Se spanning and a second in					
		Februar	y 2023				a. Participation in Institute Rankings					
UN MON	TUE	WED	THU	FRI	SAT		b. Student's Feedback – I, Feedback Analysis & Action Taken Report					
		1	2	3	4	а	c. Academic Progress Report - I d. Student's attendance, defaulter List-I & Action Taken Report					
5 6	7	8	9	10	11	b-c	e. Unit Test -I & Result Analysis					
12 13	14	15	16	17	18	d-e	f. Remedial Teaching & Retest g. 5 Days (30 hours) Faculty development Program/Workshop for					
19 20	21	22	23	24	25	f-g	Teaching from department of AI&DS					
26 27	28					h	h. NSS Activities					



Principal
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Engineering, Lohegaon, Pune

				2023	March						
a. Student Notice for Exam form Submission b. SPPU INSEM EXAM - Invigilation & CAP duty		SAT	FRI	THU	WED	TUE	MON	SUN			
a c. Parents Teacher's Meet-1 (PTM-I)	а	4	3	2	1		ATTE A				
d. Field Projects/Industrial Visits e. Academic Progress Report - II (APR-II)	b	11	10	9	8	7	6	5			
c-d f. Student's attendance , defaulter List-II & Action Taken Report	c-d	18	17	16	15	14	13	12			
e-f h.I.QAC Meeting No. 3	e-f	25	24	23	22	21	20	19			
g-h	g-h	27 28 29 30 31 g-h									
				2023	April 2						
a. Student's Feedback - II, Feedback Analysis & Action Taken Repor		SAT	FRI	THU	WED	TUE	MON	SUN			
a b. Unit Test -II & Result Analysis	a	1									
c. Remedial Teaching & Retest d.Tech Fest/Project Competition	ь	8	7	6	5	4	3	2			
e. Annual Social Gathering	c-d	15	14	13	12	11	10	9			
f. Graduation Day Celebration [BE Students Send-off] e-f-g g. Convocation	e-f-g	22	21	20	19	18	17	16			
h-i h.Mock OR/PR Exam & Student Submission	h-i	29	28	27	26	25	24	23			
i. Conclusion of Teaching:SEM-II (29/04/2023)	Economismos como		-	,				30			
a SPRII OR / RR EVAM				023	May 2			300000000000000000000000000000000000000			
a. SPPU OR / PR EXAM b. SPPU ENDSEM EXAM - Invigilation ,CAP duty & Vacation		SAT	FRI	THU	WED	TUE	MON	SUN			
c. Academic & Administrative audits d. Student Satisfaction Survey, Course & Programmme Exit Survey,	a	6	5	4	3	2	1				
b-c Stakehoders Feedback, Analysis & Action Taken Report	b-c	13	12	11	10	9	8	7			
e. Analysis of activities planned in Academic Calendar, IQAC Plan of a AQAR activity Calendar, DAB Activities & their action taken reports	d-e	20	19	18	17	16	15	14			
f. Website Updation & Annual Report	f	27	26	25	24	23	22	21			
g. IQAC Meeting No. 4	g				31	30	29	28			



IQAC Coordinator



Dr. F. B. Sayyad

Principal
Pi ACIPAL
Dr. D. Y. Patil School of Engineering
Lohegaon, Pune.



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Department Academic Calendar 2022-23 [Tentative]

Form No:IQAC/01

			JUNE	2022				
SUN	MON	TUE	WED	THU	FRI	SAT		
			1	2	3	4	b	b. Staff identification, advertisement, recruitment
5	6	7	8	9	10	11		e. A. Y. 2021-22 Teacher diary submission f. Staff performance appraisal
12	13	14	15	16	17	18	f-g	g. Dead stock verification
19	20	21	22	23	24	25		
25	5 27 28 29 30							
			JULY	2022		32 58462 035		
SUN	MON	TUE	WED	THU	FRI	SAT	Septimber 1	
					1	2	24	a.Guest Lecture 1 (AAI) b.Guest Lecture 2 (SPB)
3	4	5	6	7	8	9	c	c. Department Academic Calendar
10	11	12	13	14	15	16	8	g. Load Distribution, Staff Portfolio & Time table h. Commencement of Teaching: SEM-I (18/07/2022)
17	18	19	20	21	22	23	h-l-j-a	i. Induction program for Staff & students i. Identification of Slow learners & advanced learners
24	25	26	27	28	29	30	I-b-m	LBridge course for Slow Learners m. Base time test for Slow Learners
31					7.43			
			AUGUST	2022				b.Guest Lecture 3 (PVK)
SUN	MON	TUE /	WED	THU	FRI	SAT	10/2	d.15th August - Independence Day Celebration e. Student's Feedback – I, Feedback Analysis & Action Taken Report
1986	1	2	3	4	5	6		1. Academic Progress Report - I
7	8	9 Mohraup	10	11	12	13	b	g. Student's attendance, defaulter List-I & Action Taken Report h. Unit Test-I & Result Analysis
14	15 Aug	16-tarsi	17	18	19	20	d-e-f	a. Guest Lecture 4 (MUD)
21	22	23	24	25	26	27	g-h-a-k	k. Tech Nova Event (VK) i. Remedial Teaching & Retest
28	29	30	31 Gamusic				1-1	j. Department Advisory Board Meeting-1

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Principal
Ajeenkya DY Patil School of
Engineering, Lohegaon, Pune





			Septemb	per 2022						
SUN	MON	TUE	WED	THU	FRI	SAT		a.Guest Lecture 5 (ABW)		
			The same	1	2	3	AND A STATE OF THE PARTY NAMED	b. SPPU INSEM EXAM , Invigilation & CAP duty		
4	5	6	7	8	9	10	c-a	c. Parents Teacher's Meet-1 (PTM-1) d. Field Projects/Industrial Visits		
11	12	13	14	15	16	17	d	e. Academic Progress Report - II (APR-II)		
18	19	20	21	22	23	24	e-f-q	ff. Student's attendance & defaulter List-II		
25	26	27	28	29	30	Vances	9179	g. Guest Lecture 6 (AVN)		
	oğn sını		Octobe	r 2022						
SUN	MON	TUE	WED	THU	FRI	SAT	00000000			
				PERSONAL STREET	Park Barrer De Trans	1	S-SPARROWS	ii. Student's Feedback - II. Feedback Andreas		
2	3	4	5-Dasra	6	7	8	100	a. Student's Feedback – II. Feedback Analysis & Action Taken Report b.Unit Test -II (UT- II) & Result Analysis of UT-II		
9	10	11	12	13	14	15	a b	c Remedial Teaching & Retest		
16	17	18	. 19	20	21	22	c-d	d. Guest lecture ▼ (SDP)		
23	32 West	25	26-Bhanni	27	28	29	1	f. Diwali Holidays		
30	31				-					
			Novembe	r 2022	Will add		1	A CONTRACTOR OF THE CONTRACTOR		
UN	MON	TUE	WED	THU	FRI	SAT	THE LAND	-3-1 -3-1 -3-1 -3-1-1		
Salta:		1	2	3	4	5	a-b	a. Mock Examination (OR/PR), Course Exit Survey, Student Submission		
6	7	Gurumanak	9	10	11	12	10-lb	b.Conclusion of Teaching:SEM-I (5/11/2022) c. SPPU OR / PR EXAM		
13	14	15	16	17	18	19	d	d. SPPU ENDSEM EXAM - Invigilation, CAP duty & Vacation e.5 Days (30 hours) Faculty development Program/Workshop for Teaching		
20	21	22	23	24	25	26	e	from department of Civil Engineering		
27	28	29	30							
			December	2022			E E E 7555			
IN	MON	TUE	WED	THU	FRI	SAT	DE TENENT			
		100		1	2	3	扩展的代码的	d. 5 Days (30 hours) Faculty development		
4	5	6	7	8	9	10		d. 5 Days (30 hours) Faculty development Program/Workshop for Teachin from department of Computer Engineering		
1	12	13	14	15	16	17	d	c. meeting for planning of Industrial Vieite /Field Project		
8	19	20	21	22	23	WHITTH BUILDING ST	3	g. Load Distribution, Staff Portfolio & Time-Table for SEM-II		
5	26	27		-		PALONA SORRAPIZA				
			28	29	30	31	g	e		

Q-

Principal
Ajeenkya DY Patil School of
Engineering, Lohegaon, Pune





4			Januar	y 2023		A-1011		
SUN	MON	TUE	WED	THU	FRI	SAT	12420	
11	2	3	4	5	6	7	а	a. Commencement of Teaching: SEM-II (02/01/2023) b. Identification of Slow learners & Advanced learners
*8	9	10	11	12	13	14	b-c	c. Bridge course for Slow Learners
15	16	17	18	19	20	21	h	e. 26th January - Republic Day Celebration g.Department Advisory Board Meeting-II
22	23	24	25	26	27	28	e	h.Guest Lecture 1
29	30	31		1/05/2	100	2.7	g	
		44 200	Februar	y 2023				
SUN	MON	TUE	WED	THU	FRI	SAT	No.	
			1	2	3	4		b. Student's Feedback - I, Feedback Analysis & Action Taken Report
5	6	7	8	9	10	11	b-c	c. Academic Progress. Report - I d. Student's attendance, defaulter List-I & Action Taken Report
12	13	14	15	16	17	18	d-e-g	e. Unit Test -I & Result Analysis
19	20	21	22	23	24	25	f	f. Remedial Teaching & Retest g.Guest Lecture 2
26	27	28	66 876					
- Vill			March:	2023			THE STREET	The second secon
SUN 🦂	MON	TUE	WED	THU	FRI	SAT	107 E.	b. SPPU INSEM EXAM - Invigilation & CAP duty
			1	2	3	4		c. Parents Teacher's Meet- I (PTM-I)
5	6	7	8	9	10	11	b-h	d. Field Projects/Industrial Visits e. Academic Progress Report - II (APR-II)
12	13	14	15	16	17	18	c-d	f. Student's attendance, defaulter List-II & Action Taken Report
19	20	21	22	23	24	25	e-f	h.Guest Lecture 3 i.Guest Lecture 4
26	27	28	29	30	31	- 1	1	



Principal
Ajeenkya BY Patil School of
Engineering, Lohegaon, Pune





7,118	- 14 15 16	48	April 2	2023	No. 10		
SUN	MON	TUE	WED	THU	FRI	SAT	
	STATISTICS OF ST					1	а
2	3	4	5	6	7	8	b-j
9	10	11	12	13	14	15	c-d
16	17	18	19	20	21	22	e-f-g
23	24	25	26	27	28	29	h-i
30	10						

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SUN	MON	TUE	WED	THU	FRI	SAT	多强
The state of the s	1	2	3	4	5	6	2
7	8	9	10	11	12	13	b/
14	. 15	16	. 17	18	19	20	d
21	22	23	24	25	26	27	124
28	29	30	31				

a. Student's Feedback - II, Feedback Analysis & Action Taken Report

b. Unit Test-II & Result Analysis

i.Guest Lecture 5

c. Remedial Teaching & Retest

d.Tech Fest/Project Competition

e. Annual Social Gathering

f. Graduation Day Celebration [BE Students Send-off]

h.Mock OR/PR Exam & Student Submission

i. Conclusion of Teaching:SEM-II (29/04/2023)

a. SPPU OR / PR EXAM

b. SPPU ENDSEM EXAM - Invigilation .CAP duty & Vacation

d. Student Satisfaction Survey, Course & Programmme Exit Survey, Stakehoders Feedback, Analysis & Action Taken Report

Lt. Col. Sanjay Kardupati(Retd.)

HOD

Civil Engineering

Alaerkya DV Patil School of Engineering
Lohegaon, Pune

Principal
Ajeenkya DY Patil School of Engineering, Lohegaon, Pune

Pune

Dr. F. B. Sayyord Principal





"Empowerment through Quality Technical Education" AJEENKYA DY Patil School of Engineering

		DEPARTMENT OF COMPUT	anazara F	orm No:IQAC/05
		Subject Choice for AY: 202	22_23 Sem-II	
lote: E	very faculty must give	choice of minimum 1 Subject for all classes (C		
r. No. I	Name of the Faculty	Subject Taught- SEM-II	Choice of subject for SEM-II	Sign
1 1	Dr. PankajAgarkar	SE: TE:	SE: Software Engineering	Bo
	21. 1 dilkaj/rgalkai	BE: HPC . CC	TE: Augmented and Virtual Reality	13
		SE:	BE: Elective VI- SOFT Computing , EL-V NL	P
2 1	Dr. Sunil Rathod	TE: Artificial Intelligence	SE: Project Based Learning TE: Artificial Intelligence	4 () you
		BE: AI, ML	BE: Deep Learning, Natural Language Process	- Tour
		SE:	SE: Software Engineering	ing) W
3 1	rof. PallaviShimpi	TE:	TE: Cloud Computing	
		BE: soft Computing and optimization algorithm	BE:Soft Computing Fla. C.	Danas
		SE: Data Structure & Algorithm	SE: Data Structure & Algorithm	Touch
4 P	rof. JayashreeChaudl	TE: Data Science and Big Data Analytics"	TE:Data Science and Big Data Analytics"	1000
		BE:	BE: Deep Learning. Elective VI- Soft Computing	no l
100		SE:Data Structures and Algorithms	SE:Data Structures and Algorithms	9
5 P	rof.Amruta Chitari	TE:Data Science and Big Data Analytics	TE:Data Science and Big Data Analytics	(1)406)
		BE:	BE: Elective VI	119
		SE:Data Structures and Algorithms	SE:Data Structures and Algorithms	()
6 P	rof. Ashwini Pandaga	TE:Data Science and Big Data Analytics, Web	TE:Data Science and Big Data Analytics, Web	X9
		BE:	BE: Elective VI	
			SE: Software Engineering, Project based	0'1
7 P	rof. Ajita Mahapadi	SE: Software Engineering, Project based learni		onto.
	r tor. zvjita wranapaur	TE: Web Technology, E-II	TE: Web Technology	
		BE:	BE:Elective VI	1 /
		SE: Software Engineering, Project based learni	SE: Software Engineering, Project based learning	grahe
8 P	rof. Nilesh Pinjarkar	TE: Web Technology .	TE: Web Technology	108
		BE: Business Intelligence, Elective VI	BE: Business Intelligence, Elective VI	.\ = 1
		PE C A C	SE: Software Engineering ,Project Based	A.
9 10	of. Minal Toley	SE: Software Engineering	Learning ,Code of Conduct	
		TE:Data Science and Big Data Analytics,Web	TE: Data Science and Big Data Analytics. Web	100
		Technology BE:	Technology	1
+		Make the control of t	BE: Elective V Image processing	Wantle
10 Pi	of, Swarupa Kambal	SE:Data Structures and Algorithms	SE: DSA	Market
'	or, Swarupa Kambar		TE: BE:HPC	
	- 17 <u>- 1</u> 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2		SE: Software Engineering, Project based learnin	1 10.
II Pi	of. Bhagyshri Vyas	TE: Elective II (cloud computing) I n II Well	TE: Elective II (cloud computing), Web Technol	g / By
			BE:	ology / 1
			SE: Microprocessor	15
2 Pr	of. Sheetal More	TE:Data Science and Big Data Analytics. Web	11: Data Science and Big Data Analytics, Web	
Z PI	or, succtar More	Technology	Technology	15
			BE: Deep Learning	7 Accert
		SE: Data structure and algorithm	SE: Data structure and algorithm	4 用题人
3 Pr	of. Anita Mahajan	TE:Data Science and Big Data Analytics, Web	TE: Data science and big data analytics	112/400
		BE: Business Intelligence- Elective VI	BE: Business Intelligence, Elective VI	7/10/ 3
	amoral and a second	ing manes mengence elective vi	BE. Busiless interrigence, Elective VI	18
		SE: Principles of Programming Languages	SE: Principles of Programming Languages	1 28 11
4 10			TE: Data Science and Big Data Analytics, Web	() 0
4 Pr	of. Renuka Gavali		Technology .	TORONT
		25	-0/	6
		BE: NIL	BE: Elective VI- Business Intelligence	1/1/
		SE: Computer Graphics. Data structure and alg	SE: Data structure and algorithm	7
5 Pr	of. Priti Rathod		TE:Software modeling and	(DINNER
1 1	orei riu Kautou	TE:Software modeling and architecture, DBMS		rik Boy Pari
		SE:PPL(Principal of Programming Language)	SE: DSA(Data Structure & Algorithm)	pering Lole
6 Pro	of. Ishwar Bharambe		TE:Web Technology	7(18
			BE:Elective VI - Soft Computing	The Property of the Parket

		SE;	SE:
17	Prof. Manisha Wasnik	TE: Computer Networks	TE: Data Science and Big Data Analytics, Information Security-Elective II. BE: Image Processing EL-V, Natural Language
		BE: ML (MACHINE LEARNING)	Processing EL-V. Elective VI-Pattern
10		SE:PPL(PRINCIPAL OF PROGRAMING LA	SE DSA(DATA STRUCTURE & ALGORITHM)
19	Prof.Neha Rajas	TE:WT(WEB TECHNOLOGY)	TE DSBDA(DATA SCIENCE & BIG DATA ANALYTICS)
		BE: ML (MACHINE LEARNING)	BE HCI(HUMAN COMPUTER INTERFACE)
		SE: Software Engineering	SE: Software Engineering
20	Prof. Ujvala Patil	TE:-Web Technology,Software modeling and	TE: Elective-II Software Modelling and Architecture,
		BE:	BE: Elective VI- Soft Computing
		SE: Software Engineering	SE: Software Engineering
21	Section 1	TE: Software modeling and architecture	TE: Elective II - Software modeling and architecture, Information security
		BE:	BE: Elective V Image processing, Elective VI - Advanced Digital Signal processing
		SE:	SE:Principles of Programming Languages
22	Prof. Neha Sharma	TE: SPOS	TE:Web Technology, Data Science and Big Data Analytics
		BE:	BE: Image Processing
		SE: Software Engineering	SE: software Engineering, Data Structure & Algorithms
23	Prof. Swati B	TE: Information Security	Modelling and Architecture-Elective2, Cloud computing -Elective 2
		BE: Big Data Analysis	BE: Business Intelligence Elective VI, Elective VI- Soft Computing
24	Described to the state of	SE: Software Engineerin, Project Based Learni	SE: Software Engineerin, Project Based
24	Prot.Prajakta Jadhav	TE: Web Technology	TE: Data science and big data analytics, Web Technology BE: Deep Learning, Elective VI- Soft Computing

Prot. Amruta Chitari TimeTable In-



Computer Engineering Ajeenkya DY Patil School of Engineering Lohegaon, Pune





Principal
Ajeenkya DY Patil School of
Engineering, Lohegaon, Pune



"Empowerment through Quality Technical Education" AFEENKYA DY Patil School of Engineering

Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohegaon, Pune - 412 105 Form No:IQAC/06 Department of Computer Engineering DY Paril School of Englacering Sem II A. Y. 2022-23 Load distribution Practical/ Total Sign Theory Subject Class & DIV Tutorial Name of the Faculty No. HPC BE-A 14 3 Elective -VI BE-B Dr. Pankaj Agarkar 1 8 LP-V BE-A1, A2, A3, A4 avois 6 14 Elective -VI BE-A&B 8 Prof. Pallavi Shimpi 2 BE-A1,A2,A3.A4 LP-Vl Data Science & Big Data Analyti 4 TE-A 16 2 Honor Subject Prof.Amruta Chitari BE A& B 12 DSBDA Lab TE-A1,A2 3 Data StructureS & Algorithm SE-A 16 1 Code of Conduct Prof. Ashwini Pandagale SE-A 12 DSL SE-A1.A2,A3 Software Engineering 3 15 SE-A 12 Prof. Ajita Mahapadi 5 SE-A1, A2, A3 4 Data Science & Big Data Analytic 14 TE-B 8 Prof. Minal Toley 6 DSBDA Lab TE-B1.B2 8 Web Technology TE-A&B 16 2 Honor Subject Prof. Nilesh Pinjarkar 6 TE-B1,B2,B3 Data StructureS & Algorithm SE-B 16 1 Code of Conduct SE-B Prof. Sheetal More 12 SE-B1,B2,B3 8 3 Data StructureS & Algorithm SE-C 16 1 Code of Conduct SE-C Prof. Swarupa Kambale 12 DSL SE-C1.C2,C3 4 Web Technology TE-C 6 16 TE-C1,C2,C3 WIL Prof. Bhagyshri Vays 6 PBL SE-C1,C2 3 Deep Learning BE- A 13 10 Prof. Anita Mahajan BE-Laboratory Practice V A1.A2.A3.A4.B4 6 PPL SE-A&B 2 Honor Subject 16 BE A& B Prof. Renuka Gavali 7 PBL SE-B3 6 WII TE-A1.A2.A3 4 Elective -II TE-A 16 Prof. Priti Rathod 12 LP-II TE-A1, A2, A3 8 Artificial Intelligence 16 TE-B&C Prof. Ishwar Bharambe 8 TE-C1,C2 LP-II 3 HPC BE-B 14 3 Software Engineering SE-C Prof. Ujvala Patil 8 DSBDA Lab TE-B3,A3 Elective -II 4 16 TE-B 12 Prof.Gauri Rasane LP-II TE-B1,B2,B3 3 Software Engineering SE-B 3 PPL 16 SE-C Prof. Swati B 10 SE-B1,B2,B3 Data Science & Big Data Analytic TE-C 16 Prof. Neha Sharma 12 18 ds ACMSON TE-C1,C2,C3 Deep Kaarning BE-B 15 Aria Deni Panlligence Prof.Prajakta Jadhav TE-A Laborator Practice V BE-B1,B2,B3,B4 Elective M Ajeenkya DY Patil School of TE-C

199 A *

Engineering, Lohegaon, Pune

1		SE-C2,C3	PBL		- 6	16	Ricinal
20	Prof. Jayashri Waman	BE A & B	Honor Subject	2			300
		TE-C3 ·	LP-II		, 4	+	1.019
		BE-A&B	Elective -V	6	0	14	Bhala.
21	Prof. Suvidha Shah	BE-B1,B2,B3,B4	LP-VI		0		
22	Prof. Manisha Wasnik			ENTC		(Phen

Prof. Amruta Chitari TimeTable In-Charge Dr. Pankaj Agarkar HoD (Comp) Dr. Niranjan L Shegokar Dean Academics Dr. F. B.-Sayyad Principal Principal

Ajeonkya DY Park School o Engineering, Lahegaus, Pur

HOD

Corcouter Engineering Ajzenkya DY Patil School of Engineering Lohegaon, Pune



Principal
Ajeenkya DY Patil School of
Engineering, Lohegaon, Pune



Dr. D. Y. Patil Technical Campus
Dr. D. Y. Patil School of Engineering
Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohegaon, Pune – 412 105
Website: www.dypic.inContact No.:020-30619458

W.e.f.:8-3-2023

Revision:1

Master Time Table of Computer Department, AY 2022-23, Sem - II, w. e. f. 8-2-2023

Day	Year - Div.	10:00 -11:00	11:00-12:00	12:00-12:45	12:45 -01:45	01.45 -02.45	02:45 - 03.00	03:00 -04.00	04:00 -05.00	
	SE-A	EM-III(DG) [326]	DSA(AP) [326]		PPL(RG) [326]SE(AM) [326]	COC(AP) [326]	4	A1-DSL(AP)-[327] A2-ML(SP)-[342] A3-PBL(AM)-[338]		
	SE-B	DSA(SM) [328]	PPL(RG) [328]		B1-DSL(S B2-PBL(S B3-ML(M		EM-III(DG) [326]	SE(SB) [326]		
	SE-C	PPL(SB) [312] ₊	· DSA(SK) [312]		SE(UP) . [312]	EM-III TUT(DG) [312]	4.	C1-PBL(BV)-[310] C2-DSL(SK)-[309] C3-LIB		
ý	TE – A	EL-II(PR) [334]	DSBDA(AC) [334]		A1- DSBDAI A2-I A3-LP-II(I	LIB		AI(PJ) [334]	WT(NP) [334]	
Monday	TE B	B1-DSBDL * B2-WTL(i B3-LP-II(i	NP)-[331]		WT(NP) [328]	EL-II(GR) [328]		AJ(IB) [328]	DSBDA(MT) [328]	
	TE-C	C1-DSBDA C2-WTL(I	BV)-[321]	Long Break	. AI(IB) [312]	EL-II(JW) [312]	,	WT(BV) [312]	DSBDA(NS) [312]	
	BE – A	DL(PJ) [425]	EL-V(SS) [425]		EL-VI (PS) [425]	HPC(PA) [425]	Short Break	A1- LP-V(PJ)-[325] A2- LP-VI (PS)-[321] A3- LIB A4-LIB		
	BE-B	HPC(UP) [425]	EL-VI(PS) [425]		Schop2-NP-VI	3-LIB		Honor Subject(AC/RG/ JW)		
	SE-A	DSA(AP) [326]	EM-III(DG) [326]	2	Pune MP(SP) 8 [326]90	SE(AM) [326]		A2-DSL(AM)-[338] AP)-[327] SP)-[342]	
Tuesday	SE-B	MP(MS) . [328]	PPL(RG) [328] Ajeenl	Principal ya DY Patil Schoo ering, Lohegaon, F	of B1-ML(N B2-DSL(S B3-PBL(S	/AS)-[342] SM)-[327] SR)-[338]		COC(SM) [326]	EM-III TUT(DG) [326]	
	SE-C	SE(UP)	PPL(S B) Igine)	DSA(SK) [312]	EM-I: p G)		(1)	-LIB BV)-[310]	

		[534]			A3-WT	L(RG)-[331		[برنجا		
	TE-B	B2-DSBDI	((GR)-[333] L(MT)-[330] ((NP)-[331]		WT (NP) [328]	Al(IB) [328]	DSBDA(MT) [328]	EL-II(GR) [328]		
	TE-C	C2-DSBD	((IB)-[310] L(NS)-[309] (BV)-[321]		WT(BV) [334]	EL-II(JW) [334]	C1- . C2 - LP-II C3- DSBDI	(IB)-[310]		
	BE-A	EL-V(SS) [312]	EL-VI(PS) [312]		DL(PJ) [425]	HPC(PA) [425]	A1- A2-LP-V(A3-LP-VI	LIB (PJ)-[325] (PS)-[321]		
	BE – B	DL(AMM) [425]	EL-V(SS) [425]		B2-LP-V B3-LP-V	1-LIB (AMM)-[325] VI(SS)-[321] 4-LIB	Constitution Cons			
	SE-A	· MP(SP) [326]	PPL(RG)* [326]		SE(AM) [326]	• DSA(AP) [326]	A1- ML(S A2-PBL(A A3-DSL(A	M)-[338]		
	SE-B	EM-III(DG) [328]	SE(SB) [328]	•	B1 ₇ PBL(SB)-[338] B2-ML(MS)-[342] B3-DSL(SM)-[327]		MP(MS) [328]	DSA(SM) [328]		
	SE-C	C1- DSL(C2- C3-PBL(J						COC(SK) [312]	EM-III(DG) [312]	DSA(SK) [312]
Wednesday	TĖ-A	EL-II(PR) [334]	DSBDA(AC) [334]	•	· A2-LP-	L(RG)-[331] [I(PR)-[333] * AL(UP)-[330]	AJ(PJ) • [334]	WT(NP) [334]		
Wed	TE-B	B1- WTL(B2-LP-II(B3-DSBDL	GR)-[333]		EL-II(GR) [328]	WT(NP) [328]	B1-DSBDL(B2-U B3-LP-II(C	JB		
	TE-C	DSBDA(NS) [312]	AI(IB) [312]			L(NS)-[309] I(IB)-[310] B-LIB				
	BE – A	DL(PJ) [425]	HPC(PA) [425]	Principal	Pune S A	- LIB 2-LIB (/PJ)-[325] I(PS)-[321]	Honor Subject(AC/RG/ JW)			
	BE – B	B1-I B2-I B3-LP-V(A) B4-LP-VI(LIB Engined	ya DY Patil School ering, Lohegaon, Pu	VI	DL(AMM) [334]	EL-VI(PS) [326]	HPC(UP) [326]		

			1.4		A3-PBL	(AM)-[338]
	SE-B	B2-PBL(SM)-[327] SB)-[338] -LIB		DSA(SM) [326]	MP(MS) [326]
	SE-C	MP(MS) [328]	SE(UP) [328]	·	PPL(SB) [328]	EM-III(DG) [328]
7	TE - A	A1-DSBDA A2-WTL(A3- LP-H)			WT(NP) [334]	EL-II(PR) [334]
Concrete	TE-B	DSBDA(MT) [334]	AI(IB) [334]		B2-DSBD	I(GR)-[333] L(MT)-[330] LIB
	TE-C	AI(IB) - [312]	DSBDA(NS) [312] ·		EL-II(JW) [312]	WT(BV) [312]
	BE – A	EL-V(\$\$) [425]	EL-VI(PS) [425]		A2 A3	I(PS)-[321] -LIB -LIB '(PJ)-[325]
	BE-B	EL-VI(PS) [425]	DL(AMM) [425]		EL-V(SS) [425]	HPC(UP) [425]
	SE-A	A1-I A2-PBL(A A3-DSL(A	M)-[338]		PPL(RG) - [326]	EM-III-TUT(DG [326]
	SE-B	SE(SB) [328]	EM-III(DG) [328]		B2-	SB)-[338] -LIB SM)-[327]
Liluay	SE-C	C1-DSL(S C2-ML(M C3-LI	S)-[342]		AP(MS) Selii Schoo AP(MS) Selii Schoo MP(MS) Selii Schoo MP(MS)	COC(SK) [328]
	TE – A	AI(PJ) [334]	DSBDA(AC) [334]	Principal	# Bulles Al-	L LIB (PR)-[333] L(UP)-[330]
	TE-B	B1-L B2- LP-II(C B3- DSBDL(3R)-13331 Ajeeni	rya DY Patil School o ering, Lohegaon, Pur	f Al(IB)	DSBDA(MT) [334]

[326]										
	-LIB									
B3- PBL	(SM)-[327] (RG)-[338]									
TOR THE RESIDENCE SHIP OF THE PARTY OF	(BV)-[310]									
	(SK)-[309]									
C3-ML(MS)-[342] A1- LP-II(PR)-[333] A2- DSBDAL(AC)-[330] A3- LIB										
									A3-	LIB
									WT(NP)	EL-II(GR)
[328]	[328]									
CI-WTL((BV)-[321]									
C2-DSBDI	L(NS)-[309]									
С3-СР-П(JW)-[310]									
	(SS)-[3221]									
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	aviivi)-[323]									
A1-PBL(/	AM)-[338]									
. A2-DSL(AP)-[327]									
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PPL(RG)	COC(SM)									
[328]	[328]									
1	[320]									
C1- ML(I	MS)[342]									
C2- PBL(J										
C3- DSL(S										
	Aug Epiger III									

	ן נייץ	[512]		C3-DSBDL(NS)-[309]
BE-A	Project Work	Project W	ork	Project Work
BE-B	Project Work	Project W	ork	Project Work

Year	Notation	Name of The Subject	Notation	Name of The Faculty	Notation	Name of The Faculty	Lab Loc atio
	EM-III	Engineering Mathematics(A & B & C)	DG	Prof. Dilip Ghule			
	DSA	Data Structure & Algorithm (A &B)	AP	Prof. Ashwini Pandagale	SM	Prof. Shital More	
	DSA	Data Structure & Algorithm (C)	SK	Prof. Swaroopa Kambale		1101. Suitai Wore	
	SE	Software Engineering(A&B)	AM	Prof. Ajita M	SB	Prof. Swati Bagade	
	SE	Software Engineering(C)	UP	Prof. Ujvala Patil		1101. Strike Bugudo	
	MP	Microprocessor(A)	SP	Prof. Sheetal Patil			
	MP	Microprocessor(B & C)	MS	Prof. Monali Shirsath			
	PPL	Principals of Programming Language(A & B)	RG .	Prof. Renuka Gavli	· · · · · · · · · · · · · · · · · · ·		
SE	PPL	Principals of Programming Language(C)	SB	Prof. Swati Bagade			
Computer	DSAL	Data Structures & Algoritham Laboratory(A&B)	AP	Prof. Ashwini Pandagale	SM	Prof. Shital More	327
	DSAL	Data Structures & Algoritham Laboratory(C)	SK	Prof. Swaroopa Kambale		3.11.1.20.001.00.00	100.
	ML	Microprocessor Laboratory(A)	SP	Prof. Sheetal Patil			342
	ML	Microprocessor Laboratory(B & C)	MS	Prof. Monali Shirsath			
	PBL	Project Based Learning II (A)	AM	Prof. Ajita M	SB RG	Prof. Swati Bagade	338
	PBL	Project Based Learning II (C)	BV	Prof.B.Vyas	JW	Prof. Renuka Gavli(B3)	
	COC	Code of Conduction(A&B)	AP	Prof. Ashwini Pandagale	SM	Prof. Jayashri Waman Prof. Shital More	
	COC	Cade of Conduction(C)	SK	Prof. Swaroopa Kambale	SIVI	Prof. Sintal More	
	DS &BDA .	Data Science and Big Data Analytics(A&B)	AC	Prof. Amruta Chifari	MT	Prof. Minal Toley.	
	DS &BDA	Data Science and Big Data Analytics (C)	NS	Prof. Neha Sharma			
	WT	Web Technology (A&B)	NP .	Prof. Nilesh Pinjarkar			-
	WT	Web Technology (C)	BV	Prof.B.Vyas			
	AI	Artificial Intelligence(A&B)	PJ Tosii	Prof. Prajakata Jadhav	IB	Prof. Isliwar Bharambe	
ГЕ	Al	Artificial Intelligence(C)	IB SLOP	Prof Jahwar Bharambe			
Computer	EL-II	Elective II(A&B)	PR A	97.70	GR	Prof. Gauri Rasane	-
	EL-II	Elective II(C)	JW S	Prof. Jayashri Waman			
	DS& BDL	Data Science and Big Data Analytics Laboratin Cip.	al ^{AC}	Prof. Amruta Chitari	MT	Prof. Minal Toley.	
		(A&B) Ajeenkva BY Pati	School of	Prof. Ujvala Patil	UP	Prof. Ujvala Patil	330
	DS& BDL	(A&B) Ajeenkya DY Pati Data Science and Big Data Analytics Laborator, Lohe (C)	gaon, Pune	Prof. Neha Sharma			310
	WTL	Web Technology Laboratory(A&I)	RG	Prof. Renuka Gavlie	NP	Prof. Nilesh Pinjarkar	331

	LP-II	Laboratory Practice II (A & B	PR	Prof. Priti Ra	GR	Drof Caust P	+
	7		JW	Prof. Jayashri Waman		Prof.Gauri Rasane	
	LP-II	Laboratory Practice II (C)	IB	1101. Jayashri Waman	JW	Prof. Jayashri Waman	333
	HPC	High Performance Computing		Prof. Ishwar Bharambe	JW	Prof. Jayashri Waman	309
	DL .	Deep Learning	PA PJ	Dr. Pankaj Agarkar	· UP	Prof. Ujvala Patil	- 305
	EL-V	Elective V		Prof. Prajakata Jadhav	AMM	Prof. Anita Mahajan	
T.	EL-VI	Elective VI	SS	Prof. Suvidha Shah			
BE Computer	LP- V	Laboratory Practice V	PS	Prof.Pallavi Shimpi			
- surparter	LP-VI		PJ	Prof. Prajakata Jadhav	AMM	Prof. Anita Mahajan	1201
	PW	Laboratory Practice VI Project Work	PS	Prof.Pallavi Shimpi	SS	New Faculty	321
	PW	Project ,Work			-	-	325
			-			•	

Year - Div.	Class Teacher	Batch	Mentor	Batch					
SE – A	Prof. Sheetal More	41		Daten	Mentor	Batch	Mentor	Batch	
SE – B		Al	Prof. Pallavi Shimpi	A2	Prof. Sheetal More	12	Prof. Manisha Wasnik		•
	Prof. Ishwar Bharambe	B1	Prof. Ishwar Bharambe		Prof. Suvidha Shah	A3			
SB-C .	Prof. Swarupa Kamble	C1		B2		В3	Prof.Jayashree Waman		
ΓE – A	Prof. Renuka Gavli		Prof. Swarupa Kamble	C2	Prof. Priti Rathod	- C3	Prof. Monali Shirsath		
TĒ -B		AI	Prof. NileshPinjarkar	A2	Prof. Renuka Gavli			+	•
10 - B	Prof. Gauri Rasane	B1	Prof. Ashwini Pandagale			A3	Prof. Minal Toley		
TE-C	Prof.B.Vyas	CI		B2	Prof. Gauri Rasane	В3	Prof. Suvidha Shah	-	
BE - A	Prof. Suvidha Shah		Prof. Neha Sharma	C2	Prof. Swati Bagade	C3	Prof.B. Vyas		
	1 Tot. Suvidita Shan	A1	Prof. Prajakta Jadhav	A2	Prof. Amruta-Chitari			-	
E-B	Prof. Ujvala Patil	BI	Prof. Ujvala Patil			A3	Prof. Anita Mahajan		
				B2	Prof. Priti Rathod	В3	Prof. Ajita Mahapadi		

Prof.Amruta Chitari Time Table In-Charge

Principal
Ajeenkya DY Patil School of
Engineering, Lohegaon, Pune

Dr.Pankaj Agarkar HoD Computer





Dr. E. B. Sayyad Principal Principal

Ajeenkya DY Path School of Engineering, Lollogcan, Pune





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AJEENKYA DY PATIL SCHOOL OF ENGINEERING

Dr. D. Y. Patil Knowledge City, Charholi (Bk.), Via. Lohgaon, Pune - 412 105

Department of Mechanical Engineering

ACADEMIC PROGRESS REPORT(Theory & Praticals) AY 2022-23 Term II

26/04/2023

Form No.: IQAC/27

						Lectures cticals	Lec	o. of tures / ticals	Sy		mpleted a 4/2023)	s on		
Sr. No.	Class	Div.	Div. Subject	Name of Faculty	Planned till date (26/04/2023)		The state of the s		Units / No Practicals		Percentage (%)		Remarks	Sign
					Th	Pr.	Th	Pr	Th	Pr	. Th	Pr.		
			Engineering \ Mathematics - II	PRM	25	-	22	-	3	_	50%.	-	Insern & neint	PEM
			Kinematics of Machinery	PGK	25	06	22	65	3	05	5%	5%	Gothering Gothering	Post .
1	SE	A	Applied Thermodynamics	TH - RNG PR - DDS	31	8	24	06	2.8	04		40	Insem Exam I conthering	ALL S
	22		Fluid Mechanics	JCN	39		24		03	06	56%	70%	Insem exam	a
			Manufacturing Processes	DSM	26	~	22	_	2.7	J	50%	1		Bunds
			Machine Shop	ANP/GIA		08		-		2.0	-	62.5	1. 4	B 8

Academic Coordinator Ajeenkya DY Patil School Hop Engineering, Lohegaon, Pune



Dr. F. B. Sayyad

Principal







AICTE ID - 1-3847411 AISHE Code: C-46648

DTE Code: EN6732

SPPU PUN Code: CEGP015720

(Approved by AICTE, Recognized by Govt. of Maharashtra, Affiliated to Savitribai Phule Pune University)

(Accredited by NAAC, NABL & ISO 9001:2015 & 21001:2018 Certified Institute)

<u>Industry Visit Report – 2022-23</u>

Name of the Event: Industrial visit

Date: 11th Oct, 2022 to 15th Oct, 2022

Students: Students from TE Computer Engineering Department

Location: Pune - Bangalore - Mysore - Pune

Organized By: Ajeenkya DY Patil School of Engineering, Computer Engineering

Department Pune.

Tour Organized By: Eduventura Industrial Visit Organizer

No of Students: 59

Staff: 2

Name of Staff:

Prof. Poonam Nagale Prof. Yogesh Pawar

Tour Coordinators: 2

Introduction

The Ajeenkya D.Y Patil University Organized Industrial visit.

Aim for organizing this Industrial Visit is to enhance studentpractical knowledge and Improve student's practical skills. This Year we Organized Industrial visit to- Pune-Bangalore-Mysore-Pune for AI&DS Engineeringstudents of 3rd year. Before the trip, Our Coordinator Mr.Rafiq Shaikh & Sandip Gaikwad conducted meeting of all trip members. He informed students of the rules and regulations regarding the trip. He gave a consent letter to the students and detailed schedule of Industry Visit. He asked to carry required things, medicine etc.





Details of Journey

DAY 1 [13Th October 2022]: PUNE TO BANGALORE

We Assembled at CSMT Railway station at 10:30am in Morning to boardTrain 11301 Udyan Express and started the journey towards Bangalore.

DAY 2 [14th October 2022]: BANGALORE

We reached Bangalore at Early morning 07:40am. Due to Delay in Train schedule we Freshen up at station itself and proceed for ISRO Industrial visit, With Bus 1(49 seaters & Tempo Traveller 2 (12 seaters).

ISRO Visit Details: Received warm welcome by ISRO officials and we started a Session In ISRO.

- We got to see an Audio & Video Presentation about ISRO.
- Demo Satellite and Other Equipment which is useful for spaceresearch program demo, we saw and were explained by ISRO officials.
- · Actual satellite working floor also shown from Gallery view
- The concerned person conducted a Question and Answer session for all students where Students had cleared their doubtswith the ISRO scientist
- During the industrial visit all students followed all Rules and regulations followed by ISRO officers.

Post Session with ISRO Industrial visit we moved to Explore Bangalore Culture and we went Back to the hotel for Lunch, after lunch proceeded tograb destination knowledge we explored Cubbon Park, Vishveshwarya Technical Museum, Evening at ISKCON temple. Safely we went back to Hotel for Dinner & overnight stay at hotel. (Accomodation in CRN Canary sapphire 3-Star Hotel). Dinner and overnight stay at the hotel.

DAY 3 [15th October 2022]: MYSORE "WELCOME TO THE CULTURAL

CAPITAL OF KARNATAKA" Morning breakfast and check out from the hotel. We proceed to Mysore on the way visit Srirangapatna, Tipu Sultan summer palace we explore and proceed to Lunch in The Quorum Hotel (4 star Hotel) Taken Rest in Hotel and continue with Sighteen of Mysore Mysore Palace, one of the major attractions of Mysore, Our coordinator's Guide us to entire Mysore Palace. Later we moved to the Brindavan Garden to enjoy the sound- light - Musical fountain show. Evening we backto The Quorum Hotel for Dinner and Overnight stay at Mysore (Meals:B,L,D)

School of Charles and Amagin A

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DAY 4 [16th October 2022]: Morning we had delicious Breakfast and check out from the Hotel and Move to Chamundi devi Temple to Take a Darshan of Chamundi Devi and experience Of south Indian Temple architecture, Late we Go to Nandi Bull Temple 331 Steps from Chamundi devi Temple Down side direction of Chamundi Hill, Both group taken a darshan of Nandi bull temple and proceed at Mourya Residency Hotel where a Rooftop tasty food was waiting for all of us. Post Lunch students Enjoy 20-30 Minutes for shopping at Kaveri shop for Sandalwood Items and Mysore Silk items, 03:15pm we all Boarded our Buses and start Journey towards KSR bangalore Junction to Board our return journey train 11302 Udyan Express, Our Coordinators Inform about train seating arrangements and taken care of everyone received their Luggage and ensure while boarding in train all students was very well coordinated with Tour coordinators of EDUVENTURA and the instructions and timing which they informed students followed it.

During this Industrial Visit students are learn so many things Punctuality Team work Gain Destination Knowledge Explore Cultural Values of Historical Monuments

Industry Exposure of ISRO & RED HAT.

DAY 5 [17th October 2022]: All students were sharing the information with Friends and social media about their visit experience and enjoyed train journey and on time Reach Pune with Industrial Practical world Knowledge.

This Industrial visit at ISRO and RedHat Company is very helpful in our future practical Life & bring a positive change in our thinking & practical behavior regarding Education & specializing our technical skills. Other historical places like Tipu Sultan summer palace at srirangpattana, Mysore palace at Mysore, Chamundi devi temple and Nandi bull temple visit was very peaceful, beautiful and enjoyable for all students. Other places and sightseeing was very joyful. Visit management was good by organizer. This visit was very joyful and useful for all students.

Δ

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Engineering, Lohegaon, Pune

Photos:













Prof. Poonam Nagale Event Coordinator

Patrio Paris Constitution of the Constitution

Prof. Bhagyashree Dhakulkar Head of AI&DS Department

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Department of Civil Engineering and Industry Institute Interaction Cell (IIIC)

Ref no:

Date: 20/01/2023

INVITATION LETTER

To,
Dr. Rajesh Katdare
(Assistant professor Civil Engineering Department,
ADYPSOE, Lohegaon, Pune)

Subject: Bridge Course on Process of E-Tendering

Respected Sir,

1. This is to introduce ourselves i.e., Dr DY Patil School of Engineering Lohegaon, Dist.-Pune established in year 2010-11 affiliated to Savitribai Phule Pune University. This college runs degree courses in Civil, Mechanical, Electronics & Tele-Communication, Computer and Artificial Intelligence it also runs 3 PG Courses approved by AICTE, DTE & Savitribai Phule Pune University. From the beginning, we have been continuously delivering very effectual knowledge to our students and we expect very good returns from them for the progress of the country.

- 2. It is our honour to take valuable guidance from the Professional experts like you. It will add to the knowledge of our faculties.
- 3. Being a Subject matter expert in the field, may I request you to present your views on "Process of E- Tendering". We request you to deliver your session on 23th Jan 2023 at 11:00am to 1.00pm, including 10 minutes of Questions and answer Session. Looking forward to encouraging response from you.

Meeting Linkhttps://teams.microsoft.com/l/meetup-

join/19%3amceting_YzEwZWJhYjgtNDgzNC00ZGExLThmMjktYjA1OGMxODQzZmU2%40thread.v2/0 2context=%7b%22Tid%22%3a%2213817675-036e-4ca3-ba1c-

d69032c5d0a0%22%2c%22Oid%22%3a%2299b5d536-0333-48b7-98f2-d4713cbcf81c%22%7d

Thanking You.

Yours sincerely,
Prof. Lt. Col. Sanjay Karodpati (Retd.)
(Head of Civil Dept. DY Patil School of
Engineering, Lonegaon, Pune)
Department of Civil Engineering

Dr. D. Y. Patil School of Engg. Lohegaon

Vision Strive for technical excellence in civil engineering

Mission- To impart practical based learning to meet the needs of the industry
To inculate social and ethical values among the students
To establish a center of excellence by imparting soft technology skills in civil engineering



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Department of Civil Engineering and Industry Institute Interaction Cell (IIIC)

Ref no:

Date: 20/01/2023

INVITATION LETTER

To. RAMAKANT DAGADU KOSHTI

(Founder of Constroplanet Civil Engineering Services)

Subject: Bridge Course on Process of E-Tendering

Respected Sir,

- 1. This is to introduce ourselves i.e., Dr DY Patil School of Engineering Lohegaon, Dist.-Pune established in year 2010-11 affiliated to Savitribai Phule Pune University. This college runs degree courses in Civil, Mechanical, Electronics & Tele-Communication, Computer and Artificial Intelligence it also runs 3 PG Courses approved by AICTE, DTE & Savitribai Phule Pune University. From the beginning, we have been continuously delivering very effectual knowledge to our students and we expect very good returns from them for the progress of the country.
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Meeting

Linkhttps://teams.microsoft.com/l/meetup-

join/19%3ameeting YzEwZWJhYjgtNDgzNC00ZGExLThmMjktYjA1OGMxODQzZmU2%40thread.v2/0 ?context=%7b%22Tid%22%3a%2213817675-036e-4ca3-ba1c-

d69032e5d0a0%22%2e%22Oid%22%3a%2209b5d536-0333-48b7-98f2-d4713cbcf81c%22%7d

Thanking You.



Yours sincerely.

Prof. Lt. Col. Sanjay Karodpati (Retd.) (Head of Civil Dept. DY Patil School of

Engineering, Lohegaph Pune)

Department of Civil Engineering Dr. D. Y. Patil School of Engg. Lohegaon

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Department of Civil Engineering and Industry Institute Interaction Cell (IIIC)

Date: 27th January 2023

Event Report

Academic Year: 2022-2023

Name of the event: Bridge course on Process of E-Tendering

Date and Time	Date:23 th Jan 2023 to 25 th Jan 2023			
Event Venue	Online mode			
Organized by	Department of Civil Engineering Industry and Institute Interaction Cell (IIIC)			
Targeted Audience	TE And BE CIVIL Engineering Students			
Resource Person of visit	Dr. Rajesh Katdare Mr. Ramakant Dagadu Koshti			

Event Contents:

To bridge course has provide gap between subjects studied at university level and subjects they would be studying in engineering syllabus.

Objectives:

- Bridge courses are aimed at remolding the students' approach to subjects like technical felid, they
 are mostly taught by experienced teachers.
- Students had got details of E-Tendering, Contracts.



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Details of Event:

The department of Civil Engineering of Dr. D. Y. Patil School of Engineering,
Lohegaon, Pune, had organized an educational course, on dated 23th Jan 2023 to 25th Jan
2023 as a part of curriculum of the subject, "Bridge course on Process of E-Tendering" for
T.E and B.E Civil engineering students. The Bridge course was organized with the prior
permission and guidance of Hon. Principal Dr. F.B Sayyad and HOD of Civil Engineering
Department Lt. Col. Sanjay Karodpati. 60 students of T.E. (Civil) along with faculty
members Prof.Sarika kokate, Prof. Sarika Thombre, attended the Educational Visit which
started at 11:00 am and concluded at 1 pm.



Photo 1: Sir was giving information about Central Public building to the students.



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Engineering, Lohegaon, Pune

Central Public building:

"Public Buildings" used in this manual applies only to buildings borne on the books of the Works Departments and maintained from funds provided in their budget.

Section 1.- Definition and Classification of Public Buildings

Section 2.- Additions and Alterations to Public Buildings

Section 3.- Capital Cost of Buildings

Section 4- Occupation of Public Buildings by Private persons, etc.

Section 5—Residences for Government Officials

Section 6 & 7-Sale and Dismantlement of Public Buildings Permanent Building

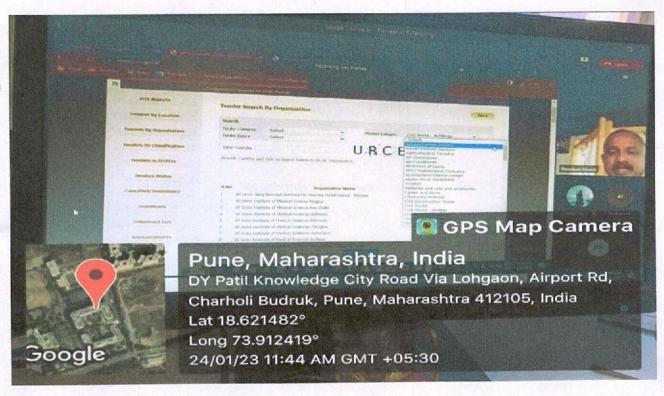


Photo 2: Mr Koshti explaining tender search by organization.

Tender search by Organization

Mahatenders - Search business opportunities from largest Tender Database for Maharashtra Government tender s, e tender Maharashtra, Online Tender Notices published on mahatenders. This site provides you with the latest news about maharashtra e tenders, maha tenders. You can also get latest information regarding eprocurement maharashtra, maharashtra tenders by organization, pmgsy tenders maharashtra and category wise tenders from Maharashtra. To date, 4180992 tenders are published by various Government departments of Maharashtra, Corporations, State PSUs & Private Companies from Maharashtra State. 4180992 live tenders are available on mahatenders gov for various products, services, and Works.

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Photo 3: Sir was explaining Digital Signature Certificate's (DSC)

Digital Signature Certificate's (DSC)

Digital Signature Certificates (DSC) are the digital equivalent (that is electronic format) of physical or paper certificates. Few Examples of physical certificates are drivers' licenses, passports or membership cards. Certificates serve as proof of identity of an individual for a certain purpose; for example, a driver's license identifies someone who can legally drive in a particular country. Likewise, a digital certificate can be presented electronically to prove one's identity, to access information or services on the Internet or to sign certain documents digitally.

The different types of Digital Signature Certificates are:

Class 2: Here, the identity of a person is verified against a trusted, pre-verified database.

Class 3: This is the highest level where the person needs to present himself or herself in front of a Registration Authority (RA) and prove his/her identity.



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Benefits of E- Tendering

The term E-Tendering can be a confusing concept if the term isn't fully explained to a buyer who's just in the process of taking their procurement processes online. Generally speaking, e-Tendering refers to those processes, as it is an electronic (digital) way for the buyer to advertise their needs, connect with a supplier, and ultimately draw up a contract. There are different types of e-Tendering portals where buyers can connect with suppliers, and from there, contracts can be finalized. Procurement software solutions such as e-Tendering can streamline your procurement process overall, making this one of the many benefits of e-Tendering—however, there are many others. Below, let's take a look at the top five benefits of e-Tendering.

- 1. An Increase in Efficiency with e-Tendering
- 2. A Higher Quality Vendor Pool with e-Tendering
- 3. Increased ROI with e-Tendering
- 4. Transparency and Spend Visibility with e-Tendering

5. e-Tendering is Easy to Use

Prof. Sarika Thombare Prof. Sarika Kokate Skokate

Event Coordinators

Lt. Col. S M Karodpati HOD

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Ajeenkya BY Patil School of
Engineering, Lohegaon, Pune



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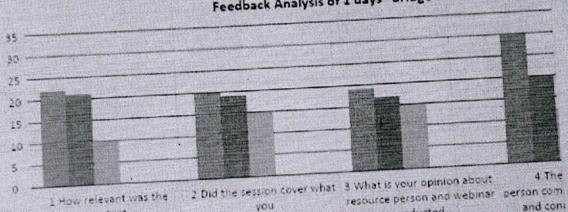
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Feedback Analysis of 1 days "Brid

		Feedback Analysis of 1 days "Bridge Course on E-Te					
Sr.No.	Point (Number of Forms Evaluated=(41)	1.How relevant was the content discussed by resource person	2.Did the session cover what you were expecting	3. What is your opinion about resource person and webinar conducted.			
			20	19			
1	Strongly agree	22	19	17			
1	Agree	21		15			
	Neutral	10	15	0			
3		0	0				
4	Disagree	0	0	0			
5	Strongly disagree						

Feedback Analysis of 1 days "Bridge Course on E-Tende

conducted



were expecting

discussed by resource person Disagree-2 Strongly agree #2 Agreeutral 3 Neutral #4 Strongly disagree -1

Event Co-ordinator

content.

HOD.



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Department of Civil Engineering Dr. D. Y. Patil School of Engg. Lohegaon





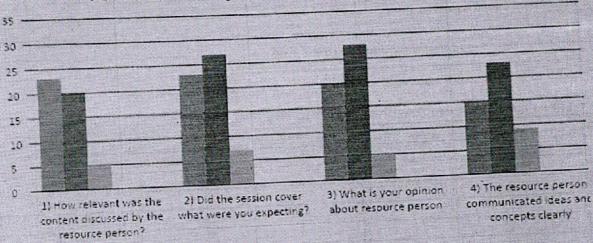
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Accredited by NAAC,NABL & ISO 9001:2015&21001:2018 Certified Institute Department of Civil Engi Feedback Analysis of 2 days "Bridge Course on E-Tendering"							
Sr.No.	Point (Number of Forms Evaluated=55)	1) How relevant was the content discussed by the resource person?	2) Did the session cover what were you expecting?	3) What is your opinion about resource person	The resource person communicated ideas and concepts clearly		
	Strongly agree	23	23	20	15		
	NO DESCRIPTION OF THE PARTY OF		27	28	23		
2	Agree	20	2		q		
3	Neutral	5 5	1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -	5			
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	Strongly disagree	0	0	0	1. 0		

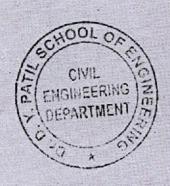
Feedback Analysis of 2 days Bridge Course on "E-Tenc



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Event Co-ordinator

Hod



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Principal Alcenkya DY Patil School of Engineering, Lohegaon, Pune





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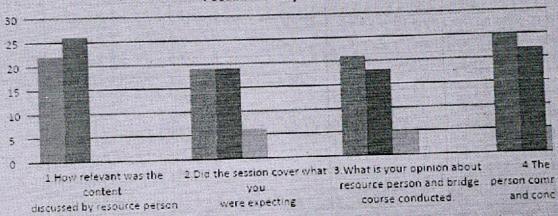
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Feedback Analysis of 3 days "Bridge Co

Sr.No.	Point (Number of Forms Evaluated=(41)	1. How relevant was the content discussed by resource person	2.Did the session cover what you were expecting	3.What is your opinion about resource person and bridge course conducted.		
1	Strongly agree	22	19	21		
2	Agree	26	19	18		
3	Neutral	0	6	5		
4	Disagree	0	0	0		
5	Strongly disagree	U	0	0		

Feedback Analysis of 3 days "Bridge Course on E-Tenderi



1 2 Ad Nautral 3 Neutral 1 Auestree Disa eg-Strongly agree Strongly disagree -1

Event Co-ordinator

Department of Civil Engineering Dr. D. Y. Patil School of Engg. Lohegaon

Principal Counkya BY Patil School of Engineering, Lohegaon, Pune



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8 Aboli Ravikar			۵	gree	A	gree
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10 Ashwini Salunkhe		Technology, Pimpri		Agree	S	Strongly
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12 Ajay Giridhar Chaudhari		Dr D Y Patil School of Engineerin	ıg	Agre	9	Agree
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17 Kshitija Tikhe		Dr.D Y Patil School of Engineerin Lohagaon Pune				Strong
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Principal
Aleenkya DY Patil School of
Engineering, Lohegaon, Pune



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5 Prof. Sunil Deshpande	Dr D.Y Patil Institute of Technology Sunil Deshpande Pimpri		e 1	Neutral	
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> Department of Civil Engineering and Industry Institute Interaction Cell (IIIC)

> > Date: 20/01/2023

To, Dr. Rajesh Katdare (Assistant professor Civil Engineering Department, ADYPSOE, Lohegaon, Pune)

Subject: -Expression of Gratitude

Respected Sir,

- 1. On behalf of Dr. D.Y. Patil School of Engineering, Lohegaon Pune, We are thankful to you for delivering the expert speech on subject "Bridge Course on Process of E-Tendering" on 23th Jan 2023.
- 2. We believe that, the knowledge you shared with the participants will help immensely in their personal and academic development. Your presentation was excellent and enjoyed by everyone. We appreciate you for sharing your time, talent and expertise with us.
 - 3. We look forward many more such association in future.

Sincerely,

Prof. Lt. Col. Sanjay Karodpati (Retd.) Mob No - 7709708941 (Head of Civil Dept. DY Patil School of Engineering,

Lohegaon, Pune)

H.O.D.

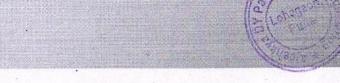
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Department of Civil Engineering and Industry Institute Interaction Cell (IIIC)

Date: 20/01/2023

To,
Mr. Ramakant Dagadu Koshti
(Founder of Constroplanet Civil Engineering Services)

Subject: -Expression of Gratitude

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(Head of Civil Dept. DY Patil School of Engineering,
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Department of Civil Engineering

Dr. D. Y. Patil School of Engg. Lohegaon

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Website: www.dypic.in Contact No.:020-6707 7926

RESULT ANALYSIS

SPPU Examination: NOV-2022

Semester: III

Department: CIVIL

Class: SE

Date of declaration of result: 28/03/2023

A. Overall Result Analysis

Sr. No.	Details	No. of Students	
1	No. of students appeared	71	
2	No. of students passed (All clear)	01	
3	No. of students failed (ATKT)	70	
4	All clear passing Percentage	1.41	
5	All clear passing Percentage (University)		
6	Percentage of passing with ATKT		
7	Percentage of passing with ATKT (University)	_	
8	No. of students passed with Distinction	00	
9	No. of students passed with First Class	00	
10	No. of students passed with Higher Second Class	00	of Engineer
11	No. of students passed with Second Class	01	3 4 1
12	No. of students passed with Pass Class	00	
13	No. of students failed in 1 Th. Sub	02	Puanty * Sills
14	No. of students failed in 2 Th. Subs	06	
15	No. of students failed in 3 Th. Subs	22	
16	No. of students failed in more than 3 Th. Subs	40	18/
17	No. of students failed in 1 Pr/Or	13	
18	No. of students failed in 2 Pr/Or	02	Principal
19	No. of students failed in 3 Pr/Or	00 Ajeenk	ya DY Patil School of ling, Loheguon, Pune



B. Subject Wise Result Analysis:

Sr. No.	Name of the Subject	TH/Pr	Name of the Staff Member	No. of Students Appeared	No. of Students Passed	% of Passing
1	ВТАР	TH	Prof. Shreedhar D. Patil	71	29	40.84
2	MOS	TH	Prof. A. V. Nemade	71	13	18.30
3	FM	TH	Prof. U. A. Shaikh	71	2	2.8
. 4	MIII	TH	Prof. Rahul Mali	71	9 ·	12.67
5	EG	TH	Prof. Gayatri C Sherkar	71	41	57.74

C. Subject Wise Scoring Pattern:

			No. of	No	he ra	ie range				
Sr. No.	Subject	Passing students	students appeared	66 to 100	60 to 65	55 to 59	50 to 54	41 to 49	40	Less than 40 (Fail)
1	BTAP	40.84	71	l	2	4	5	12	5	42
2	. MOS	18.30	71	0.	0	0	2	7	4	58
3	FM	2.8	71	0	0	0	0	1	,1	69
4	MIII	12.67	71	0	0	0	0	7	2	62
5	EG	57.74	71	. 0	3	1	4	22	11	30

Toppers:

Rank	Name of students	Total Marks out of 700	Percentage/SGPA
1.	VISHWAJEET SUDHIR CHATAKE	371	5.73

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Aleenkya DY Patil School of
Engineering, Lohegaon, Pune



Dr. D. Y. Patil Technical Campus

Dr. D. Y. Patil School of Engineering

Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohegaon, Pune – 412 105
Website: www.dypic.inContact No.:020-6707 7926
Department of Civil Engineering

RESULT ANALYSIS

SPPU Examination: NOV-2022

Semester: IV

Department: CIVIL

Class: TE

Date of declaration of result: 24/03/202

A. Overall Result Analysis

Sr. No.	Details	No. of Students
ı	No. of students appeared	73
2	No. of students passed(All clear)	19
3	No. of students failed (With ATKT)	54
4	All clear passing Percentage	26.02
5	All clear passing Percentage(University)	
6	Percentage of passing with ATKT	1
7	Percentage of passing with ATKT(University)	
8	No. of students passed with Distinction	05
9	No. of students passed with First Class	07
10	No. of students passed with Higher Second Class	05
11	No. of students passed with Second Class	02 gail School
12	No. of students passed with Pass Class	00 S Civil
13	No. of students failed in 1 Th. Sub	09 Engineering
14	No. of students failed in 2 Th. Subs	14 Cohegaon States
15	No. of students failed in 3 Th. Subs	12
16	No. of students failed in more than 3 Th. Subs	19
17	No. of students failed in 1 Pr/Or	07
8	No. of students failed in 2 Pr/Or	Principal Ajeenkya Dy Patil School
9	No. of students failed in 3 Pr/Or	00 Engineering, Lohegaon, Pu

B. Subject Wise Result Analysis:

Sr.No.	Name of the Subject	TH/Pr	Name of the Staff Member	No. of Students Appeared	No. of Students Passed	% of Passing
1	HWRE	TH	Prof. Uzma A. Shaikh	73	38 .	52.05
2	WSE	TH	Prof. Swapnil P. Bijwe	73	34	46.57
3	DSS	TH	Prof. Jitendra D. Dalvi	73	28	38.35
4	EEFM	TH	Prof. Gauray C. Vispute	73	54	73.10
5	CM	TH	Lt. Col. Sanjay M. Karodpati	62	46	74.2
6	ACT	TH	Prof. Uday A. Kakde	11	- 11	100
0,	Urban Housing and Infrastructure	TH	Dr. Milind M. Darade	8	7	87.5

C. Subject Wise Scoring Pattern:

			а.	· No	o. of stud	ents obta	ining ma	arks. in t	he ra	nge
Sr. No.	Subject	% of Passing	No. of students appeared	66 to 100	60 to 65	55 to 59	50 to 54	41 to 49	40	Less than 40 (Fail)
1	HWRE	52.05	73	2	4	4	4	21	3	35
2	WSE	46.57	73	5	- 4	4	4	16	1	39
3	DSS	38.35	73	0	5	L	3	13	6	45
4	EEFM	73.10	73	0	6	7	9	24	8	19
5	CM	74.2	62	8.	. 7	8	6	15	2	16
6	ACT	100	11	3	2	2	1	3	0	0
7	UHI .	87.5	8	1	2	e I	0	3	0	

D. Toppers:

Rank	Name of students	Total Marks out of 700	Percentage/SGPA
1	AYUSH AMAR MADIKUNT	477	8.38
2	KARAN YADAV	474	8.24
3	KHANDARE SAHIL RAVI	464	8.19
4	SHINDE MANDAR PURUSHOTTAM	456	
5	ROY MITALI ANIL	457	8.14 Principal
	air Si	3	Econeering Lohegaon P

rincipal

HoD Civil Engineering Ajeenkya DY Patil School of Engineering Lohegaon, Pune

42



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RESULT ANALYSIS

SPPU Examination: NOV-2022

. Semester: VII

Department: CIVIL

Class: BE

School o

Date of declaration of result: 16/03/2023

A. Overall Result Analysis

Sr. No.	Details	No. of Students
1	No. of students appeared	100
2	No. of students passed (All clear)	39
3	No. of students failed (With ATKT)	61
4	All clear passing Percentage	39
5	All clear passing Percentage (University)	-
6	Percentage of passing with ATKT	
7	Percentage of passing with ATKT (University)	-
8	No. of students passed with Distinction	16
9	No. of students passed with First Class	13
10	No. of students passed with Higher Second Class	05
11	No. of students passed with Second Class	04
12	No. of students passed with Pass Class	01
13	No. of students failed in 1 Th. Sub	16
14	No. of students failed in 2 Th. Subs	12
15	No. of students failed in 3 Th. Subs	17
16	No. of students failed in more than 3 Th, Subs	14
17	No. of students failed in 1 Pr/Or	11
18	No. of students failed in 2 Pr/Or	06
19	No. of students failed in 3 Pr/Or	05

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Engineering, Lohegaon, Pune

B. Subject Wise Result Analysis:

Sr. No.	Name of the Subject	TH/Pr	Name of the Staff Member	No. of Students Appeared	No. of Students Passed	% of Passing
1	Foundation Engineering	тн	Prof. Gayatri C Sherkar	100	54	54
2	Transportation Engineering	тн	Prof. Uday A. Kakde	100	54	54
3	Integrated Water Resources Planning & Management	тн	Prof. Sarika Thombare	100	80	, 80
4.	Air Pollution and Control	тн	Prof. Sheetal Marwar	100	68 .	68
3	HON	TH	Prof.Sheetal Marwar	29	25	86.20

C. Subject Wise Scoring Pattern:

			No. of	lo. of stud	ining m	marks. in the range				
Sr. No.	Subject	% of Passing	students appeared	66 to 100 ·	60. to 65	55 to 59	50 to 54	41 to 49	40	Less than 40 (Fail)
·1 .	FE	54	100	5	6	10	6	21	6	46
2	TRE	54	100	. 4	6	9	13	19	3	46-
3	IWRPM	80	100	20	11	13	10	21	5	20
4	APC	68	100	10	4	7	13	27	7	32
5	HON	86.20	29	7	5	4	4	03	2	4

D. Toppers:

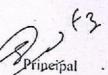
Rank	Name of students	Total Marks out of 700	Percentage/SGPA
1	SAYYED SALMA JAVED	575	9.70
2	OZARKAR GUNVANT PRAMOD	567	9.55
3	MAHER ARUN SANTOSH .	527	9.05
4	SHENDAGE MEENAKSHI BAJARANĠ	536	8.95
5	MOKLE PAWAN SANJAY	493	8.55

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Civil Engineering
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Department of E&TC Engineering

Form No. IQAC/10

Course File Index

Class: T.E.

Subject: Power Devices & Circuits

Sr.No.	Contents	Remark
1.	Department Academic Calendar	~
2.	Syllabus with Course Objectives	<u></u>
3.	Master Time Table	L
4.	Individual Time Table	-
5.	Teaching & Practical Plan	L
6.	Subject Notes (Handwritten Notes, Presentations)	_
7.	Best Practices	~
8.	Additional Topics Covered (other than syllabus)	_
9.	University Question Papers & Model Answer Sheets (Insem & End Sem)	V
10.	Question Bank (Theory)	V
11.	Question Bank (Oral)	L
12.	Assignments/Tutorial	L.
13.	Attendance Record	-
14.	Continuous Assessment Record	1-
15.	Unit Test I: Question Paper, Attendance Record, Result	V
16.	Unit Test II: Question Paper, Attendance Record, Result	レ
17.	Subject Result	V
18.	Lab Manual	L
19.	CO/PO mapping & attainments	V
20.	Faculty Feedback	L

(Riya) L. Kazi)
Name and Sign of Faculty

Gramal

HoD

Cohegaon Pune

Phear

Dean Academics

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Department of E&TC Engineering

1

Department Academic Calendar

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Department Academic Calendar 2022-23 [Tentative] Form No:1QAC/01(b)

Bold Colour Activities for Department Academic Calendar **JUNE 2022** a. Admission Committee Meeting with Principal b. Staff identification, advertisement, recruitment SUN MON TUE WED THU FRI SAT c. Meeting for preparation of annual budget d. First Meeting of College Development Committee [CDC] 2 3 4 a-b c. A. Y. 2021-22 Teacher diary submission f. Staff performance appraisal 5 6 7 8 9 10 11 c-d-e g. Dead stock verification h. 360 Degree Feedback of Institute Staff 12 13 14 15 16 17 18 f-g-h i. Service book updation j. A. Y. 2022-23 IQAC plan of action 19 20 21 22 23 24 25 i-j-k k. AQAR 2022-23 Activity Calendar 28 1.5 Days (30 hours) Faculty development Program /Workshop for 29 30 - 1 Teaching from department of E&TC Engginering **JULY 2022** a. IQAC Meeting No. 1 for Annual Planning. b. NAAC & AQAR Criterion Chairpersons Meeting with Principal & IQAC SUN MON TUE WED THU FRI SAT Coordinator for execution of AQAR/NAAC activities Calendar c. Department Academic Calendar 1 2 a-b d. Cell Coordinator Meeting with Principal & IQAC Coordinator e. Meeting of Section Incharges (TPO, CEO, Librarian, SDO) with Principal 3 4 5 6 7 8 9 c-d-e f. Statutory & Non-Statutory Committee Meeting with Principal 10 11 12 13 g. Load Distribution, Staff Portfolio & Time table 14 15 16 f-g h. Commencement of Teaching: SEM-I (18/07/2022) 17 18 i. Induction program for Staff & students 19 20 21 22 23 h-i-j j. Identification of Slow learners & advanced learners 24 25 26 27 k.5 Days (30 hours) Faculty development Program/Workshop for 28 29 30 k-l-m Teaching from department of Mechanical Engineering 31 I.Bridge course for Slow Learners m. Approval Process for Staff a. Administrative Training Program for Non-Teaching Staff from IQAC AUGUST 2022 b. Notice for Student Eligibility & Scholarship Form Submission SUN MON TUE WED THU FRI SAT c. University Affiliation d. 15th August - Independence Day Celebration 1 3 4 5 6 a e. Student's Feedback - I, Feedback Analysis & Action Taken Report 7 8 9 Alahuam 10 11 12 13 b-c f. Academic Progress Report - 1 15-Aug Th-Pausa g. Student's attendance, defaulter List-I & Action Taken Report 14 17 18 19 20 d-e-f h. Unit Test -I & Result Analysis 21 22 23 24 25 26 27 g-h i. Remedial Teaching & Retest j. Department Advisory Board Meeting-I 28 ij

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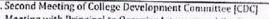








a. Second Meeting of College Dev				er 2022	Decembe				
 b. Meeting with Principal to Orga c. Meeting with Principal to Orga 		SAT	FRI	THU	WED	TUE	MON	SUN	
d. 5 Days (30 hours) Faculty de	а	3	2	1					
Teaching from department of e. Meeting for planning of Ind	b-c	10	9	8	7	6	5	4	
f. Review Meeting of NAAC/AQA	d	17	16	15	14	13	12	11	
Committees with Principal & IQA	е	24	23	22	21	20	19	18	
g. Load Distribution, Staff Port	f-g	31	30	29	28	27	26	45	
		January 2023							
a. Commencement of Teaching b. Identification of Slow learne		SAT	FRI	THU	WED	TUE	MON	SUN	
c. Bridge course for Slow Learn	3 8	7	6	5	4	3	2	1	
d. 5 Days (30 hours) Professional IOAC	b-c	14	13	12	11	10	9	8	
e. 26th January - Republic Day	d	21	20	19	18	17	16	15	
f. Administrative Training Progra g.Department Advisory Board		28	27	26	25	24	23	22	
gazepan tinent Auvisury Board	f-g					31	30	29	
- Barrier at - 1 f - 1 at - 2 f - 1				2023	February				
a. Participation in Institute Rankli b. Student's Feedback - I, Feed		SAT	FRI	THU	WED	TUE	MON	SUN	
c. Academic Progress Report - I	4	4	3	2	1				
d. Student's attendance, default e. Unit Test -1 & Result Analysis	b-c	11	10	9	8	7	6	5	
f. Remedial Teaching & Retest	d-e	18	17	16	15	14	13	12	
g. 5 Days (30 hours) Faculty des Teaching from department of A	f-g	25	24	23	22	21	20	19	
h. NSS Activities	h		7.0			28	27	26	



anize International Conference

anize Techfest/Project Competition

evelopment Program/Workshop for Computer Engineering

ustrial Visits /Field Projects

AR Chairpersons, Cell coordinators, and C Coordinator

folio & Time-Table for SEM-II

g: SEM-II (02/01/2023)

ers & Advanced learners

ners

development Program for Teaching from

Celebration

nm for Non-Teaching Staff from IQAC Meeting-II

lback Analysis & Action Taken Report

ter List-I & Action Taken Report

velopment Program/Workshop for I&DS







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March 2023 a. Student Notice for Exam form Submission SUN WED MON TUE THU FRI SAT b. SPPU INSEM EXAM - Invigilation & CAP duty c. Parents Teacher's Meet- I (PTM-I) 1 2 3 4 a d. Field Projects/Industrial Visits 7 8 9 10 5 6 11 b e. Academic Progress Report - II (APR-II) f. Student's attendance, defaulter List-II & Action Taken Report 12 13 14 15 16 17 18 c-d g. International Conference 19 20 21 22 23 24 25 e-f h.IQAC Meeting No. 3 26 27 28 29 31 g-h April 2023 a. Student's Feedback - II, Feedback Analysis & Action Taken Report WED THU FRI SAT MON TUE SUN b. Unit Test -II & Result Analysis 1 a c. Remedial Teaching & Retest 7 8 b d.Tech Fest/Project Competition 4 5 6 2 3 c. Annual Social Gathering 15 c·d 14 9 10 11 12 13 f. Graduation Day Celebration [BE Students Send-off] 22 17 18 19 20 21 e-f-g g. Convocation 16 h.Mock OR/PR Exam & Student Submission 29 27 28 26 i. Conclusion of Teaching:SEM-II (29/04/2023) 24 25 23 30 May 2023 a. SPPU OR / PR EXAM b. SPPU ENDSEM EXAM - Invigilation, CAP duty & Vacation SAT FRI WED THU TUE MON SUN c. Academic & Administrative audits d. Student Satisfaction Survey, Course & Programmme Exit Survey, 5 5 3 3 4 1 2 Stakehoders Feedback, Analysis & Action Taken Report b-c 12 13 11 9 10 e. Analysis of activities planned in Academic Calendar, IQAC Plan of action, 7 8 20 d-e AQAR activity Calendar, DAB Activities & their action taken reports 19 17 18 14 15 16 f. Website Updation & Annual Report 26 27 ſ 25 24 22 23 g. IQAC Meeting No. 4 21 g 31 29 30 28

HoD



Principal

Principal Dr. D. Y. Patil School of Engineering

Lollegaon, Pune.





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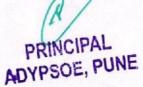
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Department of E&TC Engineering

2

Syllabus with Course Objectives





Savitribai Phule Pune University

Third Year of E & Tc Engineering (2019 Course)

304194: Power Devices & Circuits

Teaching Scheme:	Credit	Examination Scheme:		
Theory: 03 hrs./week	03	In-Sem (Theory): 30 Marks		
		End Sem (Theory): 70 Marks		

Prerequisite Courses, if any:

- 1. Basic Electrical Engineering
- 2. Basic Electronics Engineering
- 3. Electronic Circuits
- 4. Electrical Circuits

Companion Course, if any: Power Devices & Circuits Lab

Course Objectives:

- To introduce different power devices viz. SCR, GTO, MOSFET and IGBT with construction, characteristics, repetitive and non repetitive ratings and typical triggering/driver circuits.
- To understand working, design and performance analysis and applications of various power converter circuits such as ac to dc converters, inverter and chopper
- To know various protection circuit requirements of power electronic devices.

Course Outcomes: On completion of the course, learner will be able -

CO1: To differentiate based on the characteristic parameters among SCR, GTO, MOSFET & IGBT and identify suitability of the power device for certain applications and understand the significance of device ratings.

CO2: To design triggering / driver circuits for various power devices.

CO3: To evaluate and analyze various performance parameters of the different converters and its topologies.

CO4: To understand significance and design of various protections circuits for power devices.

CO5: To evaluate the performance of uninterruptible power supplies, switch mode power supplies and battery.

CO6: To understand case studies of power electronics in applications like electric vehicles, solar systems etc.



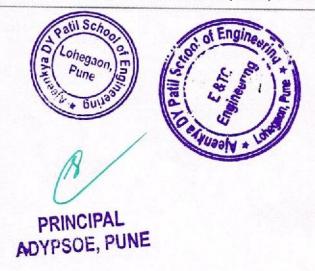
	Course Contents ,	
Unit I	Study of Power Devices	(06 Hrs.)
Construction, VI charact	eristics (input, output and transfer if any), switching character	istics of SCR, GTC
	BT, Performance overview of Silicon, Silicon Carbide & GaN	
	and non-repetitive ratings of SCR, GTO, Power MOSFET	
significance, requirement	t of a typical triggering / driver (such as opto isolator) circuit	te for various nows
	ries and parallel operations of various power devices (no derivation	
	CO1: To differentiate based on the characteristic parameter	
Outcomes for Unit I	GTO, MOSFET & IGBT and identify suitability of the	s among SCK, e nower device for
	certain applications and understand the significance o	f device ratings.
	CO2: To design triggering / driver circuits for various power	devices
Unit II	ACto DCD. G	
	AC to DC Power Converters	(06 Hrs.)
	commutation, Single phase Semi & Full converters using SCR for	
	sis and numerical, Effect of source inductance, Significance of po-	
	based techniques, Three phase Full converters using SCR for R lo	
	gle Phase PWM Rectifier using IGBT, Three Phase Controlled Rec	ctifier Using IGBT,
	pased conventional rectifiers and IGBT based rectifiers.	
Mapping of Course Outcomes for Unit II	CO3: To evaluate and analyze various performance parameter	ers of the differen
Unit III	converters and its topologies.	
	DC to AC Converters	(06 Hrs.)
	Il bridge square wave inverter for R and R-L load using MOSI	
performance analysis and	numerical, Cross conduction in inverter, need of voltage contr	ol and strategies in
	f voltage control techniques, control of voltage using various P	
	and need of harmonic elimination / reduction in inverters, Three Pl	
	r R load with 120 and 180 degree mode of operation, device	
Advanced Converters like	matrix inverter, multi-level inverters and their topologies and its d	river circuits (no
derivation and numerical).		
Mapping of Course	CO3: To evaluate and analyze various performance parameter	ters of the
Outcomes for Unit III	different converters and its topologies.	
Unit IV	DC to DC Converters	(06 Hrs.)
lassification of choppers,	Step down chopper for R and RL load and its performance analys	is, Step up chopper,
arious control strategies for	or choppers, types of chappers (produted and non isolated) such as	type A, B, C, D &
switch mode power supp	oly (SMPS) viz but a pooler and burk boost, Fly back, Half and fu	ll Bridge isolated
	d bidirectional topy forces, and concept of integrated converter and	d design of LM3524
ased choppers, concept of	maximum power point tracking MPPT school of	
Apping of Course		rs of the different
Outcomes for Unit IV	converters and its topological	I.
DO		
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Unit V	Power Devices Protection and Circuits	(06 Hrs.)
heat sink design, Resonan Electromagnetic interfere	t, di/dt and dv/dt protection circuits and their design, Various coort converters such as Zero current switching (ZCS) and Zero volta ence such as radiated and conducted EMI, Difference between g and minimizing / shielding techniques for EMI, Various EMI ansformer.	nge switching (ZVS), EMI and EMC, EMI
Mapping of Course Outcomes for Unit V	CO4: To understand significance and design of various prot power devices.	tections circuits for
Unit VI	Power Electronics Applications	(06 Hrs.)
drive for single phase sepa three phase induction m performance parameters o EVs, Architecture of EVs	sing IGBT & SCR, Fan Regulator, Electronic Ballast, LED Landrately excited dc motor, BLDC motor drive, Variable voltage & notor drive, On-line and Off- line UPS, study of various of batteries in battery operated power systems, battery charging mattery charger, PFC stage circuit topologies with details of Fulled for EV battery charger, case study of power electronics in	variable frequency selection criteria and odels and modes for -bridge boost rectifier
Mapping of Course Outcomes for Unit VI	CO5: To evaluate the performance of uninterruptible power mode power supplies and battery. CO6: To understand case studies of power electronics in appelectric vehicles, solar systems etc.	

Learning Resources

Text Books:

- M. H. Rashid, "Power Electronics Circuits Devices and Applications", PHI,4th Edition 2017 New Delhi.
- 2. M. D. Singh and K. B. Khanchandani, "Power Electronics", TMH, 2nd Edition 2006.



Reference Books:

- Bogdan M. Wilamowski, J. David Irwin, "The Power Electronics and Motor Drives Handbook", CRC Press, 1st Edition, 2011.; eBook: ISBN 9780429165627, 2019.
- 2. Muhammad H. Rashid, "Power Electronics Handbook", Academic Press, 2nd Edition, 2001
- Ned Mohan, T. Undeland & W. Robbins, "Power Electronics Converters Applications and Design, John Willey & sons, Singapore, 2nd Edition Oxford University Press, New Delhi, 2005
- Ali Emadi Alireza Khaligh Zhong Nie Young Joo Lee, "Integrated Power Electronic Converters and Digital Control", CRC Press, 1st Edition.
- 5. Vinod Kumar Khanna "Insulated Gate Bipolar Transistor IGBT Theory and Design", John Wiley & Sons, Illustrated Edition.

Print ISBN:9780471238454; Online ISBN:9780471722915, DOI:10.1002/047172291.

 L. Ashok Kumar, S. Albert Alexander and Madhuvanthani Rajendran, "Power Electronic Converters for Solar Photovoltaic Systems", Elsevier, 1st Edition, 2020.

MOOC/NPTEL Courses:

1. NPTEL Course on "Power Electronics"

Link of the Course: https://nptel.ac.in/courses/108/105/108105066/

https://nptel.ac.in/courses/108/102/108102145/

https://nptel.ac.in/courses/108/107/108107128/

https://nptel.ac.in/courses/108/108/108108077/

https://batteryuniversity.com/

Patil School of Engines in the Control of En

PRINCIPAL ADYPSOE, PUNE

Savitribai Phule Pune University

Third Year of E & Tc Engineering (2019 Course)

304197: Power Devices & Circuits Lab

Teaching Scheme:	Credit	Examination Scheme:
Practical: 02 hrs. / week	01	Practical: 50 Marks

Prerequisite Courses, if any:

- 1. Electrical Circuit Laboratory
- 2. Electronic Circuit Laboratory

ompanio	n Course, if any: Power Devices & Circuits
	List of Laboratory Experiments
	Group A (All Compulsary)
1.	VI Characteristics of SCR i) Plot output V-I characteristics to measure I _H , I _L and voltage before and after breakdown, ii) Observe the effect of gate current on forward break down iii) gate characteristics iv) compare with datashect specifications
2.	V-I Characteristics of Power MOSFET i) Plot output characteristics and calculate output resistance ii) Plot transfer characteristics and measure threshold voltage iii) compare with datasheet specifications
3.	V-I Characteristics of IGBT i) Plot output characteristics and calculate output resistance ii) Plot transfer characteristics and measure threshold voltage iii) compare with datasheet specifications
	Group B (Any 2)
6.	Single phase Full Converter using IGBT / SCR with R & R-L load i) Observe load voltage waveform, ii) Measurement of average o/p voltage across loads, iii) Verification of theoretical values with practically measured values.
8.	Single-Phase PWM Power MOSFET / IGBT based bridge inverter for R and motor load i) Observe output voltage waveforms and measure set of rms output voltage for varying pulse width and variable input dc voltage for R and motor load, ii) compare measured output voltages with the theoretical findings
9.	Step down / Step up chopper using power MOSFET / IGBT i) Measure duty cycle and observer effect on average load voltage for DC chopper
A COL	Group C (Any 4)
11.	SMPS /UPS Performance Evaluation i) find load & line regulation characteristics for no load til School on and at 500 mA & 1A load ii) compare the performance with supplier specifications
O BANNOO STA	Purpose Phase AC voltage controller using IGBT/SCR for R and RL load Purpose Purpose Phase AC voltage waveforms, ii) Measurement output voltage across load, Purpose Purpose Phase AC voltage waveforms, iii) Measurement output voltage across load, Purpose Purpose Phase AC voltage controller using Powers In 1 and open source circuit simulation software

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14.	To design and implement a solar cell operated emergency lighting system.	
15.	To study battery testing, safety and maintenance of batteries	







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Department of E&TC Engineering

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Master Time Table







Dr. D. Y. Patil Technical Campus

Ajeenkya D. Y. Patil School of Engineering

Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohgaon, Pune – 412 105 Department of Electronics and Telecommunication Engineering Form No.: IQAC/7

Revision:01

Date: 06/02/2023

AY 2022-23 Master Time Table SE, TE and BE Sem – II w. e. f. 06/02/2023

Day	Year -Div.	10:00 -11:00	11:00 -12:00	12:00 – 12:45	12:45 – 01:45	01:45 - 02:45	02:45- 3:00	03:00 -04.00	04:00 -05.00
	SE(A)	SS(AB)-227	CS(KM)-227		OOPS(AD)- 227	OOPs(AD)-227	,		SA2-OOPs(AD)-23 BL(SA)-240
	SE(B)	CS(KM)-224	PCS(SS)-224		ESD(SD)-2	1W)-239,SB2- 23, SB3-SS+CS (AB)-220	•	SS(AB)-224	Library Reading
Mon day	TE(A)	NS(SMK)-232	PM(SA)-232		TA3-CN(KR NS(SMK)-239	()-236, TA1- , TA2-PDC(SP)- 20		CN(KK)-232	PDC(RLK)-232
	TE(B)	TB3-CN(KK NS(MW)-239, 22	TB2-PDC(SP)-		PM(SA)-232	PDC(RLK)-232		NS(SMK)-227	CN(KK)-227
	BE	DM(SI)	MC(SK)	Lunch Break	FOC(PKH)	DBM(SI)	Short Break	B2-FOC(PK)	H), B1-MC(SK)
	SE(A)	PCS(SS)- 227	SS(AB)-227		OOPs(AD)- 227	PCS(SS)-227	Dieak		SA1-OOPs(AD)-239 L(SA)-240
	SE(B)	CS(KM)- 224	PCS(SS)-224		ESD(SD)-22	W)-239,SB1- 3, SB2-SS+CS AB)-220		SS(AB)-224	ESD(KM)-224
Tues day	E(A) oneg	85 SMK)- 232	PM(SA)-232		TA2-CN(KK NS(SMK)-239, 22	TA1-PDC(RLK)-		NS(SMK)-232	CN(KK)-232
>	TE PUIGO	NS(MW)-239 (RLK)	D. TB1-PDC		PM(SA)-232	Mini Project(SK)- 232	all School	CN(KK)-227	PDC(RLK)-227
CIPAL DE, PU	BE	MC(SK)	FOC(PKH)		DM(SI)	DBM(SI)	E &TC	B1-FOC(PKH), B2-MC(SK)

	REPORT OF	The steer of the last	III Waller and the second
	SE(A)	SS(AB)-227	CS(KM)-227
Wed	SE(B)	PCS(SS)- 224	SS(AB)-224
nesd	TE(A)	PDC(RLK)	PDC(RLK)- 232
	TE(B)	TB1-CN(KK) NS(MW)-239 (SP)-220	
1	BE	DM(SI)	FOC(PKH)
	SE(A)	SA2-DA(MW)- ESD(PKH)-223, SS+CS(KM/AB)	SA1-
	SE(B)	SS(AB)-227	CS(KM)-227
Thur sday	TE(A)	PM(SA)- 232	Mini Pro- ject(SK)-232
	TE(B)	CN(KK)- 224	PDC(RLK)- 224
	BE		S-II
Frid /	SE(Apple)	SAI-DA(MW) SASD(PKH)-22 SE+CS(KM/A	3, SA3-
	S S S S S S S S S S S S S S S S S S S	ŒХD(КМ)- 227	OOPs(AD)- 227
2	TE(A)	InternS	Ship(AB)-224
NCIPAL	TE(B)	Mini Pr	oject(SK)-232
SOE, PUN	E	***************************************	

PCS(SS)- 227	SS(AB)- 227
OOPs(AD)- 224	OOPs(AD) -224
TA1-CN(KK)- NS(SMK)-239 (RLK)-220	
PM(SA)- 232	Mini Pro- ject(SK)-232
MC(SK)	I &E(PKH)
ESD(P	KH)-227
OOPs(AD)-239	S)-236, SB1- 9, SB2-PBL (SI)- 40
OOPs(AD)-239	9, SB2-PBL (SI)-
OOPs(AD)-239	9, SB2-PBL (SI)-
OOPs(AD)-239	9, SB2-PBL (SI)- 240 InternShip(AB
OOPs(AD)-239 2 InternShip(A B)-232 InternShip(A B)-232	J, SB2-PBL (SI)-40 InternShip(AB)-232 Library
OOPs(AD)-239 InternShip(AB)-232 InternShip(AB)-232 PS SA3-DA(MWESD(PKH)-2	InternShip(AB)-232 Library Reading S-II 7)-239,SA1- 23, SA2-
InternShip(AB)-232 InternShip(AB)-232 InternShip(AB)-232 InternShip(AB)-232 PS	P, SB2-PBL (SI)-440 InternShip(AB)-232 Library Reading S-II 7)-239,SA1-23, SA2-AB)-220 236, SB3-
OOPs(AD)-239 InternShip(A B)-232 InternShip(A B)-232 PS SA3-DA(MW ESD(PKH)-2 SS+CS(KM/A SB2-PCS(SS)-OOPs(AD)-239 PBL(SI)-240	P, SB2-PBL (SI)-440 InternShip(AB)-232 Library Reading S-II 7)-239,SA1-23, SA2-AB)-220 236, SB3-

SA2-PCS(SS)-236, SA3-OOPs(AD)-239, SA1-PBL(SA)-240 SB2-DA(MW)-239,SB3-ESD(SD)-223, SB1- SS+CS(KM/AB)-220 CN(KK)-232 Library Reading NS(SMK)-NS(SMK)-232 232 I &E(PKH) PS-II CS(KM)-227 Library Reading SB1-PCS(SS)-236, SB2-OOPs(AD)-239, SB3-PBL (SI)-240 Library Read-Library Reading ing Library Reading Library Reading PS-II Lib.Reading Lib.Reading pall Schoo! Lib.Reading E &TC Engineering Lib.Reading

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	BE		9	PS-II	Lib.Res	Lib.Reading	
		Notation	Name of The Subject	Notation	Name of The Faculty		
		SS	Signal and System	AB	Prof. Ashwini Bagde		
		CS	Control System	KM	Prof. Kalpita Mane		
		PCS	Principle of Communication System	SS	Prof. Shaurya Shubham		
	SE(A)	DA	Data Analytics	MW	Prof. Manisha Wasnik		
		ESD	Employability Skill Development	PK	Prof. Prajakta Khairnar		
		PBL	Project Based Learning	SA	Prof. Saniya Ansari		
		OOPS	Object Oriented Programming	AD	Prof. Anjali Dagade		
		SS	Signal and System	AB	Prof. Ashwini Bagde		
		CS	Control System	KM	Prof. Kalpita Mane		
		PCS	Principle of Communication System	SS	Prof. Shaurya Shubham		
	SE(B)	DA	Data Analytics	MW	Prof. Manisha Wasnik		
		ESD	Employability Skill Development	KM	Prof. Kalpita Mane		
	(30)	A. ERES	Project Based Learning	SCI	Dr. Sharan Inamdar		
	*	PSPS	Object Oriented Programming	AD	Prof. Anjali Dagade		
	La la	BM S	Project Management	SA	Prof. Saniya Ansari		
	TEIA	PD	Power Devices & Circuits	RLK	Prof. Riyaj Kazi	100	
CIPAL	TE(A) JNE	NS	Network Security	SMK	Dr. S. M. Koli		
E PL	JNE	CN	Cellular Networks	KK	Prof. Kranti Kamble	ERTC	

		1411111 1 10)000		1 101. Small Edumate
		Inter	AB	Pl Ashwini Bagde
	PM	Project Management	SA	Prof. Saniya Ansari
	PDC	Power Devices & Circuits	RLK	Prof. Riyaj Kazi
	NS	Network Security	SMK	Dr. S. M. Koli
TE(B)	. 1		MW	Prof. Manisha Wasnik
	CN Cellular Networks		KK	Prof. Kranti Kamble
		Mini Project	SW	Prof. Swati Khawate
		Internship	AB .	Prof. Ashwini Bagde
	DM	Digital Marketing	SCI	Dr. Sharan Inamdar
	DBM	Digital Business Management	SCI	Dr. Sharan Inamdar
BE	FOC	Fiber OPtics Communication	PK	Prof. Prajakta Khairnar
	MC	Mobile Computing	SW	Prof. Swati Khawate
	I&E	Innovation & Entrepreneurship	PK	Prof. Prajakta Khairnar

		Clas	s Teachers		
SE(A)	Prof. Anjali Dagde	TE(A)	Prof. Shital Patil	BE	Prof. Swati Khawate
- SECOND	Prof. Shaurya Shubham	TE(B)	Prof. Kranti Kamble		

PRINCIPAL Prof. Shaurya Shubham
ADYPSOE, PUNE Timetable Incharge

Dr. Sharan Inamdar

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E&TC Engineering

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Lohegaon, Pune

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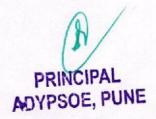
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Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohgaon, Pune – 412 105 Department of E&TC Engineering

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Individual Time Table







Dr. D. Y. Patil Technical Campus

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Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohgaon, Pune – 412 105 Department of Electronics and Telecommunication Engineering Form No.: IQAC/7

Revision:01

Date: 06/02/2023

AY 2022-23 Master Time Table SE, TE and BE Sem - II w. e. f. 06/02/2023

Individual Time Table: Mr. Riyaj Kazi

Day	Year -Div.	10:00 -11:00	11:00 -12:00	12:00 - 12:45	12:45 - 01:45	01:45 - 02:45	02:45- 3:00	03:00 -04.00	04:00 -05.00
	TE(A)								PDC(RLK)-232
MON	TE(B)	TB2-PDC	(RLK)-220			PDC(RLK)- 232			
	SE(A)								
	SE(B)								
CODA DAS	TE(A)				TA1-PDC(RLK)-220			
TUE	TE(B)	TB1- (RLK	-PDC ()-220						PDC(RLK)-227
1	BE								
	SE(A)								
	SE(B)		2		100 H				
WED	(B)p	-232	PDC(RLK)- 232		TA3-PDC(RLk	X)-220			
	ABE S	3 8					A E		
THU	SEAPOU	603				//s	not of Engine	13	

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	SE(B)	8	
	TE(A)		
	TE(B)		
	BE		
FRI	SE(A)		
	SE(B)		
	TE(A)	TA2-PDC(RLK)-220	
	TE(B)		
	BE		

PRINCIPAL ADYPSOE, PUNE Prof. Shaurya Shubham

Timetable Incharge





Dr. Sharan Inamdai

[HoD-E&TC]



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Teaching & Practical Plan



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DY Patil Knowledge City, Charoli (Bk), Via Lohegaon, Pune 412105

TEACHING PLAN

Department of Electronic & Telecommunication Engineering

Class: TE Div: A Sem II AY 2022-23

Subject: Power Devices & Circuits (304194)

Lectures per Week: 3

Subject In-charge: Mr. Riyaj Kazi

Prerequisite Courses:

Basic Electrical Engineering, Basic Electronics Engineering, Electronic Circuits, Electrical Circuits

	The CO-PO Mapping	g Matri	x						News Pa		N. W.			CHI GAR
CO			PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	To differentiate based on the characteristic parameters among SCR, GTO, MOSFET & IGBT and identify suitability of the power device for certain applications and understand the significance of device ratings.	CO1	3									1010		2
CO2	To design triggering / driver circuits for various power devices.	CO2	3	2	1	1								2
CO3	To evaluate and analyze various performance parameters of the different converters and its topologies.	соз	3	2	2	2								2
CO4	To understand significance and design of various protections circuits for power devices.	CO4	1	2	1	2	Tan 1							2
CO5	To evaluate the performance of uninterruptible power supplies, switch mode power supplies and battery.	CO5	1						•					2
CO6	To understand case studies of power electronics in applications like electric vehicles, solar systems etc	CO6	3				2							2

Lect No	Unit No	Planned Date	Details of topic to be	Actual date of conduction	Suggested Books (Ex. R1, T1) (Chap No)	* Mode of Teaching with details	** Competency based Education	Remark by HOD. Dean & Principal
Since.			50 Minutes T	'eaching	* aund 'u		10 Minutes Teaching	

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						A. Conventional method	GoI Initiative on Electric Vehicle, Green Energy Awareness & Scope of Power Devices & Circuits.	
						B. Experiential	https://e-amrit.niti.gov.in/benefits- of-electric-vehicles + EV PPT Discover what silicon-controlled rectifiers (SCR) can do for EV	
1		01-02-2023	Unit I: Study of Power Devices: SCR Construction & VI characteristics	01-02-2023	T1 (Ch 9), T21(Ch 2)	learning	charging applications https://www.youtube.com/watch?v =UjJV95XWnbo	Andrau D
						A. Conventional method	Switching Devices https://www.tutorialspoint.com/po	
2	C		Switching characteristics of SCR	1-02-23	R3(Ch 23)	F. Interactive Method	wer_electronics/power_electronics_switching_devices.htm & https://www.electronicshub.org/tuto_rials/	
			GTO: Construction, VI characteristics			A. Conventional method	Hitachi Energy's gate turn-off thyristor is optimized for low conduction losses.	
3	1	07-02 23 23 ch	egaon, raging	02-02-23	R3 (Ch 24), R1(Ch 1) R6(Ch.4)	F. Interactive Method	https://www.hitachienergy.com/in/en/products-and-solutions/semiconductors/gate-turn-off-thyristors-gto	
4		08-02-2023	* 500	SONO 1 * Aje	T1 (Ch 4)	A. Conventional method	MOSFETs with extraordinary SOA for industrial applications	
			PRINCIPAL ADYPSOE, PUNE	The State of 7	DV Pa			

							1
		Power MOSFET: Construction, VI characteristics	02-02-23		F. Interactive Method	https://www.youtube.com/watch ?v=w99HJDNwBDY Ease of use in power MOSFET product development https://www.youtube.com/watch ?v=4T-R06Wt6TE	
5	8-02-2023	IGBT: : Construction, VI characteristics	07-02-23	R3(Ch 2)	A. Conventional method F. Interactive Method	IGBTs in Electric Vehicles https://www.allaboutcircuits.com/n ews/spike-evs-means-spike- insulated-gate-bipolar-transisters- igbts/	mana 2
					A. Conventional method	Experience the difference of Si / SiC / GaN technology: Future Electronics https://www.youtube.com/watch?v =aake09AW1D8 & Enabling the Future of Power Electronics: Yes, We GaN!	
6	14-02-2023	Performant diversew of Silicon, Silicon Carbide & GaN based MOSFET and IGBT	07-02-23	T1 (Ch 4)	F. Interactive Method	https://www.powerelectronicsne ws.com/enabling-the-future-of- power-electronics-yes-we-gan/ SiC Power Devices https://www.youtube.com/watch?v =iz_QNdhFG0Q	
		PRINCIPAL ADYPSOE, PUNE	Ingineering Adjusted	N Scroon	Wethod		

						Still State
14-02-2023	Various repetitive and non- repetitive ratings of SCR, GTO, Power MOSFET & IGBT and their significance.	08-02-25	T1 (Ch 1), R2(Ch 6)	A. Conventional method	NA	
14-02-2023	Requirement of a typical triggering / driver (such as opto isolator) circuits for various power devices	14-02-23	T1 (Ch 9), R3(Ch 6)	A. Conventional method	MOSFET Gate driver circuit using optocoupler https://www.youtube.com/watch?v=wakrtG4XesE	
15-02-2023	Importance of series and parallel operations of various power devices	14-02-23	R6(Ch 4)	G. Assessment of Competency based Education	NA	Brania
21-02-2023	Unit II : AC to DC Power Converters : Concept of line & forced commutation	15-02-23	T1 (Ch 9)	A. Conventional method F. Interactive Method	AC to DC Switching Converters Market with trend & forecast to 2028. https://www.marketwatch.com/pres s-release/ac-dc-switching- converter-market-2023-top- manufacturers-key-trends- progression-status-and-business- trends-and-forecast-to-2028-2023- 02-10	
	Single phase Semiconverter & Full Convertes using SCR	21-2-23	-	A. Conventional method B. Experiential	Industrial AC to DC Converters: Products, Specifications, & Manufacturers: https://www.everythingpe.com/sear	
22-02-2023	Pune of Employed	8-3-23	R2(Ch 12)	A. Conventional method B. Experiential learning	AI helps optimise power electronic converters https://www.cardiff.ac.uk/news/view/2683943-ai-helps-optimise-power-electronic-converters	
	14-02-2023	Requirement of a typical triggering / driver (such as opto isolator) circuits for various power devices Importance of series and parallel operations of various power devices Unit II: AC to DC Power Converters: Concept of line & forced commutation Single phase Semiconverter & Full converters using SCR for thirds Single phase Semiconverter & Full converters wising SCR for thirds Single phase Semiconverter & Full convertes wising SCR for thirds Single phase Semiconverter & Full convertes wising SCR for thirds Single phase Semiconverter & Full convertes wising SCR for thirds Single phase Semiconverter & Full convertes wising SCR for thirds Single phase Semiconverter & Full convertes wising SCR for thirds Single phase Semiconverter & Full convertes & Full	repetitive ratings of SCR, GTO, Power MOSFET & IGBT and their significance. Requirement of a typical triggering / driver (such as opto isolator) circuits for various power devices Importance of series and parallel operations of various power devices Unit II: AC to DC Power Converters: Concept of line & forced commutation Single phase Semiconverter & Full Converter & Full Convert & Full Converter & Full Converter & Full Converter & Full Conve	repetitive ratings of SCR, GTO, Power MOSFET & IGBT and their significance. Requirement of a typical triggering / driver (such as opto isolator) circuits for various power devices Importance of series and parallel operations of various power devices 15-02-2023 power devices Unit II: AC to DC Power Converters: Concept of line & forced commutation Vinit II: AC to DC Power Converters: Concept of line & forced commutation Single phase Semiconverter & Full Converters using SCR for R-23 converters using SCR for R-24 converters using SCR for R-25 converters using SCR for	repetitive ratings of SCR, GTO, Power MOSFET & IGBT and their significance. Requirement of a typical triggering / driver (such as opto isolator) circuits for various power devices Importance of series and parallel operations of various power devices Importance of series and parallel operations of various power devices Importance of series and parallel operations of various power devices Importance of series and parallel operations of various power devices Importance of series and parallel operations of various power devices Interactive Method Interacti	repetitive ratings of SCR, GTO, Power MOSFET & IGBT and their significance. 14-02-2023 Requirement of a typical triggering / driver (such as opto isolator) circuits for yarious power devices Importance of series and parallel operations of various power devices 15-02-2023 Dunit II: AC to DC Power Converters: Concept of line & forced commutation Unit II: AC to DC Power Converters: Concept of line & forced commutation T1 (Ch 9), R3(Ch 6) T1 (Ch 9), R3(Ch 6) T1 (Ch 9), R4 Conventional method A. Conventional method B. Experiential learning A. Conventional method B. Experiential learning A. Conventional method B. Experiential learning A. Conventional method B. Experiential learning converters A. Conventional method B. Experiential learning converters of the province

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14	22-02-2023	Performance analysis and numerical on Semi & Full Converter	8-3-23	T1 (Ch 11)	A. Conventional method	NA	
		Effect of source inductance, Significance of power factor	8-3-23		A. Conventional method	Economic Improvement of Power Factor Correction: A Case Study Journal of Power and Energy Engineering > Vol.9 No.6, June 2021	
15	28-02-2023	and its improvement using PWM based techniques		R3 (Ch 5) + Notes	F. Interactive Method	https://www.scirp.org/journal/paper information.aspx?paperid=109717	
		Three phase Full converters	9-3-23		A. Conventional method	Experimenting the 3 Phase Converter https://www.youtube.com/watch?v	De l
16	01-03-2023	using SCR for R load and its performance analysis	3-5-65	T1(Ch.10)	F. Interactive Method	=xUZKgD1FeIM	Francis
		Single Phase PWM Rectifier	9-3.23		A. Conventional method	Design and Simulation of Three Phase Controlled Rectifier Using IGBT	
17	01-03-2023	using IGBT, Three Phase Controlled Rectifier Using IGBT		T1(Ch.4)	B. Experiential learning	https://www.ijaiem.org/Volume6Iss ue8/IJAIEM-2017-08-04-2.pdf	
18	07-03-2023	Difference between SCR based conventional rectifiers and IGBT based rectifiers.	13-3-23	T1(Ch.4) & R2 (Ch 12)	A. Conventional method G. Assessment of Competency based Education	NA	
19	07-03-2023	Unit III: DC to AC Power - Converters:		RE (CH 12)		NO.	,
20	8-03-2023	Single phase half and full bridge square wave inverter for R load using MOSFET / IGBT	(5-3-22 School	(Ch 5)	A. Conventional method F. Interactive Method	Direct Industry & Manufacturers: https://www.directindustry.com/ind ustrial-manufacturer/dc-dc-ac- inverter-84156.html	
		Single phase half and full bridge square wave inverter for R-L load using MOSFET	Lohegaon Pune 2843-23	ngines	A. Conventional method B. Experiential	Y Patil Scrip	
21	8-03-2023	/ IGBT Performance analysis and	23-3-23	T1(Ch 5)	learning	To The	
22	14-03-2023	numericals	23-3-25	T1(Ch 6)	DINCIDAL	NA Engineering	

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					A. Conventional method	Anti-cross conduction drive control circuit and method: Patent.	
23	15-03-2023	Cross conduction in inverter, need of voltage control and strategies in inverters	23-3-23	T1(Ch 6) + Notes	F. Interactive Method	https://patents.google.com/patent/U S7187226B2/en	
24	15-03-2023	Classifications of voltage control techniques, control of voltage using various PWM techniques and their advantages	12-4-23	T1(Ch 6)	A. Conventional method	NA	O mar
25	21-03-2023	Concept and need of harmonic elimination / reduction in inverters	12-4-23	T1(Ch 6)	A. Conventional method	NA	3
26	22-03-2023	Three Phase voltage source inverter for balanced star R load with 120 and 180 degree mode of operation	17-4-23	R6(Ch 8)	A. Conventional method F. Interactive Method	Article from circuitdigest https://circuitdigest.com/tutorial/three-phase-inverter-circuit-diagram- 120-degree-and-180-degree- conduction-mode	٥
27	22-03-2023	Device utilization factor, Advanced Converters like matrix inverter	18-4-23	T1(Ch 6), https://www .eaton.com/	A. Conventional method	NA	
		-	19-4-23.	-	A. Conventional method F. Interactive Method	Online Technical Journal: https://www.technicaljournalsonline.com/ijeat/VOL%20VII/IJAET%2 0VOL%20VII%20ISSUE%20I%20	
28	28-03-2023	Multi-level inverters and their topologies and his driver circuits Unit IV: Description Pune			G. Assessment of Competency based Education	JANUARY%20MARCH%202016/ 20167128.pdf	
29	29-03-2023	Classification of thompers, Step down chopper for R	19-4-23	STAYS DY Pa	Conventional	DC to DC Converter Market: Research & Methodology	

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		and RL load and its performance analysis		T1(Ch 5), R2 (Ch 13) R6(Ch 7)	F. Interactive Method	https://www.marketsandmarkets.co m/Market-Reports/global-de-de- converter-market-17565254.html	
20	20.02.2022	Step down chopper for RL load and its performance analysis	20-4-23	T1(Ch 5)	A. Conventional method B. Experiential learning	Industrial, Space Qualified, Military, Surface Mount DC to DC Converters https://www.everythingpe.com/scar ch/dc-dc- converters/filters?page=1&country =global&sindustry=;Military;	
30	29-03-2023	Step up chopper, various control strategies for	20-4-23	R6(Ch 7),	A. Conventional method F. Interactive	Types, Specifications & Costing: https://www.mouser.in/c/power/dc-dc-converters/ Control Strategies: https://www.tutorialspoint.com/power_electronics/power_electronics_control_methods.htm	Dama
31	04-04-2023	choppers	A CHARLETT	R1(Ch 11)	Method		
22	05-04-2023	Types of choppers (isolated and nonisolated) such as type A, B, C, D & E	24-4-23	R6(Ch 7), R1(Ch 13)	A. Conventional method	NA	
32	05-04-2023	Switch mode power supply (SMPS) viz buck, boost and buck-boost, Fly back, Half and full Bridge isolated	24-4-23	R1(Ch 13)	A. Conventional method B. Experiential learning	sdDesignSuite: https://eds.st.com/console/#/app/dc dc/ic_dcdc_catalogs/filter/%5B%5 D/grid	
	11-04-2023	Non-isolated interleaved bidirectional topologies	25-4-23	R1(Ch 13)	A. Conventional method	NA	
34	11-04-2023	Concept of integrated converter and design of LM3524 based choppers, concept of maximum power	25-4-23	T1(Ch 16), 3524 Data Sheet Application		Texas Instruments: https://www.ti.com/lit/ds/symlink/l m3524d.pdf MPPT: www.leonics.com https://www.leonics.com/support/ar ticle2_14j/articles2_14j_en.php#:~:	
35	12-04-2023		130	A die.	Lohegaon, Pune	text=module%20and%20battery-	

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					G. Assessment of Competency based Education	,What%20is%20MPPT%3F,(or%2 Opeak%20power%20voltage).	
36	12-04-2023	CR-V : Power Devices Protection and Circuits					6
		Over voltage, over current,	2-5-23		A. Conventional method	Power System Protection Schemes: https://www.allumiax.com/blog/pro- tection-schemes-for-electrical-	
36	18-04-2023	di/dt and dv/dt protection circuits and their design,	2-3-23	T1(Ch 17)	F. Interactive Method	power-system	
			2-5-23		A. Conventional method	Heat Sink Design Guide: https://www.fictiv.com/articles/heat -sink-design-guide Key Factors:	
37	19-04-2023	Various cooling techniques and heat sink design	2-3-23	T1(Ch 17)	F. Interactive Method	https://www.simscale.com/blog/key -factors-heat-sink-design/	
38		Resonant converters such as	7-5-23		A. Conventional method	Resonant converters for electric vehicle application:	7
	19-04-2023	Zero current switching (ZCS) and Zero voltage switching (ZVS)	(online)	T1(Ch 7)	F. Interactive Method	https://www.sciencedirect.com/s cience/article/pii/S23524847210 14517	Som
0.0		Electromagnetic interference	7-5-23-		A. Conventional method	The Difference:	

	26-04-2023	Various EMI and EMC standards, Importance of isolation transformer. CR-VI: Power Electronic Applications	7-5-23	T1(Ch 17), R2 (Ch 14)	A. Conventional method G. Assessment of Competency based Education	EMI and EMC standards, Testing, Benefits https://corpbiz.io/emi-emc- testing	
42	02-05-2023	AC Voltage Controller using IGBT & SCR, Fan Regulator,	9-5-23	T1(Ch 11), R6 (Ch 9)	A. Conventional method F. Interactive Method	Power-electronics-revolutionizing-the-world-s-future-energy-systems https://www.hitachienergy.com/in/e n/news/perspectives/2021/08/power -electronics-revolutionizing-the- world-s-future-energy-systems	
43	03-05-2023	Electronic Ballast, LED Lamp driver, DC motor drive for single phase separately excited dc motor	9-5-23	R2 (Ch 22), R3(Ch 16)	A. Conventional method F. Interactive Method	Electronic Ballast https://www.electrical4u.com/electronic-ballast/ LED Lamp Drivers: https://www.sunpower- uk.com/glossary/what-is-an-led- driver/	Sand
44	AQ Lone	BLE motor drive, Variable voltage & variable frequency three phase induction motor	11-5-23	T 1 (h 15),	A. Conventional method F. Interactive	https://www.infineon.com/cms/en/a pplications/solutions/motor-control- and-drives/high-power-bldc-pmsm- acim-motor-drive-up-to-200v/ Variable Frequency Drives: https://www.invertekdrives.com/su pport/iknow/vfd- fundamentals/what-is-a-variable- frequency-drive	
	03-05-2023	PRINCIPAL ADYPSOE, PUNE	Engineer Pure	RACCA 33)	Method		

45	09-05-2023	On-line and Off- line UPS, study of various selection criteria and performance parameters of batteries in battery operated power systems,	11-5-23	T1(Ch 13)	A. Conventional method B. Experiential learning	On-line and Off- line UPS https://www.electricaltechnology.or g/2020/05/difference-between- online-ups-offline-ups.html All about batteries: https://batteryuniversity.com/article s	
46	10-05-2023	battery charging models and modes for EVs, Architecture of EVs battery charger	14-5-23 (unline)	T1(Ch 13) + Notes	A. Conventional method F. Interactive Method	Battery charging models: https://www.researchgate.net/public ation/263207426_A_Simplified_Ch arging_Battery_Model_for_Electric Vehicles_Smart_Charging_Applic ations Designing Electric Vehicle Battery Charging Solutions: https://www.electronicsforu.com/electronics-projects/electronics-design-guides/electric-vehicle-battery-charging-solutions	
47		PFC stage circuit topologies with details of Full-bridge boost rectifier and Full-bridge interleaved for EV	14-5-23 (online)	Batteries for	A. Conventional method F. Interactive Method	Texas Instruments: https://www.ti.com/lit/an/slua896/sl ua896.pdf?ts=1677734427220&ref url=https%253A%252F%252Fww w.google.com%252F Journal of Power Electronics: https://jpels.org/digital- library/manuscript/file/17112/4_JP E-16-06-007.pdf	Ramas
48		case study of power PALicle and photograms of power PALicle and photograms GHarPUNE system	(online) (online) (online) (online) (online) (online) (online)	DC DC	A. Conventional method	Power Electronics for Electric Vehicle: https://www.idtechex.com/en/resear ch-report/power-electronics-for- electric-vehicles-2022-2032 Solar Power Electronics Devices: https://www.energy.gov/eere/solar/ solar-power-electronic-devices	

	F. Interactive Method	Advanced Power Electronics Design for Solar Applications: https://www.energy.gov/eere/solar/ advanced-power-electronics- design-solar-applications-power- electronics	a

Web References: https://nptel.ac.in/courses/108/105/108105066/ https://nptel.ac.in/courses/108/102/108102145/ https://nptel.ac.in/courses/108/107/108107128/

https://nptel.ac.in/courses/108/108/108108077/

https://batteryuniversity.com/

Books:

Text Books:

T1. M. H. Rashid, "Power Electronics Circuits Devices and Applications", PHI,4th Edition 2017 New Delhi.

T2. M. D. Singh and K. B. Khanchandani, "Power Electronics", TMH, 2 nd Edition 2006.

Reference Books:

R1. Bogdan M. Wilamowski, J. David Irwin "The Power Electronics and Motor Drives Handbook", CRC Press, 1 st Edition, 2011.; eBook: ISBN 9780429165627, 2019. ISBN 9780429165627, 2019.

R2. Muhammad H. Rashid, "Power Electronics Handbook", Academic Press, 2nd Edition, 2001

R3. Ned Mohan, T. Undeland & Robbins, Power Electronics Converters Applications and Design, John Willey & sons, Singapore, 2 nd Edition Oxford University Press, Wew Delhi 2015

R4. Ali Emadi Alireza Khaligh Zhang Joo Lee, "Integrated Power Electronic Converters and Digital Control", CRC Press, 1st E

R5. Vinod Kumar Khanna "Insulated Gate Bipolar Transistor IGBT Theory and Design", John Wiley & Sons, Illustrated Edition. Print ISBN:9780471238454; Online ISBN:97804717229164702184002/047172291.

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R6. L. Ashok Kumar, S. Albert Alexander and Madhuvanthani Rajendran, "Power Electronic Converters for Solar Photovoltaic Systems", Elsevier, 1 st Edition, 2020.

Traditional Teaching	A. Conventional method	Chalk board, PPT (LCD Projector), Smart Display
Student centric learning	B.Experiential learning	Experiential learning practices to improve creativity and cognitive levels of the students - Laboratory Sessions are conducted with content beyond syllabus experiments, Project development on latest technologies by students where they platform their working model in the technical fest, Industrial Visits to engage them in experiential learning while visiting the organization, the state of the plant training, Field work, Participation in technical events.
100	C. Participatory Learning	Students participate in different activities such as seminars, group discussions, wallpapers, projects, and skill-basedn, Salid-on courses. Annual cultural program, Regular Quizzes- Quizzes are organized for student participation at intra or inter-department levels., Seminar Presentation, Paper and Poster presentation.

D. Reflective learning	One minute paper (Surprise test) during classroom interaction, Open ended questions by teacher by students, Question Bowl taken around the class by teacher or one of students, Preparation of question bank by students at various cognitive levels
E.Think, pair, share activity	Think (Individually): Choose a topic that you are about to teach. Come up with one activity that you will implement in class that requires students to talk, write, reflect and express their thinking. (~3 min). Pair (with your neighbour): Examine your neighbour's activity. Does it ensure that majority of students are actively engaged - beyond listening, copying of notes? If not, what modifications do you suggest? (~5 min). Share (with everyone): Your topic and strategy. (~10 min).
F. Interactive Method	Motivating student participation in group discussion, role-play, subject quiz, news analysis, discussion, and questions and answers on current affairs
G. Assessment of Competency based Education	Conduction of quiz/test on the taught unit through Google form (MCQ type)

Note: Along with traditional teaching, you have to select any one or more from student centric method depending upon content of topic on that day. You have to design teaching plan so as to cover 70% Traditional teaching and 30% Student centric learning method.

** Compete	ency based Education (10 Minute)	Remark
1	Recent trends/application on a particular topic	Share any one of the information (Sr. No.1 to 8) through the website link or Google drive link on
2	Latest information on National & International updates on current topic	whatsapp with title about 5-10 minutes followed by discussion with students:
3	Minor, Major and Research project related to current topic	by discussion with students:-
4	Interesting things / Historical background	
5	Research Article or Magazine, Current affairs	
6	Website links	
7	PDFs, PPT, Miles links	
8	Virtual Tour et Cohegaon, g	

Active Learning Strategy / Problem based learning during Practical Hours

1 Learning by doing, discussion among per group and analysing case studies

- Small group projects, projects in community services
- Interdisciplinary and multidisciplinary projects related to real time problem lead to innovations

Student Centric Activity to be conducted by department

Student-centric methods such as Hackathons, Workshops, Seminars, Virtual Lab, Simulation, Roleplay, Review web literature, Video, Demonstration, Activity-based learning, Flipped Classroom, Guest lecture, Professional practice school, GD/ debate, Peer learning groups, MOOCs, Google Classroom, Project-based learning, Real-time case studies, Worksheets, PPT, Mind map, Journal Review, Proto-type model, Crosswords, Research projects, Language games, Viva, Poster presentation, Public Speaking to encourage Participative, Problem-solving and Experiential learning, Project and Field work

Mr. Riyaj Kazi . (Subject Teacher)

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^{*} Prepared by Dean (Academics) and approved in the Principal meeting on 27/01/2023.



AJEENKYA DY Patil School of Engineering, Pune -412 105

Department of E&TC Engineering

Class: TE

(Batch: A3)

Subject Name: PDC

Sem: II

Practical Plan

Form No. IQAC/12

Practical hours per Week: 02

Subject In-charge: Mr. Riyaj Kazi

Note:

1. Conduct experiments as per Syllabus and two additional experiments

Sr. No.	Expt. No.	Planned Date	Broad Topics To Be Covered	Name of Experiment	Actual Date of Conduction	Remark
1.	1	8-2-2023	Characteristics of Power Devices	SCR Characteristics	21-2-23	
2.	2	15-2-2023	Characteristics of Power Devices	MOSFET Characteristics	8-3-23	
3.	3	22-2-2023	Characteristics of Power Devices	IGBT Characteristics	08-3-23	Ne
4.	4	1-3-2023	AC-DC Converters	Single Phase AC to DC Converters	15-3-23	Lama
5.	5	8-3-2023	DC-DC Converters	Step Down Chopper	13-3-23	0
6.	6	15-3-2023	AC-AC Converter	AC Voltage Controller	12-4-23	
7.	7	22-3-2023	Power Suppla C Lohegaon, B Pune	Switched Mode Power Supply	28-4-23	

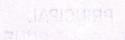
8.	8	29-3-2023	DC Motor	Speed Control of DC Motor	28-4-23
9.	9	05-4-2023	Battery: Types & maintenance	Study of Batteries for EV & its maintenance	
10.	10	12-4-2023	Solar Power Plant	Visit & Demonstration of Solar Power Plant	26-4-23



Subject In-charge (Mr. Riyaj Kazi)









AJEENKYA DY Patil School of Engineering, Pune -412 105

Department of E&TC Engineering

Class: TE

(Batch: A1)

Subject Name: PDC

Sem: II

Practical Plan

Form No. IQAC/12

Practical hours per Week: 02

Subject In-charge: Mr. Riyaj Kazi

Note:

1. Conduct experiments as per Syllabus and two additional experiments

Sr. No.	Expt. No.	Planned Date	Broad Topics To Be Covered	Name of Experiment	Actual Date of Conduction	Remark
1.	1	7-2-2023	Characteristics of Power Devices	SCR Characteristics	15-2-23	
2.	2	14-2-2023	Characteristics of Power Devices	MOSFET Characteristics	14-3-23	
3.	3	21-2-2023	Characteristics of Power Devices	IGBT Characteristics	21-3-23	
4.	4	28-2-2023	AC-DC Converters	Single Phase AC to DC Converters	11-4-23	PD
5.	5	7-3-2023	DC-DC Converters	Step Down Chopper	18-4-23	Alem
6.	6	14-3-2023	AC-AC Converter	AC Voltage Controller	18-4-23 A Patil Sc	
7.	7	21-3-2023	Power Supply	Switched Mode Power Supply	Coneggo]	

8.	8	28-3-2023	DC Motor	Speed Control of DC Motor	28-4-23
9.	9	04-4-2023	Battery: Types & maintenance	Study of Batteries for EV & its maintenance	
10.	10	11-4-2023	Solar Power Plant	Visit & Demonstration of Solar Power Plant !	26-4-23



Subject In-charge (Mr. Riyaj Kazi)









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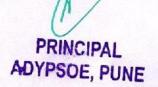
Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohgaon, Pune – 412 105

Department of E&TC Engineering

6

Subject Notes (Handwritten Notes, Presentations)







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Department of E&TC Engineering

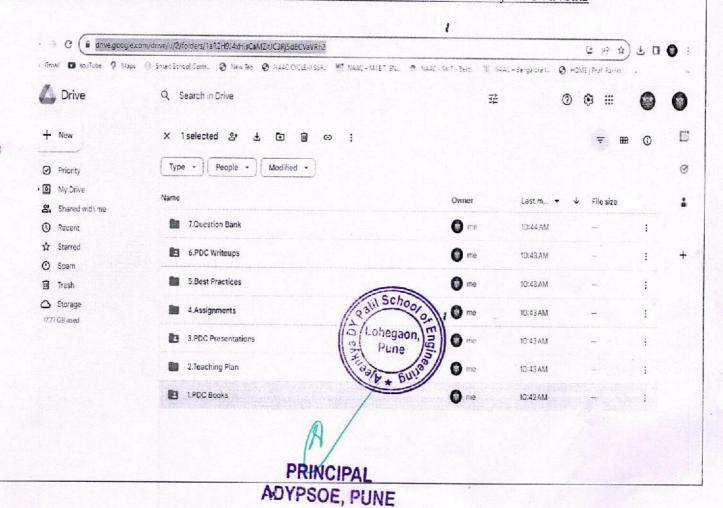
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Subject Notes (e-Notes, PPTs, PDFs)

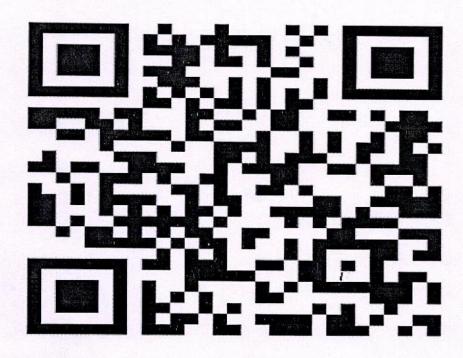
Google Drive Link:

https://drive.google.com/drive/u/2/folders/1aR2H9J4xHisCaMZltJC3Rj5dBCVaVRh2



https://sites.google.com/dypic.in/central-library/e-content

Scan QR Code to access e-contents



Click the respective department to view/download the subject wise study material developed by ADYPSOE





AJEENKYA DY Patil School of Engineering
Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohgaon, Pune – 412 105
Department of E&TC Engineering

Best Practices

1:



AJEENKYA DY PATIL SCHOOL OF ENGINEERING

Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune - 412 105.

Academic Year 2022-23

Form No. IQAC/44

Best Practice - I

Name of the faculty: - Mr. Rivey L. Kazi

Name of the Subject: Power Devices and Citruits.		
Department: ESTC		
Class: .T.E Div:A4.B	ir.	
Competency Based Learning (Part A and Part B)		
It is an active, adaptive, experiential and participatory learning where students' choice and are present in order to bring variety and variation in learning.	voice	
Note- Part A and B should cover latest developments, advancements, innovation technologies in the subject domain.	ns and	
{Select at least Three from each of Part-A & Part-B for every subject and map the outco	me of act	ivity}
Part A- Experiential Learning (Learning by Doing) (Reference: NAAC Manual Pg. 72)		
Is a process of learning through experience and is more specifically defined as "learning the reflection on doing".	nrough	
The following statements/ questions are related to the subject you teach in this semester		1
1. Is it possible to develop any prototype or model by the students related to the subject?	Yes	No
If Yes, mention the related UNIT No and Prototype Name		
2. Do you want to show any product demo to the students related to any topic / UNIT?	Yes	No
If Yes, mention the related UNIT No and Name of the Demo - A.C	Controlle	1
3. Do you want to assign any mini / major project to the students related to any topic?	Yes	No
If Yes, ment of the real of UNIT No and the Project Name		
4. Are you grant the man and industrial / Field / Laboratory visit?	Yes	No
Name of Marstry Field Vab visit - Visit to Solar Power Plant		
Call Street of Facility of Fac	Yes	No
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5.	Do you want to discuss a case studies related to the subject domain?	
	If Yes, mention the UNIT No and Title of the case study	
6.	Do you want to focus on any simulation/gaming based learning in the subject?	Yes No
	If Yes, mention the UNIT No and Name of the Simulation/Game	
7.	Do you want to assign any mentor from industry /company to the students so that they	
	understand the subject well?	Yes No
	If Yes, Name of the mentor and Industry name –	
8.	Is there any student club at department /college level related to the subject domain?	Yes No
	If Yes, Number of students to be enrolled to the club –	
9.	Do you want to conduct any workshop related to the subject domain?	es No
	If Yes, Name of the workshop No. of students enrolle	
10.	Do you want to invite any expert / guest from an industry related to the subject?	Yes No
	If Yes, Name of the expert-Mr. Ayaz Sayyad Name of the Topic - Power Dev	ices + Application
11.	Do you want to empower the students to join an ad-on- course related to the subject?	Yes No
	If Yes, Name of the Ad-on-courses available	
12.	Do you want to empower the students to join a skill and practical oriented courses related	
	the subject?	Yes No
	If Yes, Name of the course available	
	Do you want to show a few videos on advances, latest development and technology in	the
	domain or subject?	LYE'S No
	If Ves Number of Videos to be shown (maintain the assert)	
	If Yes, Number of Videos to be shown (maintain the record)- 6 videos. One on ec	
14.	Any course available for the students w.r.t the subject domain at different learning plat	forms
	like Udemy, Coursera, Skillshare, MasterClass, Edx, Udacity etc.	Yes No
	Name of the courses -4NPTEL Courses available 1) nptel. ac.in/courses/108/103 3) nptel. ac.in/courses/108/103	1108105066/
Par	t -B Participative Learning (Learning by participating) (Reference- NAAC Manual P	108107128/
	icipatory Learning and Action is a family of approaches, methods, attitudes, behaviou	
rela	tionships, which enable and empower people to share, analyze and enhance their know	vledge
of th	neir life and conditions, and to plan, act, monitor, evaluate and reflect.	
1.	Do you want to ask open ended questions in the domain?	es No
	UNIT No Question 1:	
	UNIT No Question 2:00	School
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Academic Year 2022-23

Form No. IQAC/45

Best Practice – II	
Bridging the Gap between Education and Employment through Industry Connect	
Name of the faculty: - Mr. Riyaj Kazi	
Name of the Subject: - Power Devices & Circuts Class: T.E. Div: AAB.	
Sector of the industry Identified: - Power Electronics	
Name of the industry to be connected:	
Plan of Action (PoA):- {Select at least five for each subject and map the outcome of activity}	
1. Do you want to sign Sign MoU with the industry? Yes No Tentative date to sign the	
MoU:/2023	
2. Are there any hot topics, areas, trends, advances, technologies in the industry sector? Yes No	
and and to Mention Names Power Electrics in Electric Vehicles.	
3. Do you want to identify issues, problems and challenges in the industry? Yes No	
List the issues.	1
4. Do you want to conduct the product survey of the industry with the help of students? Yes No	Bernan
B. D. was want to let the students to understand the wahrite of the industry? Yes No	
5. Do you want to let the students to understand the website of the industry? Yes No	
6. Do you want to arrange a physical visit to the industry? Yes No	
Acceptance and acceptance of the property of t	
7. Do you want to arrange a virtual visit of the industry? Yes No	
Books-stranslates among a function of the stranslates of the stranslat	
8. Do you want to arrange a guest legant school es No	
Title of the guest lecture - Lohegaon, Pune	
9. Is it possible to get sponsored project(s) from the industry for the final year students? Yes No	Bernand
School of E	
A Service Serv	
10. Is it possible to get internship for the SE/TE students from the like this try? Yes No	
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	*.
11. Do you want to write a conference paper/article on the best practices of the industry by	
Yes No	
12. Do you want to arrange a hands-on workshop for the subject in collaboration with the industry?	
Demonstration Yes No	
13. Is there any skill development course such live	
the course - pasts of lower steptanics	
NSDC/Skill India/Make in India for the subject? Yes No.	ters, s en]
Names of the courses –	
15. Do you want to motivate the students to participate in National/international employability tests	9
or exams in the subject? Yes No Name of the tests	0
reame of the tests	
Plan	
Sign of Subject Teacher:	
Sign of HoD H.O.D.	
Dept. of E. & T.C. Engl	neering
Dr. D.Y. Patil School of Engg.,	Lohegaon
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Dr. D. Y. Patil Group of Institutions' Technical Campus Dr. D. Y. PATIL SCHOOL OF ENGINEERING

Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune – 412 105.

Department of E&TC Engineering

Form No. IQAC/30

Date: 04/05/2023

Event Report

Academic Year: 2022-23

BEST PRACTICE- [PART-B) Semester-II

Name of the event: Peer Learning on Advanced Topic:- Electric Vehicle:- Battery Management System, Safety and Maintenance of batteries: Advanced Learner Activity.

Date and Time	Monday, 19/04/23, 10:30 AM and 03/05/2023, 11:30 AM
Event Venue	E&TC Department. Room No 232
Organized by	Department of E&TC
Targeted Audience	TE – A & B Students
Resource Person	TE Students: Ms. Chetna Patil, Mr. Hardik Chotalia, Ms. Vashnavi, Ms. Monali Jatti.

Contents of Peer Learning:

- 1] Introduction of Battery Management System
- 2] Block Diagram of BMS
- 3] Designing a BMS
- 4] The functions of a BMS suitable for a hybrid electric vehicle.
- 5] Characteristics of Rechargeable Batteries
- 6] Maintenance of batteries
- 7] Precautionary Note
- 8] Geo-Tagged Photos



INTRODUCTION:

Battery Management System is a component of a much more complex fast acting Energy Management System and must interface with other on board systems such as engine management, climate controls, communications and safety systems. To some it is simply Battery Monitoring, keeping a check on the key operational parameters during charging and discharging such as voltages and currents and the battery internal and ambient temperature. The monitoring circuits would normally provide inputs to protection devices which would generate alarms or disconnect the battery from the load or charger should any of the parameters become out of limits. For the power or plant engineer responsible for standby power who's battery is the last line of defense against a power blackout or a telecommunication's network outage BMS means Battery Management Systems. Such systems encompass not only the monitoring and protection of the battery but also methods for keeping it ready to deliver full power when called upon and methods for prolonging its life. This includes everything from controlling the charging regime to planned maintenance.

BLOCK DIAGRAM:

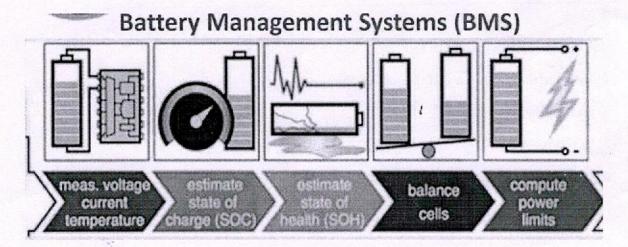


Fig. 1: General Block Diagram of BMS

BMS Building Blocks There are three main objectives common to all Battery Management Systems:

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Pune

1] Protect the cells or the batter of m dam

2] Prolong the life of the batter

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UNIT No Question 3:-		
Do you want to conduct group discussion/ Buzz sessions in the subject domain?	Yes	No
UNIT No Topic -		
UNIT No Topic		
Do you want to use a question bowl approach in the discussion?	Yes	No
Topic- Power Devices and Applications.		
Do you want to conduct news analysis with the help of student on any topic?	[Va.]	(No
Unit No Example	Yes	No
Do you want to focus on peer learning w.r.t. the subject? Unit No. V & VI Example Power Devices Protection & Application	Yes	No
Do you want to use survey methods with the help of student to understand any topi	ç 2	
Unit No Example	Yes	No
Do you want to use Brainstorming session to understand the topic better?		r
Unit No	Yes	No
Do you want to conduct a small scale research in the subject domain with the help	of	
students?	Yes	No
Unit No Topic		
Do you take any initiative with the help of the students to collect information relate	ed to a	
topic for better learning?	Yes	No
Unit No. VI Topic Batteries: Architecture/charging models	FOO EV	
and learning?	Yes	No
Unit No Incident		
. Do you want to use the approach of video viewing and discussion?	Yes	No
. Do you want to use any other participatory approach or method or game in teachin	g and	
learning?	Yes	No
Name of the approach method/game -		
PRINCIPAL Constant		
	Do you want to use a question bowl approach in the discussion? Topic - Do you want to use a question bowl approach in the discussion? Topic - Do you want to conduct news analysis with the help of student on any topic? Unit No. Example Do you want to focus on peer learning w.r.t. the subject? Unit No. Str. Example Power Devices Protection & Application Do you want to use survey methods with the help of student to understand any topic Unit No. Example Do you want to use Brainstorming session to understand the topic better? Unit No. Name of the Topic Do you want to conduct a small scale research in the subject domain with the help students? Unit No. Topic Do you take any initiative with the help of the students to collect information relate topic for better learning? Unit No. Topic Do you take any initiative with the help of the students to collect information relate topic for better learning? Unit No. Incident Do you want to use the approach of video viewing and discussion? Do you want to use any other participatory approach or method or game in teachin learning? Name of the approach of video viewing and discussion? PRINCIPAL PRINCIPAL	Do you want to conduct group discussion/ Buzz sessions in the subject domain? UNIT No Topic - UNIT No Topic - Top

INTRODUCTION:

Battery Management System is a component of a much more complex fast acting Energy Management System and must interface with other on board systems such as engine management, climate controls, communications and safety systems. To some it is simply Battery Monitoring, keeping a check on the key operational parameters during charging and discharging such as voltages and currents and the battery internal and ambient temperature. The monitoring circuits would normally provide inputs to protection devices which would generate alarms or disconnect the battery from the load or charger should any of the parameters become out of limits. For the power or plant engineer responsible for standby power who's battery is the last line of defense against a power blackout or a telecommunications network outage BMS means Battery Management Systems. Such systems encompass not only the monitoring and protection of the battery but also methods for keeping it ready to deliver full power when called upon and methods for prolonging its life. This includes everything from controlling the charging regime to planned maintenance.

BLOCK DIAGRAM:

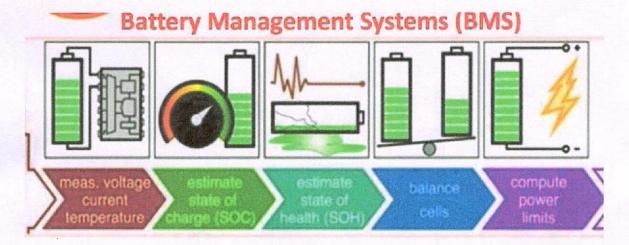


Fig. 1: General Block Diagram of BMS

BMS Building Blocks There are three main objectives common to all Battery Management Systems:

1] Protect the cells or the battery from damage

2] Prolong the life of the battery

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3] Maintain the battery in a state in which it can fulfill the functional requirements of the application for which it was specified.

Designing a BMS:



In order to control battery performance and safety it is necessary to understand what needs to be controlled and why it needs controlling. This requires an in depth understanding of the fundamental cell chemistries, performance characteristics and battery failure modes particularly Lithium battery failures.

BMS may incorporate one or more of the following functions Cell Protection Protecting the battery from out of tolerance operating conditions is fundamental to all BMS applications. In practice the BMS must provide full cell protection to cover almost any eventuality. Operating a battery outside of its specified design limits will inevitably lead to failure of the battery. Apart from the inconvenience, the cost of replacing the battery can be prohibitive. This is particularly true for high voltage and high power automotive batteries which must operate in hostile environments and which at the same time are subject to abuse by the user. Charge control This is an essential feature of BMS. More batteries are damaged by inappropriate charging than by any other cause. Demand Management While not directly related to the operation of the battery itself, demand management refers to the application in which the battery is used. Its objective is to minimize the current drain on the battery by designing power saving techniques into the applications circuitry and thus prolong the time between battery charges. SOC Determination

Many applications require a knowledge of the State of Charge (SOC) of the battery or of the individual cells in the battery chain. This may simply be for providing the user with an indication of the capacity left in the battery, or it could be needed in a control circuit to ensure optimum control of the charging process. SOH Determination The State of Health (SOH) is a measure of a battery's capability to deliver its specified output. This is vital for assessing the readiness of emergency power equipment and is an indicator of whether maintenance actions are needed. Cell Balancing In multi-cell battery chains small differences between cells due to production tolerances or operating conditions tend to be magnified with each charge / discharge cycle. Weaker cells become overstressed during charging causing them to become even weaker, until they ever the state of the battery. Cell balancing is a way of

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- 9] Providing information on the State of Health (SOH) of the battery. This measurement gives an indication of the condition of a used battery relative to a new battery.
- 10] Providing information for driver displays and alarms
- 11]Predicting the range possible with the remaining charge in the battery (Only EVs require this)
- 12]Accepting and implementing control instructions from related vehicle systems
- 13]Providing the optimum charging algorithm for charging the cells
- 14] Providing pre-charging to allow load impedance testing before switch on and two stage charging to limit inrush currents
- 15]Providing means of access for charging individual cells
- 16]Responding to changes in the vehicle operating mode
- 17]Recording battery usage and abuse. (The frequency, magnitude and duration of out of tolerance conditions) Known as the Log Book function
- 18]Emergency "Limp Home Mode" in case of cell failure.

In practical systems the BMS can thus incorporate more vehicle functions than simply managing the battery. It can determine the vehicle's desired operating mode, whether it is accelerating, braking, idling or stopped, and implement the associated electrical power management actions. Cell Protection One of the prime functions of the Battery Management System is to provide the necessary monitoring and control to protect the cells from out of tolerance ambient or operating conditions. This is of particular importance in automotive applications because of the harsh working environment. As well as individual cell protection the automotive system must be designed to respond to external fault conditions by isolating the battery as well as addressing the cause of the fault. For example cooling fans can be turned on if the battery overheats. If the overheating becomes excessive then the battery can be disconnected. Battery State of Charge (SOC) Determining the State of Charge (SOC) of the battery is the second major function of the BMS. The SOC is needed not just for providing the Fuel Gauge indication.

The BMS monitors and calculates the SOC of each individual cell in the battery to check for uniform charge in all of the cells in order to verify that individual cells do not become overstressed. The SOC indication is also used to determine the end of the charging and discharging cycles. Over-charging and over-discharging are two of the prime causes of battery failure and the BMS must maintain the cells within the desired DOD operating limits. Hybrid vehicle batteries require both high power charge capabilities for regenerative braking and high power tissing capabilities for launch assist or boost. For this reason, their batteries must be

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maintained at a SOC that can discharge the required power but still have enough headroom to accept the necessary regenerative power.

Characteristics of rechargeable batteries:

- A cell is an electro-chemical device capable of supplying the energy that results from an internal chemical reaction to an external electric circuit.
- A battery is composed of one or more cells, either parallel or series connected to obtain required current/voltage capability (batteries comprised of series connected cells are by far the most common).
- ESR (Equivalent Series Resistance) is the internal resistance present in any cell that
- limits the amount of peak current it can deliver.
- The Amp-hour capacity of a battery (or cell) is its most important figure of merit: it is defined as the amount of current that a battery can deliver for 1 hour before the battery voltage reaches the end-of-life point.
- The "c" rate is a current that is numerically equal to the A-hr rating of the cell. Charge and discharge currents are typically expressed in fractions or multiples of the c rate.
- The MPV (mid-point voltage) is the nominal voltage of the cell, and is the voltage that is measured when the battery has discharged 50% of its total energy.
- The measured cell voltage at the end of its operating life is called the EODV, which stands for End of Discharge Voltage (some manufacturers refer to this as EOL or End of Life voltage).
- The gravimetric energy density of a battery is a measure of how much energy a battery contains in comparison to its weight.
- The volumetric energy density of a battery is a measure of how much energy a battery contains in comparison to its volume.
- A constant-voltage charger is a circuit that recharges a battery by sourcing only enough current to force the battery voltage to a fixed value.

• A constant-current charger is a circuit that charges a battery by sourcing a fixed current into the battery, regardless of battery voltage.

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Maintenance of batteries:

Battery service and maintenance are critical to UPS reliability. A gradual decrease in battery life can be monitored and evaluated through voltage checks, load testing or monitoring. Periodic preventive maintenance extends battery string life by preventing loose connections, removing corrosion and identifying bad batteries before they can affect the rest of the string.

What can go wrong with batteries?

Condition	Cause
Plate separation	Repeated cycling (charging and discharging), damage during handling and shipping, and overcharging
Grid corresion	Normal aging, operating in an acidic environment and high temperatures
Internal short circuit	Heat (plates expand causing shorts), separator failure, handling and shipping, and grid comosion
External short circuit	Human error (shorting terminals) and leaks,
Sulfation of plates	Sitting discharged for an extended period, not on charge or being undercharged, such as battery shell life being exceeded past manufacturer's guidelines
Excessive gassing	Often due to high temperatures or overcharging, electrolyte volume is decreased
Drying out	Excessive gassing, high temperatures or overcharging, resulting in too little electrolyte for battery to function and provide full backup time

Precautionary Note:

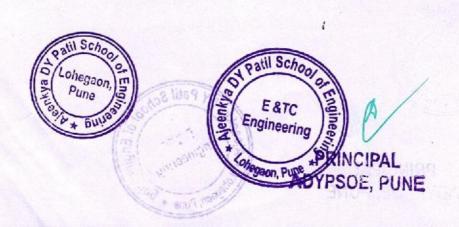
- Do not charge by higher current or higher voltage than specified. Doing so may generate gas inside the battery, resulting in swelling, fire, heat generation or bursting.
- Do not heat, disassemble, nor dispose of in fire. Doing so damages the insulation materials and may cause fire, heat generation, leakage or bursting.
- Do not solder directly to the battery. If soldering is performed directly to the battery, the battery is heated up, consequently causing leakage, explosion or fire due to overheating from internal short-circuit.
- Do not short. If the (+) and (-) come into contact with metal materials, short-circuit occurs. As a result, fire deat generation, leakage or bursting may occur.

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- Keep batteries out of the reach of children. It is dangerous if children swallow the battery. Keep batteries which are considered swallow able out of the reach of children. When designing mechanical hardware around the battery, make sure that the battery cannot be removed by children. Swallowing may lead to burns, perforation of soft tissue, and death. Severe burns can occur within 2 hours of ingestion. In case of ingestion of a battery, seek medical attention immediately.
- Do not reverse placement of (+) and (-). If the (+) and (-) side of the battery is reverse inserted, it may cause a short-circuit or over discharge of the battery on some equipment and it may induce overheating, explosion or fire.
- Do not discharge by force. If the battery is discharged by direct connection to an external power supply etc., voltage of the battery will decline lower than 0 volt (electrical reversal) and will cause the battery case to expand, overheat, leak, explode or burn.
- In case of leakage or a strange smell, keep away from fire to prevent ignition of any leaked electrolyte.
- Do not weld terminals to the battery. The heat by welding may cause fire, heat generation, leakage or bursting. We weld standard terminals under strictly controlled conditions. If you need to weld terminals to the battery, please consult us in advance.
- In case of disposal, insulate between (+) and (-) of battery by an insulating material. Jumbling batteries or with other metal materials cause short-circuit. As a result, fire, heat generation, leakage or bursting may occur.

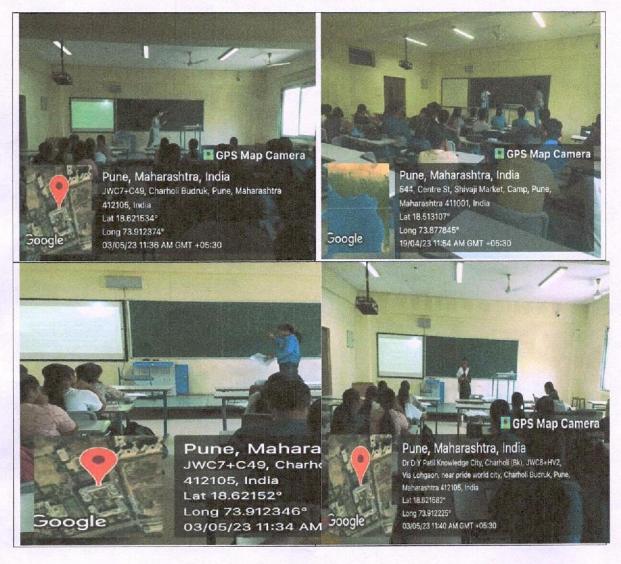
For More Details:

https://batteryuniversity.com/



Geo-Tagged Photos:

Advanced Topic Discussion among students of different learning Level



Students peer learning activity on Electric Vehicle: BMS, Safety and Maintenance.

Departmental Event Coordinator:



"Empowerment through quality technical education" AJEENKYA DY PATIL SCHOOL OF ENGINEERING

Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune – 412 105.

Department of E&TC Engineering

Form No. IQAC/30

Date: 26/04/2023

Event Report

Academic Year: 2022-23 BEST PRACTICE-I (PART-A) Semester-II

Name of the event: Educational Visit to Solar Power Plant

Date and Time	Wednesday, 26/04/23, 10:15 AM
Event Venue	Dr. D Y Patil Knowlegde City
Organized by	Department of E&TC
Targeted Audience	TE Students & Staff
Resource Person	Mr. Riyaj Kazi

Contents:

- 1. Introduction to Solar Power Plant
- 2. Observations on Solar power Plant
- 3. Details of daily power generation and utilization
- 4. Geotagged Photos

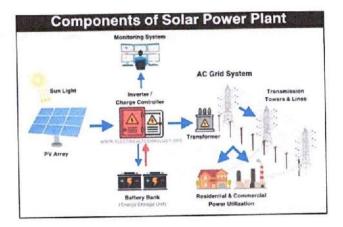


INTRODUCTION:

A solar power plant is based on the conversion of sunlight into electricity, either directly using photovoltaics (PV), or indirectly using concentrated solar power (CSP).

Solar energy is the radiation from the sun capable of producing heat, causing chemical reactions, or generating electricity. The total amount of solar energy incident on Earth is vastly in excess of the world's current and anticipated energy requirements. If suitably harnessed, this highly diffused source has the potential to satisfy all future energy needs. In the 21st century, solar energy is expected to become increasingly attractive as a renewable energy source because of its inexhaustible supply and its nonpolluting character, in stark contrast to the finite fossil fuels coal, petroleum, and natural gas. The main concern of a solar power plant is to provide complete energy independence while also lowering electricity costs.

BLOCK DIAGRAM:



PRINCIPLE:

The working principle is that we use the energy of photons to get the drift current flowing in the circuit using reversed bias p-n junction diode (p-type and n-type silicon combination). Thus, solar energy is converted to electrical energy by photovoltaic cells.

CONSTITUENTS:

1. Solar Panels

It is the near of the solar power plant. Solar panels consists a number of solar cells. One panel consists of about 35 solar cells.

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2. Solar Cells (Photovoltaic cells)

It is the energy generating unit, made up of p-type and n-type silicon semiconductor that converts the solar energy into electrical energy.

3.. D.C. to A.C. Converter (Inverter)

Solar panels produce direct current which is required to be converted into alternating current to be supplied to homes or power grid.

4. Battery

Batteries are used to produce the power bank or store the excess energy produced during day, to be supplied during night. It is optional and sometimes not preferred due to its high maintenance costs.

5. Transformer

A solar panel transformer has to convert the DC voltage coming out of the photovoltaic systems and step it up to the rated output. Sometimes it is integrated with the inverter.

6. Monitoring system

The system uses data logger and similar other applications for an effective surveillance of power production and consumption.

7. A circuit breaker

A circuit breaker is an electrical switch designed to protect an electrical circuit from damage caused by overcurrent/overload or short circuit. Its basic function is to interrupt current flow after protective relays detect a fault.

SOLAR POWER PLANT FUNCTIONING:

A batch of third year students of Electronics and Telecommunications of Dr. Ajeenkya DY Patil School of Engineering with their pedagogue Mr. Riyaj Kazi commenced an educational visit to the solar power plant of the anniversity for an explicit evaluation of the mechanism of the solar cells and an insightful comprehension of their consequential role towards sustainable development. The trip made a headway

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with 72 students and halted at the Solar Power plant in the parking sector wherein our mentor Mr Amol Sawant began a comprehensive elucidation of the solar panels.

The illustration covered the 8 unit solar power plant in the Technical Campus that accounts for about 630 kW of power generation per day enabling the university towards a self-reliance and self-sufficiency in perpetuating the cycle of power production and consumption. The project had initiated under the Kusum Yojna Scheme of the Government of India. The output of each panel is DC voltage which is combined together and further supplied to a 60 kW inverter that converts DC into AC. The solar plant is connected in series oriented in the North - South to get maximum solar energy. The efficiency can further be enhanced by removing the dust layer on the panel surface. The same is durable enough not to be damaged easily.

The solar power plant system in the campus doesn't use a storage battery on account of a heavy expenditure on its maintenance and replacement once in every 5 - 15 years of time slot. The power is recorded and tracked online via a data logger that keeps a précised record of everyday production and consumption of power. The online monitoring system also detects any malfunctioning of the system. The institute has the UPS system, which is an uninterrupted supply of energy. Solar panels are placed at the roof of the buildings of engineering schools, hostels and parking areas. The solar water heater is also placed on the hostel's roof.

DETAILS OF DAILY POWER GENERATION AND UTILIZATION:

The campus accounts for about 630 kW of power generation per day enabling the university towards a self-reliance and self-sufficiency in terms of energy conversation. The same saves about 6-8 lakhs of electricity bill per month. The power generated doesn't only compete with the exigencies of the campus but also produces an adequate amount of supplemental power that is further supplied to MSEB.

SOLAR POWER PLANT DOCUMENTATION:

The installation of the solar power panel was a joint venture of Renew Power Private Limited and Dr. DY Patil Group of Institutions with Solitis Electrical Solutions PVT LTD as the Electrical Contractor. The Company for the initial cost of installation and infrastructure signing an agreement for a power supply to the coming 15 years.

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However the project emerges as a boon for the university in terms of self-reliance and energy sufficiency saving about 6 - 8 lakhs of electricity bill on monthly basis.

Geotagged photos



Pune, Maharashtra, India
JWC7+C49, Charholi Budruk, Pune, Maharashtra
412105, India
Lat 18.621658*
Long 73.912447°
26/04/23 11:09 AM GMT +05:30

Visit to Solar Power Plant, DYPSOE



Visit to Control Room



Roofs of the buildings of Engineering Schools and Hostels





Gist of lessons ascertained during the educational visit:

- 1) We evaluated the structure and mechanism of the solar panels.
- 2) The visit also envisaged their consequential role towards the sustainable development by switching towards renewable and replenishable sources of energy in lieu of the conventional sources.
- 3) We analyzed the implementation, cost, maintenance and surveillance factors profoundly and were able to spot the advantages of being self reliant in power generation by the installation of solar panels.
- 4) The visit also helped us understand the joint venture between the corporate sector and the university for the installation of the solar power plant.

Name and Sign of Coordinator (Faculty): Mr. Riyaj Kazi

Departmental Event Coordinator:

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Lohegaon, Pune

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Discussion:- Components of Solar Power Plant: Capacity, Specifications & Technology





DC to AC Conversion-Inverter (60 KW)

Data Logger



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Control Panel and monitoring system

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Dr. D. Y. Patil Group of Institutions' Technical Campus Ajeenkya DY PATIL SCHOOL OF ENGINEERING

Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune – 412 105.

Department of E&TC Engineering

Form No. IQAC/30 Date: 10/05/2023

Event Report

Academic Year: 2022-23 BEST PRACTICE-I (PART-B) Semester-II

Title of best practice: Competency-based Education & Assessment: Activity:- Participative Learning (Question bowl approach in the discussion)

Date and Time Wednesday, 10/05/23, 10:30 AM	
Event Venue	E&TC Department. Room No 232
Organized by	Department of E&TC
Targeted Audience	TE – A & B Students
Resource Person	TE Students: Ms. Chetna Patil, Mr. Hardik Chotalia, Ms. Vashnavi, Ms. Monali Jatti.

Topics:

1] Study of Power Devices

2] AC to DC Converter

3] DC to AC Converter

4] DC to DC Converters

5] Applications of Power Electronics



In order to boost participative learning among students, one of the methods is to have a question bowl approach in the discussion.

The engagement of students to carry out this activity is essential because the students themselves learn different topics of the subject. They decide the questions to be asked along with solutions.

This activity leads to team building, group discussion, communication skills, and coordination. Participants get an opportunity to perform. This activity ensures edutainment.

In order to execute this activity; three coordinators among students were finalized as follows:

- 1] Ms. Chetana Patil
- 2] Tejas Jadhav
- 3] Choutewar Vaishnavi & Team

Entire class was divided into four teams:

- A] Team-1: Half Circuit
- B] Team-2: Short Circuit
- C] Team-3: Full Circuit
- D] Team-4: Open Circuit

The three team coordinators have prepared questions & answers and get it verified from faculty Mr. Riyaj Kazi.

Students prepared the question bowl & 40 Question chits.

The entire event is coordinated by the students.

The winners of this quiz are declared by Dr. Sharan Inamdar, HoD-E&TC.

Winner Team: FULL CIRCUIT (34 Marks)

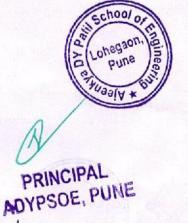
Runner Up: OPEN CIRCUIT (32 Marks)

Enclosed:

- 1) Team Details
- 2) Attendance

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Geo- Tagged Photos:



Edutainment through question bowl approach in discussion & learning

Name and Sign of Coordinator (Faculty): Mr. Riyaj Kazi

Departmental Event Coordinator:

Lohegaon

HOD: Gramal **E&TC** Engineering Ajeenkya DY Patil School of Engineering

Lohegaon, Pune

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Geo- Tagged Photos:



Edutainment through question bowl approach in discussion & learning

Name and Sign of Coordinator (Faculty): Mr. Riyaj Kazi

Departmental Event Coordinator:

E &TC

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Ajeenkya DY Patil School of Engineering, Lohegaon, Pune HoD:___

Lohegaon,

Pune

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Dr. D. Y. Patil Group of Institutions' Technical Campus Ajeenkya DY PATIL SCHOOL OF ENGINEERING

Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune – 412 105.

Department of E&TC Engineering

Form No. IQAC/30

Date: 13/05/2023

Event Report

Academic Year: 2022-23

Semester-II

Title of best practice II (2): Bridging the Gap between Education and Employment through Industry Connect

Activity:- Power Electronics in Electric Vehicle

Date and Time	Wednesday, 13/05/2023, 11:00 AM
Event Venue	E&TC Department. Room No 232
Organized by	Department of E&TC
Targeted Audience	TE – A & B Students
Resource Person	Mr.Riyaj Kazi

Topic: Role of Power Electronics in EV Technology

- 1] Electric Vehicle History & Advantages.
- 2] Components of Electric Vehicle
- 3] Types & Benefits of EV
- 4] Comparison of EVs
- 5] Configurations of EVs
- 6] Impact of EV on Grid: Negative & Positive
- 7] Vehicle to Grid Technology

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What are Electric Vehicles

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An Electric Vehicle is a vehicle that operates on an electric motor, instead of an internal combustion engine that generates power by burning a mix of fuel and gases. Electric vehicle is seen as a possible replacement for the current-generation automobile in near future to address environmental challenges.



Inspired by road going automobiles powered by electricity.



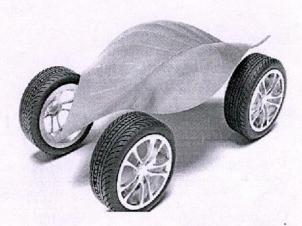
Propelled by one electric motor or more using batteries.



Electric motors give instant torque, and smooth acceleration.

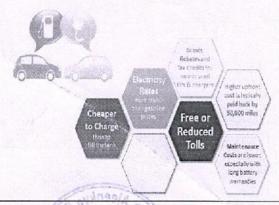


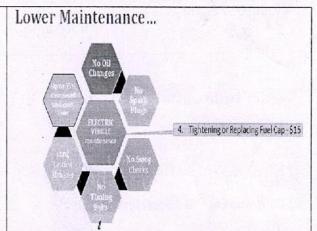
Plug-in electric vehicles (EVs) are fast, fun and efficient. Maintenance is simpler and cheaper.



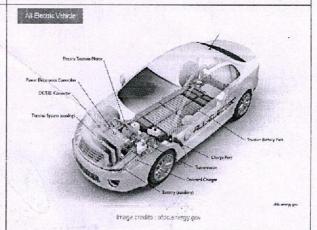
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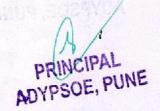












Components of Electric Vehicle

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Electric vehicles consists of an electric motor that is powered by a battery pack. The main advantage of electric vehicles is that they emit zero emissions and are eco-friendly. They also do not consume any fossil fuels, hence use a sustainable form of energy for powering the car. The main components of electric vehicles are:

Traction battery pack

DC-DC Converter

Electric motor

Power inverter

Charge Port

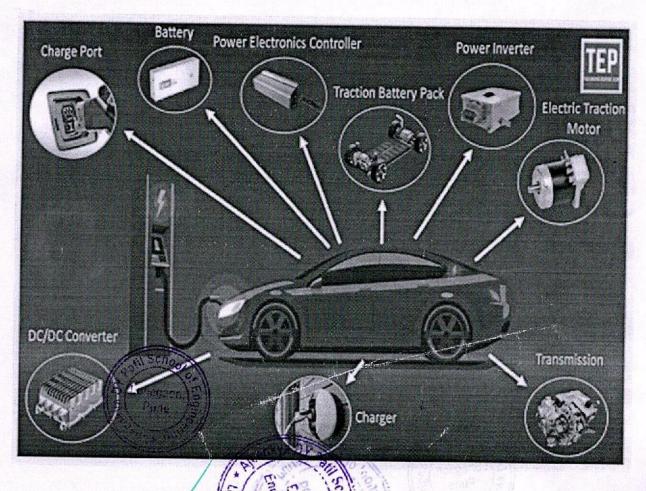
Onboard charger

Controller

Auxiliary batteries

Thermal system (cooling)

Transmission



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Types of EVs

There are four types of electric vehicles (EVs) available:

Battery Electric Vehicle (BEV): Fully powered by electricity. These are the more efficient compared to hybrid and plug-in hybrids. Hybrid Electric Vehicle:

Hybrid Electric Vehicle (HEV): The vehicle uses both, the internal combustion (usually petrol) engine, and the battery powered motor powertrain. The petrol engine is used both to drive and charge when the battery is drained. These vehicles are not as efficient as fully electric or plug in hybrid vehicles.

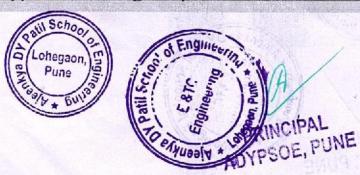
Plug-in Hybrid Electric Vehicle (PHEV): Uses both an internal combustion engine and a battery charged from an external socket (they have a plug). This means the vehicle's battery can be charged with electricity rather than the engine. PHEVs are more efficient than HEVs but less efficient than BEVs.

Fuel Cell Electric Vehicle (FCEV): Electrical energy is produced from fuel cell stacks which converts hydrogen to electricity.

Benefits of EV

- 1] Low Running Cost
- 2] Low Maintenance Cost
- 3] Zero Tailpipe Emission
- 4] Tax & Financial Benefits
- 5] Petrol & Diesel use is destroying our planet
- 6] Electric Vehicles are easy to drive & quiet
- 7] Convenience of Charging at home
- 8] No Noise Pollution

Source: https://e-amrit.niti.gov.in/benefits-of-electric-vehicles



Comparison of Electric Vehicles

COMPARISON

GQ. 5.7.1 Distinguish between the various types of Electric Vehicle.

Sr. No.	Component	(Electric Vehicle)	(Hybrid Electric Vehicle)	(Plug-in Hybrid Vehicle)	(Mild Hybrid Vehicle)	
1.	IC engine	Not Required	Required	Required !	Required	
2.	Electric Not Required Required Required		Required			
3,	Character in required the batterie		The batteries get charged by the engine	The batteries can be charged from an external source (plug)	Turns off the engine and switches to motor when coasting, braking and restarting quickly.	
4.	Battery Size Large upto Medium Medium upto 6-12 kWh upto6-12 kWh kWh.		Medium upto 6-12 kWh.	Cannot be solely driven or electric motor		
5.	Example	Tesla Model S	Honda Civic Hybrid	BMW i-8	Chevrolet Silverado Hylvid	

Configurations of EV

Based on drive arrangements

- 1] EV with a Clutch
- 2] EV without a Clutch
- 3] Single Motor Drive
- 4] Multiple Motor Drive
- 5] In wheel Drive
- 6] In wheel wireless drive
- 7] Fixed Gearing Transmission
- 8] Variable Gearing Transmission

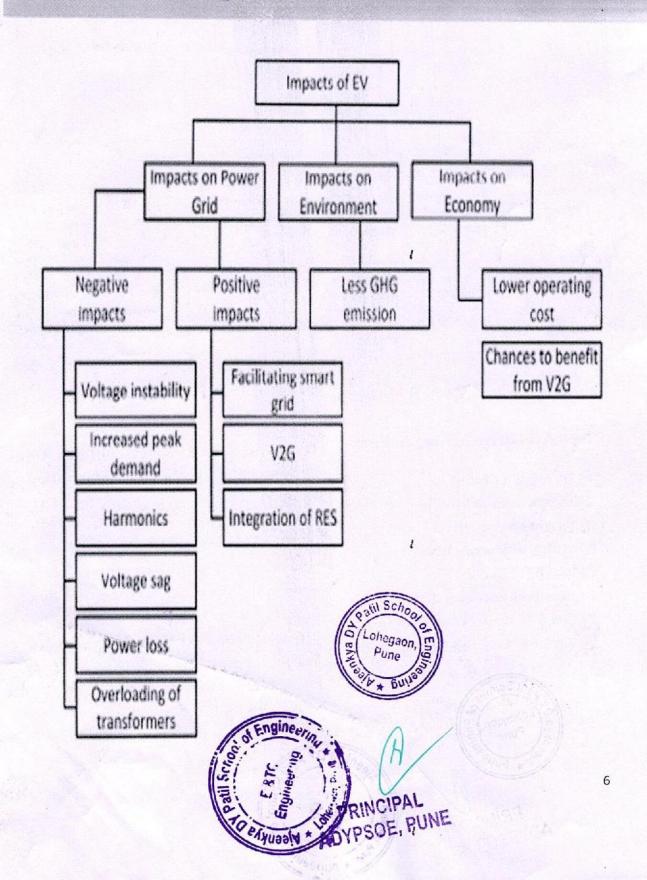
Based on power source configurations

- 1] Simple Battery Power Configuration (Battery connected to motor through a power converter)
- 2] Two Batteries and Power Converter: One battery is optimized for high specific energy and the other for high specific power.
- 3] Fuel Cell based power source
- 4] Hydrogen (Generated on board using liquid fuels such as methanol)
- 5] Battery & Super capacitor combination



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Impact of EV on Grid



Positive Impact

Smart grid: In the smart grid system, intelligent communication and decision making is incorporated with the grid architecture. In such a system, the much coveted coordinated charging is easily achievable as interaction with the grid-system becomes very much convenient even from the user end. The interaction of Essand smart grid can facilitate opportunities like V2G and better integration of renewable energy.

V2G: V2G or vehicle to grid is a method where the EV can provide power to the grid. In this system, the vehicles act as loads when they are drawing energy, and then can become dynamic energy storages by feeding back the energy to the grid. In coordinated charging, the EV loads are applied in the valley points of the load curve, in V2G, EVs can act as power sources to provide during peak hours. V2G is realizable with the smart grid

Integration of renewable energy sources: Renewable energy usage becomes more promising with EVs integrated into the picture. EV owners can use RES to generate power locally to charge their EVs Parking lot roofs have high potential for the placement of PV panels which can charge the vehicles parked underneath as well assupplying the grid in case of excess generation thus serving the increase of commercial RESdeployment.

Negative Impact

Voltage instability: EV loads have nonlinear characteristics, which are different than the general industrial or domestic loads, and draw large quantities power in a short time period, their power consumptions stay unpredictable; addition of a lot of Essat at time therefore can lead to violation of distribution constraints. To anticipate these loads properly, appropriate modeling methods are required.

Harmonics: The EV charger characteristics, being nonlinear, gives raise high frequency components of current and voltage, known as harmonics. Harmonics distort the voltage and current waveforms, thus, can reduce the power quality.

Voltage sag: A decrease in the RMS value of voltage for half a cycle or 1 min is denoted as voltage sag. It can be caused by overload or during the starting of electric machines, with an EV charger and a power-converter in stated 20% EV penetration can exceed the voltage sag firnit

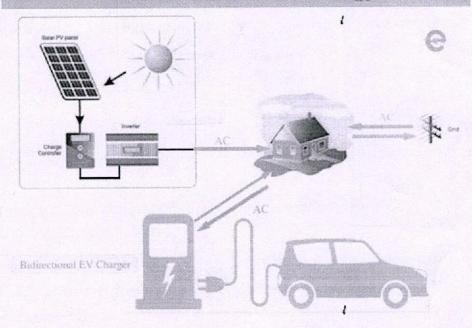
Power loss: The extra loss of power caused by EV charging can be formulated as: PLE = PLEV - PL original

Power quality degradation: The increased amount of harmonics and imbalance in voltage will degrade the power quality in case of massive scale EV penetration to the grid.

Overloading of transformers: EV charging directly affects the distribution transformers. The extra heat generated by EV loads can lead to increased aging rate of the transformers, but it also depends on the ambient temperature.



Vehicle to Grid Technology



Vehicle to Grid Technology

- Vehicle to grid' technology, also referred to as 'V2G' enables energy stored in electric vehicles to be fed back into the national electricity network (or 'grid') to help supply energy at times of peakdemand.
- This game-changing tech is about far more than potentially making EV owners money, it also plays an important part in helping to 'balance' the national electricity network
- Each vehicle must have following required elements for V2G
 - 1] Precision metering on-board the vehicle
 - 2] It would require a network of public charging stations capable of bidirectional power transfer, each station incorporating an inverter with precisely controlled voltage and frequency output to feed the energy back into the grid.
 - 3] It would also require the support of a massive communications network to manage the distributed power flows, the billing and feed-in buy back transactions.



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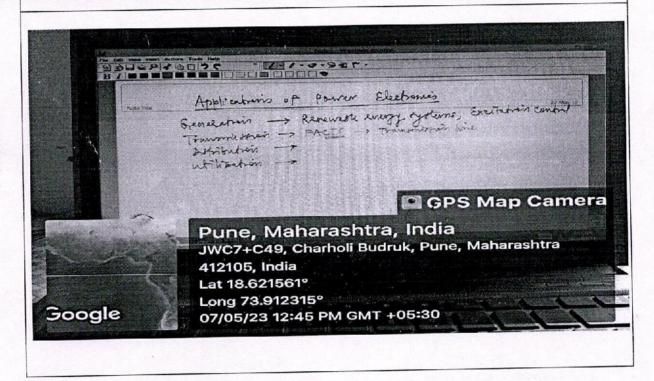
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Name and Sign of Coordinator (Faculty): Mr. Riyaj Kazi

Departmental Event Coordinator:	HoD:
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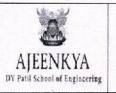
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8

Additional Topics Covered (other than syllabus)



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9

University Question Papers & Model Answer Sheets

Total No. of Questions: 4] SEAT No. : PA-10059 [Total No. of Pages : 2 [6009] 346 CEGP019280 T.E. (E &TC Engineering) (Insem) POWER DEVICES & CIRCUITS (2019/Patterff) (Semester-II) (304194) Time: 1 Hour! IMax. Marks: 30 Instructions to the ondidates 1) Answer Q.1 for Q.2, Q.3 or Q.4. Neat diagrams and waveforms must be drawn wherever necessary. Figures to the right side indicate full marks. Use of nonprogrammable calculator is allowed. Assume Suitable data if necessary. With the help of construction diagram, Explain the working of SCR. Why it is called as controlled rectifier. b) Explain with neat diagram the turn on and turn off characteristics of SCR. Write down equation for turn on and turn off time. [7] Explain with neat diagram the working of power MOSFET. Draw steady (22) a) state characteristics of Kand explain same. Explain isolated gate drive circuit for MOSFET and explain its operation. Explain operation of single phase full converter for R load with neat (23) a) circuit diagram and relevant waveforms. A single phase half controlled bridge rectifier supplies a ripple free load current of 10 A and operates from the 110V, 60Hz main. If the average o/p voltage is 75V,

Calculate:

i) Firing angle

ii) RMS o/p voltage

iii) RMS supply current

RMS 7th Harmonic supply current

OR

ADYPSOE, PUNE

of maximum possible output voltage. Calculate: rms and average currents (Technical Pg2-81 Ex 2.8.3)

PRINCIPAL

Draw the circuit diagram of three phase fully controlled converter with R load. Draw load current and load voltage waveforms with \approx -60° and 90°.

208 V, 60 Hz supply with Wload of 10 ohm. It is required to obtain 50%

b) A three phase full converter operated from three phase star connected

P.T.O.

[7]

Total No	No. of Questions : 4] SEAT No. :	
PA-10	0059 [Total No. of Pages : 2	
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	POWER DEVICES & CIRCUITS	
	(2019 Patterff) (Semester-II) (304194)	
	Hour] [Max. Marks: 30 tions to the capitalates.] Answer (1) or Q.2, Q.3 or Q.4. Neat suggrams and waveforms must be drawn wherever necessary.	
Time: 1	1 Hour] (Max. Marks: 30	
Instruct	tions to the candidates	
1)	Answer Q. Dor Q.2, Q.3 or Q.4.	
2)	Neat digrams and waveforms must be drawn wherever necessary.	
3)		
4)	Use of nonprogrammable calculator is allowed.	
59	Assume Shitable data if necessary.	
Q1) a)	With the help of construction diagram, Explain the working of SCR.	
~ , ,	Why it is called as controlled recyclier.	
b)		
	SCR. Write down equation for turn of and turn off time. [7]	
	QOR do	
(02) a)		
	state characteristics of sand explain same. [8]	
b)		
- 1	۸71	
	Ø.,	
Q3) a)	Explain operation of single phase full converter for R load with neat	
~ : '	circuit diagram and relevant waveforms. [8]	
b)	A single phase half controlled bridge rectifier supplies a ripple free load	
,	current of 10 A and operates from the 110V, 60Hz mains of the average	
	o/p voltage is 75V,	
	current of 10 A and operates from the 110V, 60Hz main. If the average o/p voltage is 75V, Calculate: i) Firing angle	
	i) Firing angle	
	n rumgaage	1
	ii) RMS o/p voltage	30

RMS supply current

iv) RMS 7th Harmonic supply current

OR

Draw the circuit diagram of three phase fully controlled converter with R load. Draw load current and load voltage waveforms with α=60° and 90°. b) A three phase full converted operated from three phase star connected 208 V, 60 Hz supply with Pload of 10 ohm. It is required to obtain 50% of maximum possible alitput voltage. ii) ruis and are rage currents (Technical Pg2-81 Ex 2.8.3) PRINCIPAL ADYPSOE, PUNE ADYPSOE, PUNE ADYPSOE, PUNE

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Total No. of Questions : 4]

SEAT No. Total No. of Pages: 2

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T.E. (E &TC Engineering) (Insem) POWER DEVICES & CIRCUITS

(2019 Patterff) (Semester-II) (304194)

Time: 1 Hour!

[Max. Marks: 30

Instructions to the candidates:

Answer Q.1 or Q.2, Q.3 or Q.4.

Neat diagrams and waveforms must be drawn wherever necessary.

Figures to the right side indicate full marks.

Use of nonprogrammable calculator is allowed.

Assume Suitable data if necessary.

Q1) a) With the help of construction diagram, Explain the working of SCR. Why it is called as controlled rectifier.

b) Explain with neat diagram the turn or and turn off characteristics of SCR. Write down equation for turn of and turn off time. [7]

Explain with neat diagram the working of power MOSFET. Draw steady (02) a) state characteristics of thand explain same.

[8]
b) Explain isolated gate drive circuit for MOSFET and explain its operation.

Explain operation of single phase full converter for R load with neat circuit diagram and relevant waveforms.

OR

b) A single phase half controlled bridge rectifier supplies a ripple free load current of 10 A and operates from the 110V, 60Hz mains of the average o/p voltage is 75V,

Calculate:

Firing angle

RMS o/p voltage

RMS supply current

RMS 7th Harmonic supply current

Draw the circuit diagram of three phase fully controlled converter with R load. Draw load current and load voltage waveforms with \a=60° and 90°.

> A three phase full converter operated from three phase star connected 208 V, 60 Hz supply, with R load of 10 ohm. It is required to obtain 50%

> of maximum possible output voltage. [7] Calculate:

Delay angle of

· 10:66L

runs and average currents (Technical Pg2-81 Ex 2.8.3)

Vde = 120.32

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10

Question Bank Theory



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Unit 4 Question Bank (MCQs + Subjective)

A.Y.: 2022-2023

Semester: II

Class: TE

Name of Subject: Power Devices & Circuits

Unit 4: DC-DC Converters and AC Voltage Controller

Question	Question
Number	
1	Choppers are used to control: the DC voltage level. a) DC Voltage Level b) AC Voltage Level c) Both AC and DC Voltage Level
2	d) None of the above Choppers converter a) AC to DC b) DC to AC c) DC to DC
	d) AC to AC
3	What is the duty cycle of a chopper? a) Ton/Toff b) Ton/T c) T/Ton d) Toff x Ton
4	The load voltage of a chopper can be controlled by varying the a) duty cycle b) firing angle c) reactor position d) extinction angle
5	The values of duty cycle (α) lies between a) $0 < \alpha < 1$ b) $0 > \alpha > -1$ c) $0 < = \alpha < = 1$ d) $1 < \alpha < 100$

1	TET : de discourse de la constant de
6	If T is the time period for a chopper circuit and μ is its duty cycle, then
	the chopping frequency is a) Ton/α
	b) Toff/α
	c) a/Toff
	d) α /Ton
7	Find the output voltage expression for a step down chopper with Vs as
	the input voltage and α as the duty cycle.
	a) $V_0 = V_s/\alpha$
	b) $Vo = Vs \times \alpha$
	c) Vo = Vs^2/α
	d) $V_0 = 2V_S/\alpha\pi$
	d) to 2 to the
8	Find the expression for output voltage for a step-up chopper, assume
	linear variation of load current and α as the duty cycle.
	a) Vs
	b) Vs/a
	c) Vs/(1-α)
	d) $V_s/\sqrt{2}$
9	Find the output voltage for a step-up chopper when it is operated at a
	duty cycle of 50 % and $Vs = 240 \text{ V}$.
	a) 240 V
	b) 480 V c) 560 V
	d) 120 V
0	If a step up chopper's switch is always kept off then (ideally)
	a) Vo = 0
	b) V ₀ = ∞
	c) $V_0 = V_S$
	d) Vo > Vs
1	For a step-up chopper, when the duty cycle is increased the average
	value of the output voltage
	a) increases
	b) decreases
	c) remains the same
	d) none of the mentioned
2	In constant frequency TRC or pulse width modulation scheme,
-	is varied.
	a) Vs
	b) Ton
	c) T
	d) f
3	In case of variable frequency system is varied
	a) T
	b) Ton c) Toff
	c) Toff
	c) Toff Pune But A

	d) supply frequency
14	In pulse width modulation scheme, is kept constant.
	a) Vs
	b) Ton c) T
	d) Toff
15	The control strategy in which on and off time is guided by the perviou
	set of values of a certain parameter is called as
	a) time ratio control
	b) pulse width modulation
	c) current limit control
	d) constant frequency scheme
17	
16	In the current limit control method, the chopper is switched off when
	a) load current reaches the lower limit
	b) load current reaches the upper limit c) load current falls to zero
	d) none of the mentioned
17	SMPS Topologies are:
	a) Buck
	b) Boost
	c) Flyback
	d) All above
18	SMPS is used for
	a) obtaining controlled ac power supply
	b) obtaining controlled dc power supply
	c) storage of dc power
	d) switch from one source to another
19	SMPS are based on the principle.
	a) Phase control
	b) Integral control
	c) Chopper
	d) MOSFET
20	Choose the incorrect statement.
20	
	a) SMPS is less sensitive to input voltage variations
	b) SMPS is smaller as compared to rectifiers
	c) SMPS has low input ripple
	d) SMPS is a source of radio interference
21	is used for critical loads where temporary power failure can
	cause a great deal of inconvenience.
	a) SMPS School
	b) UPS School

	c) MPS
	d) RCCB
22	LM 3524 is used in DC-to DC operation for:
	a) Step Down Chopper
	b) Step Up Chopper
	c) SMPS
	d) AC Voltage Controller
23	Flyback converters are used for:
	a) Low power applications
	b) Medium power applications
	c) High power applications
	d) All of the above
24	What kind of output rectifier and filter circuit is used in a fly back converter?
	a) a four-diode bridge rectifier followed by a capacitor
	b) a single diode followed by an inductor-capacitor filter
	c) a single diode followed by a capacitor
	d) will require a center-tapped secondary winding followed by a full wave rectifier and a output filter capacitor.
25	A fly-back converter has primary to secondary turns ratio of 15:1. The input voltage is constant at 200 volts and the output voltage is maintained at 18 volts. What should be the snubber capacitor voltage under steady state?
	(a) More than 270 volts.
	(b) More than 200 volts but less than 270 volts.
	(c) Less than 18 volts.
	(d) Not related to input or output voltage.
26	In the principle of phase control
	a) the load is on for some cycles and off for some cycles
	b) control is achieved by adjusting the firing angle of the devices
	c) control is achieved strong the number of on off cycles d) control cannot be achieved

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27	AC Voltage Controllers are:
	a) AC to DC Converters
	b) AC to AC Converters
	c) DC to DC Converters
	d) None of the above
28	In AC voltage controllers the
	a) variable ac with fixed frequency is obtained
	b) variable ac with variable frequency is obtained
	c) variable dc with fixed frequency is obtained
	d) variable dc with variable frequency is obtained
29	The AC voltage controllers are used in applications.
	a) power generation
	b) electric heating
	c) conveyor belt motion
	d) power transmission
20	
30	Single Phase AC Voltage Controller circuit has $Vs = 230V$ and $R = 20$
	2. Find the value of the average output voltage at the R load for a firing
	angle of 45°.
	a) 224 V
	b) -15.17 V
	c) 15.17 V
	d) -224 V
2.	
31	Choppers are classified on the basis of Quadrants of operation as:
	a)Single Quadrant
	b) Two Quadrant
	c) Four Quadrant
	C) Four Quadrant
	d)All the above
32	Cl
32	Choppers are classified as:
	a)Step Up Chopper
	b) Step Down Chopper
	c) both (a) and (b)
	: 14 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
4	d) None of the above
33	o atil Scho
33	Choppers works on:
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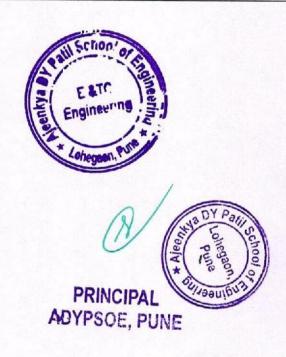
	a)Purely Resistive Load
	b) Medium Inductive Load
	c) Highly Inductive Load
	d) All of the above
34	Chopper Control Strategies Includes:
	a)Current Limit
	b) PWM Control
	c) Variable Frequency Control
	d) All of the above
35	The performance parameters of Step down chopper are:
	a)Ripple Current
	b)Maximum Switching Frequency
	c) Continuous and Discontinuous Inductor current
	d) All the above
36	The performance parameters of Step UP chopper are:
	a)Ripple Current
	b)Maximum Switching Frequency
	c) Continuous and Discontinuous Inductor current
	d) All the above
37	The performance parameters of Step down chopper are:
	a) Minimum value of inductor to maintain continuous Inductor Current
	b) Ripple content of output voltage & output current
	c) Ripple content of input current
	d) All the above
38	The performance parameters of Step UP chopper are:
	a) Minimum value of inducator to maintain continuous Inductor Current

	b) Ripple content of output voltage & output current
	c) Ripple content of input current
	d) All the above
39	SMPS Can be realized using following topology:
	a)Half Bridge
	b)Fly back
	c) LM3524 based
	d) All the above
40	Single-Phase full wave AC voltage controller consists of:
	a)One Thyristor
	b) Two Thyristors
	c) Three Thyristors
41	d) Four Thyristors For bidirectional operation of converters
	a. A parallel combination of controllable switch and a diode is used b. A parallel combination of controllable switch and capacitor is used c. A series combination of controllable switch and a diode is used d. A series combination of controllable switch and a capacitor is used
42	A step - down choppers can be used in
	l l
	a. Electric traction
	b. Electric vehicles
	c. Machine tools
	d. All of these
43	The output current in PWM DC - DC converters is equal to
	a. Average value of the output inductor current
	0. Product of an average inductor current and a function of duty ratio
	c. Littler (a) or (b)
	d. None of these
44	The control method used for PWM dc - dc converter is
	a. Voltage mode control
	b. Current mode control
	c. Hysteric control
1-	d. All of these
45	The average value of the output voltage in a step – down dc chopper is
	Patil Sch
	a. $V_0 = V_s$ b. $V_0 = DV_s$
1	$c. V_0 = V_s/D$

	d. $V_0 = V_s / (1 - D)$
46	Applications of AC Voltage Controller:
	a) Light dimmers
	b) Industrial heating application
	c) Speed control of induction motor
	d) All of these
48	Applications of AC Voltage Controller:
	a) Fan Regulator
	b) On load tap changing of a transformer
	c) Control of induction motor
	d) All of these
49	Chopper output voltage is:
	a)Directly Proportional to Input
	b)Directly Proportional to Duty Cycle
	c)Both (a) and (b)
	d)None of the above
50	Duty Cycle is used for:
	a)SMPS
	b) AC Voltage Controller
	c) Chopper
	d) All of these
51	Define choppers? List the types
52	Define Duty Cycle. Give the equation
53	Find the output voltage for a step-up chopper when it is operated at a duty cycle of 50 % and $Vs = 240 \text{ V}$.
54	What is the need of strategies of chopper?. List the strategies
55	Single Phase AC Voltage Controller circuit has Vs = 230V and R = 20
	Ω. Find the value of the average output voltage at the R load for a firing
	angle of 45°.
	angle of 45.
56	What do we mean by performance parameters of chopper? List any tw
57	What is an AC Voltage Controller? Give two applications
58	What is SMPS? What is its need?
59	
	Give any four applications of SMPS
60	How PWM control is achieved in Chopper Control Strategy?
61	How current limit control is achieved in Chopper Control Strategy?
(2	How Variable frequency control is achieved in Chopper Control
62	Strategy?
63	How Phase control is achieved in Voltage Controller?

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64	What is step down chopper? If the duty cycle is increased what is the effect on output voltage?
65	What is step down chopper? If the duty cycle is decreased what is the effect on output voltage?
66	What is step up chopper? If the duty cycle is decreased what is the effect on output voltage?
67	What is step up chopper? If the duty cycle is increased what is the effect on output voltage?
68	What is PWM control? How chopper output is controlled?
69	What is current limit control? How chopper output is controlled?
70	What is Variable frequency control? How chopper output is controlled?





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Unit 5 Question Bank (MCQs + Subjectives)

A.Y.: 2022-2023

Semester: II

Class: T.E. Div: A&B

Name of Subject: Power Devices & Circuits

Unit 5: Resonant Converters & Protection of Power Devices & Circuits

Question Number	Question
1	The converter circuit which employs turn on and turn off when the voltage and/or current through the device is zero at the instant of switching is
2	Resonant Converters are needed to :
	a) Realize high frequency switching in converter
	b) Reduce the converter size
	c) Increase the power density
	d) All the above
3	A ZCS converter is one in which:
	a) Switch turns ON and OFF at zero energy
	b) Switch turns ON and OFF at zero power
	c) Switch turns ON and OFF at zero voltage
4	d) Switch turns ON and OFF at zero current
4	A ZVS converter is one in which:
	a) Switch turns ON and OFF at zero energy
	b) Switch turns ON and OFF at zero power c) Switch turns ON and OFF at zero voltage
	d) Switch turns ON and OFF at zero current
5	In Zero Current Switching:
	a) The peak resonant carrest flows through

b) Peak switch voltage remains the same
c) The peak resonant voltage appear across the switch
d) Peak switch current remains the same.
e) Both (a) and (b)
f) Both (c) and (d)
In Zero voltage Switching:
a) The peak resonant current flows through
b) Peak switch voltage remains the same
c) The peak resonant voltage appear across the switch
d) Peak switch current remains the same.
e) Both (a) and (b)
f) Both (c) and (d)
What is the purpose of heat sink for power devices?
a) Protect power devices due to overheating
b) Absorb excess heat from power devices
c) Keep power devices at desired temperature range
d) All of the mentioned
What is the major principle behind heat sink action?
a) Avogadro's law
b) Fourier's law
c) Archimedes principal
d) Faraday's law
Comparing high heat objects with cooling objects which one will have
slow-moving molecules?
a) High heat objects
b) Cooling objects c) Both of them have equal molecular movement
d) Cannot be predicted
Active heat sinks are also called as
a) fans
b) on sinks
c) high sinks
d) normal sinks
Passive heat sinks are made of
a) Copper
b) Aluminum (Lohegaon, 2)



	c) Iron
	d) Zinc
12	Heat dissipation from heat sink take place primarily by
	a) Conduction
	b) Convection
	c) Radiation
12	d) All of the mentioned
13	Heat sinks are provided with peripheral fins to
	a) Provide good appearance
	b) Increase heat absorption
	c) Increase surface area of heat dissipation
	d) Provide material stability
14	De-rating factor is usually expressed in
	a) W/°C
	b) °C/W
	c) W/s
	d) kW/K
15	A metal oxide varistor (MOV) is used for protecting
	(a) Gate circuit against overcurrents
	(b) Gate circuit against overvoltages
	(c) Anode circuit against overcurrents
	(d) Anode circuit against overvoltages
16	The snubber circuit connected across an SCR isto
	(a) Suppress dv/dt
	(b) Increase dv/dt
	(c) Decrease dv/dt
	(d) Keep transient overvoltage at a constant value
17	The object of connecting resistance and capacitance across gate circuit
	is to protect the SCR gate against
	(a) Overvoltages
	(b) dv/dt
	(c) Noise signals
	(d) Over currents
18	Over current fault is most likely in
	a) Transformer
	b) Overhead line equipment
	c) Alternator
	d) Motors
19	di/dt protection is provided to the thryistor by
	a) connecting an inductor in parallel across the load
	c) connecting in inductor (an arallel across the gate terminal
	Coneguon, 2

	d) connecting an inductor in series with the gate
20	The dv/dt protection is provided in order to
	a) limit the power loss
	b) reduce the junction temperature
	c) avoid accidental turn-on of the device
	d) avoiding sudden large voltage across the load
21	dv/dt protection is provided to the SCR by
	a) connecting a capacitor in parallel with the load
	b) connecting an inductor in series with the load
	c) connecting a capacitor & resister in parallel with the device
	d) connecting an inductor & resister in parallel with the device
22	The effect of over-voltages on SCR are minimized by using
	a) RL circuits
	b) Circuit breakers
	c) Varistors
	d) di/dt inductor
23	Over-current protection in SCRs is achieved through the use of
	a) Varistors
	b) Snubber Circuits
	c) Fast acting current limiting fuse
	d) Zener diodes
24	Back up protection is needed for
	a) Over voltage
	b) Short circuits
	c) Over current
	d) All of these
	d) All of these
25	Sources of Electromagnetic Interference:
	a) Lighting
	b) Commercial Radio
	c) Telephone Communication
	d) All of the above
26	EMI is transmitted uptwo lowns:

	a) Radiated and Conducted
	b) Radiated and Convection
	c) Conduction and Covection
	d) All of the above
27	Conducted Noise consist of:
	a) Differential Mode
	b) Common Mode
	c) Both (a) and (b)
	d) None of the above
28	EMI is reduced by:
	a) Properly designed snubber
	b) Resonant Converter
	c) Use of proper shielding
	d) All of the above
29	The objective of shielding is:
	a) Elimination of incident fields
	b) Prevent the emission of components of the system
	c) Avoid radiations outside the boundaries
	d) All the above
30	Types of shielding includes:
	a) Electric Field Shielding
	b) Magnetic Field Shielding
	c) Electromagnetic field shielding
	d) All of the above
31	Resonant converters are needed:
	a) To avoid switching tosses

	b) To reduce electromagnetic interference due to dv/dt and di/dt
	c) Both (a) and (b)
	d) None of these
32	Switching losses in SMPS are reduced due to:
	a) Possibility of high speed switching
	b) Switching losses are reduced due to zero current and zero voltage during switching
	c) Both (a) and (b)
	d) None of these
33	Voltage and current stresses of the switches can be reduced by:
	a) Snubber circuit
	b) Changeover Switch
	c) Protection circuits
	d) None of the above
34	Snubber Circuit Works as:
	a) Losses are transferred from the switch to the R of the RC- Sunbber
	b) Capacitor Discharges through Resistor when switch is turned ON
	c) Total losses do not necessarily decrease.
	d) All the above
35	At resonance, series resonant circuit has power factor equal to:
	a)1
	b)0
	c) ∞
	d) None of these
36	At resonance, series resonant circuit has an impedance equal to:
	a)1
	b)R

TSTEE	c) ∞
	d) None of these
37	At resonance, series resonant circuit has current equal to:
	a)Minimum
	b)0
	c) Maximum
	d) None of these
38	At resonance, series resonant circuit has phase angle equal to:
	a)Minimum
	b)0
	c) Maximum
	d) None of these
39	At resonance, series resonant circuit has capacitive reactance equal to:
	a)Resistance
	b) Inductive Reactance
	c) Maximum
	d) None of these
	· · · · · · · · · · · · · · · · · · ·
40	At resonance, parallel resonant circuit has an admittance equal to:
	a)1
	b)R
	c) ∞
	d) None of these
41	At resonance, parallel resonant circuit has current equal to:
	a)Minimum
	b)0 (Loneogon) 2 (
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	c) ∞
	d) None of these
37	At resonance, series resonant circuit has current equal to:
	a)Minimum
	b)0
	c) Maximum
	d) None of these
38	At resonance, series resonant circuit has phase angle equal to:
	a)Minimum
	b)0
	c) Maximum
	d) None of these
39	At many
39	At resonance, series resonant circuit has capacitive reactance equal to:
	a)Resistance
	b) Inductive Reactance
	c) Maximum
	d) None of these
40	At resonance, parallel resonant circuit has an admittance equal to:
	a)1
	b)R
	c) ∞
	d) None of these
41	At resonance, parallel resonant circuit has current equal to:
	a)Minimum
	b)0
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	c) Maximum
	d) None of these
42	At resonance, parallel resonant circuit has an admittance equal to:
	a)1
	b)Y
	c) ∞
	d) None of these
43	At resonance, parallel resonant circuit has an voltage equal to:
	a)Minimum
	b)0
	c) Maximum
	d) None of these
44	At resonance, series resonant circuit has an voltage equal to:
	a)Minimum
	b)0
	c) Maximum
	d) None of these
45	Series load resonant (SLR) converter has:
	a) Discontinues area Ws <w<sub>0/2</w<sub>
	b) Continues area W ₀ /2< W _S < W ₀
	c) both (a) and (b)
	d) None of the above
46	Load power in resonant converter is controlled by adjusting:
	a) Voltage
	b) Current
	c) Switching frequency
	d) All of these
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47	Peak value of current in resonant circuit is:
	a)much higher than the average of the output current
	b) much lower than the average of the output current
	c) much higher than the rms output current
	d) much higher than the rms output current
48	During turning ON of the devices in resonant converters:
	a) Current and votages are not zero and losses occur.
	b) Current and voltages are zero
	c) both (a) and (b)
	d) None of these.
49	During turning OFF of the devices in resonant converters:
	a) Current and votages are not zero and losses occur.
	b) Current and voltages are zero
	c) both (a) and (b)
	d) None of these.
50	In ZCS
	a) Switch turns on and off without current
	b) Switch turns on and off without voltage
	c) Switch turns on and off with current
	d) All of these
51	In ZVS
	a) Switch turns on and off without current
	b) Switch turns on and off without voltage
	c) Switch turns on and off with current
	d) All of these
52	Soft Switching is used to reduce:
	a) Electromagnetic Interaference
	ay Steet of Mereterence

	b) Losses
	c) both (a) and (b)
	d) None of these
53	What is the Need for Resonant converters?
54	What do you mean by ZCS? How it implemented in Resonant Converters?
55	What do you mean by ZVS? How it implemented in Resonant Converters?
56	What is the necessity of Cooling & heat sinks?
57	What is the over voltage conditions? List the protection devices
58	What is the over current conditions? List the protection devices
59	How metal oxide varistors works under abnormal condition?
60	What is an EMI? What are its sources?
61	List the shielding techniques for EMI
62	How Snubber circuit protect the devices?
63	What do you mean by shielding? What is the necessity of shielding?
64	What is an EMI? How it is reduced?
65	What is dv/dt rating? How to protects the device from high dv/dt?
66	What is di/dt rating? How to protects the device from high di/dt?
67	What is overcurrent protection? How it is achieved?
68	What is flyback topology of SMPS?
59	How Half Bridge toplogy of SMPS works?
70	What are the methods of ac voltage control? How they are different?





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Department of E&TC Engineering

Unit 6 Question Bank (MCQs + Subjective)

A.Y.: 2022-2023

Semester: II

Class: T.E., DIV: A&B

Name of Subject: Power Devices & Circuits

Unit 6: Power Electronics Applications

Question Number	Question
1	is used for critical loads where temporary power failure can
	cause a great deal of inconvenience.
	a) SMPS
	b) UPS
	c) MPS
	d) RCCB
2	is used in the rotating type UPS system to supply the
	mains.
	a) DC motor
	b) Self excited DC generator
	c) Alternator
	d) Battery bank
3	Static UPS requires
	a) only rectifier
	b) only inverter
	c) both inverter and rectifier
4	d) none of the mentioned
4	Usually batteries are used in the UPS systems.
	a) NC
	b) Li-On
	c) Lead acid
	d) All of the mentioned
5	OFF line UPS running on Normal Power Supply requires following
	Subsystems:
	a) Mains, Battery Charger, Battery, Inverter, Transfer Switch, Load
	b) Mains, Transfer Switch, Load
	c) Mains, Transfer Switch, Inverter, Load
	d) Mains, Battery Charger, Battery, Inverter, Load
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6	OFF line UPS running on Power Failure requires following
	Subsystems:
	a) Battery Charger, Battery, Inverter, Transfer Switch, Load
	b) Mains, Transfer Switch, Load
	c) Inverter, Transfer Switch, Load
	d) Mains, Battery Charger, Battery, Inverter, Load
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7	ON-line UPS running on Normal Power Supply requires following
	Subsystems:
	a) Mains, Battery Charger, Battery, Inverter, Load
	b) Mains, Transfer Switch, Load
	c) Mains, Transfer Switch, Inverter, Load
	d) Mains, Battery Charger, Battery, Inverter, Load
8	ON-line UPS running on Normal Power Supply requires following
	Subsystems:
	Succession .
	a) Mains, Battery Charger, Battery, Inverter, Load
	b) Battery, Transfer Switch, Load
	c) Mains, Transfer Switch, Inverter, Load
9	d) Battery Charger, Battery, Inverter, Load
9	UPS backup time indicate:
	a) How long mains supply will charge a battery.
	b) How long battery charger will remain ON
	c) How long the UPS will provide AC supply
	d) How long the UPS will provide DC supply
10	Electronic Ballast consists of:
) 14' B C C B 10 BU
	a) Mains, Power factor Corrector, Rectifier, Filter, Inverter, Lamp
	b) Mains, Rectifier, Filter, Inverter, Lamp
	c) Mains, Power factor Corrector, Rectifier, Filter, Inverter, Load
	d) Mains, Rectifier, Filter, Inverter, Load
11	LED drivers protect LEDs from voltage or current fluctuations
	a) Voltage or power fluctuations
	b) Voltage or current fluctuations
	c) Power or current fluctuations
	d) All of the above
12	LED drivers convert:
	a)Higher voltage, alternating current to low voltage
	b) Lower voltage, alternating current to high voltage
	c) Higher voltage, alternating oltage to low current
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	d) Lower voltage, the rnating vottage to low current
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13	LED light output is proportional to:
	a) its voltage supply
	b) its energy supply
	c) its current supply
	d) all of the above
14	Typical LED Driver consists of following subsystems:
	a)Mains,EMI Filter, Rectifier, DC Filter, PFC, Dimming Control, Voltage-Current Regulator, LED
	b) Mains,EMI Filter, Inverter, DC Filter, PFC, Dimming Control, Voltage-Current Regulator, LED
	c) Mains, EMI Filter, Battery, DC Filter, PFC, Dimming Control, Voltage-Current Regulator, LED
	d)Mains, EMI Filter, Battery Charger, DC Filter, PFC, Dimming Control, Voltage-Current Regulator, LED
15	Fan regulator provide:
	a) Continuous speed control High power consumption as compared to resistive type regulators.
	b) Continuous speed control Low power consumption as compared to resistive type regulators.
	c) discontinuous speed control Low power consumption as compared to resistive type regulators
	d) None of the above
16	A Typical Fan regulator consists of:
	a) Mains & Fan/Load
	b) Diac and Traic
	c) Resistors & Capacitor d) All the above
17	For a DC motor drive if a diode rectifier is used:
	a) the energy recovered during regenerative braking is dissipated in a motor
	b) the energy recovered during regenerative braking is dissipated in a diode
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	c) the energy recovered during regenerative braking is dissipated in a heat
	d) the energy recovered during regenerative braking is dissipated in a resistor
18	Single phase separately excited DC motor drive, for a speed less than base speed:
	a) Armature current are maintained constant to meet the torque demand
	b) Field currents are maintained constant to meet the torque demand
	c) Armature voltage Va is varied to control the speed.
	d) All the above.
19	Single phase separately excited DC motor drive, for a speed higher than
	base speed:
	a)The armature voltage is maintained at the rated value
	b) The field current is varied to control the speed.
	c) both (a) and (b)
	d) None of the above
20	A stepper motor moves one step when:
	a) The direction of current flow in the field coil(s) changes
	b) Reversing the magnetic field of the stator poles.
	c) Both (a) and (b)
	d) None of the above
21	Unipolar Steeper motor drives have:
	a) Two separate field coils
	b) Change over switch
	c) both (a) and (b)
	d) None of the above
22	Bipolar Steeper more than e:
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	a) One Field Coil
	b) Two Chargeover Switches that are Switched in the Opposite Direction. c) both (a) and (b)
	d) None of the above
23	A brushed DC motor consists of a commutator and brushes that converts:
	a)DC current in an armature coil to an AC current
	b) AC current in an armature coil to an DC current
	c) Both (a) and (b)
	d) None of the above
24	A BLDC motor accomplishes commutation electronically using rotor position feedback to determine:
	a) When to switch the voltage
	b) When to switch the current
	c) When to switch the power
	d) None of the above
25	BLDC Motor has:
	a)Electronic commutation based on rotor position information
	b) High Efficiency
	c) Better thermal performace
	d) All the above
26	BLDC Motor has:
	a)High output power
	b)Fast Dynamic Response
	c)High Speed Range
	d) All the above
27	BLDC Motor has: 65thool of 63
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	a)Low Electric Noise
	b) Flat Speed/Torque
	c) Little Maintenance
	c) Little Maintenance
	d) All the above
28	Brushless DC motors use switches to realize current commutation,
	and thus continuously rotate the motor.
	a) Electric
	b) Mechanical
	c)Electromechanical
	d) All the above
	a) All the above
29	BLDC commutation relies on feedback on the position to decide
	when to energize the corresponding switches to generate the biggest
	torque.
	a)Motor
	b) Rotor
	c) Generator
	d) None of the above
30	Four quadrant operations of DC motor drives are:
	a) Motoring in forward and reverse direction
	b) Braking in forward and reverse direction
	y = saming in forward and reverse direction
	c) both (a) and (b)
	d) None of the above
	d) None of the above
31	BLDC motor is analogous to
	a) Permanent magnet synchronous motor
	b) DC motor
	c) Rotating Transformer
	d) Single-phase Induction motor
32	
32	The speed of a BLDC motor can be controlled by
	a) Changing input DC voltage
	b) Changing temperature
	c) Changing wind

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33	Which are the advantages of DLDC
55	Which are the advantages of BLDC motor? a. Low cost
	b. Simplicity
	c. Reliability
	d. Good performance
	e. All of these
34	Due to low inertia, BLDC motors have
	a) Faster acceleration
	b) Slower acceleration
	c) High-cost
	d) Low cost
35	Which of the following are the types of BLDC motor?
	a) Unipolar, Bipolar
	b) Unipolar, PWM
	c) Bipolar, PWM
24	d) Synchronous, Induction
36	In the biomedical instruments like artificial heart pumps, the motor used
	10
	a) DC shunt motor b) DC series motor
	c) Induction motor
	d) BLDC motor
37	
3/	or an application which requires smooth and precise speed control over the wide range, the motor is preferred is
	a.Squirrel cage Induction Motor
	b.Synchronous Motor
	c.DC motor
	d. Wound Rotor Induction Motor
	a. Would Rotor induction Motor
38	Which type of drive can be used for Textile Industry?
	1. DC Motor
	2. Squirrel cage Induction Motor
	Total ve Compound Motor
	4. Synchronous Motor
39	Which type of drive san be used to Minning Process?

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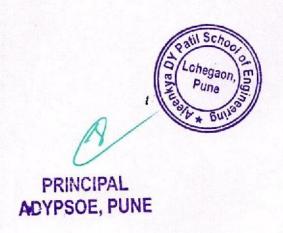
	DC Motor with Ward Leonard Speed control
	2. Double Squirrel cage Induction Motor
	3. Slip Ring Induction Motor
	4. All of the above
40	Which motor is usually preferred for the elevator nowadays?
	1. Induction Motor
	2. Synchronous Motor
	3. Capacitor Start Single Phase Motor
	4. None of the above
41	In case of a 3 phase induction motor, plugging is done by:
	1. Starting the motor on load which is more than the rated load
	2. Pulling the motor directly on line without a starter
	3. Interchanging connections of any two phases of the stator for
	quick stopping
	4. Locking of the rotor due to harmonics
	g of the fotol due to narmonics
42	A stepper motor may be considered as a converter.
	a) Dc to dc
	b) Ac to ac c) Dc to ac
	d) Digital-to-analogue
43	The rotational speed of a given stepper motor is determined solely by
	the a) Shaft load
	b) Step pulse frequency
	c) Polarity of states assessed
	d) Magnitude of stator current.
44	What is UPS? Give any two applications
45	
	What is online UPS? Does INVERTER remain ON when mains supply is available?
46	What is offline UPS? Does INVERTER remain ON when mains supply is available?
	is available?
17	What do you mean by Batter How it is related to Back up time?

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48	What is an Electronic Ballast? List any two applications
49	What is a driver circuit? List any two applications of LED Lamp Driver?
50	List the components used in Fan Regulator Circuit
51	What is the function of DC motor drive? List any two applications
52	What is the function BLDC motor drive? List any two applications
53	What is the function three phase induction motor drive? List any two applications
54	List any four specifications of UPS 1
55	Comment on battery charging rating
56	List any four specifications of LED Driver
57	What are the two circuit topology are preferred in LED Lamp Driver?
58	List the two necessities of fan regulator
59	List any four industrial applications of Power Electronics
60	List any four home applications of Power Electronics







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Department of E&TC Engineering

Question Bank (Theory)

A.Y.: 2018-2019

Semester: II

Class: T.E.

Div: -

Name of Subject: Power Electronics

Question Number	Question	Marks
1	Draw & explain two transistor analogy of SCR	6
2	For an SCR, the gate cathode characteristics has a straight line slope of 130. For triggering source voltage of 15V and allowable gate power dissipation of 0.5W, calculate the gate series resistance (Rg).	6
3	Draw construction diagram of n-channel enhancement type MOSFET and explain its steady state characteristics.	6
4	Compare power MOSFET with SCR	6
5	Draw & explain single phase fully controlled bridge converter for R-L load with various o/p voltage waveforms.	8
6	A single phase semi converter is operated from 120V, 50Hz AC supply. The load is resistive having resistance of 15 Ω . If the average output voltage is 25% of the maximum possible average output voltage, determine the firing angle (α)	8
7	Draw & explain three phase half controlled bridge converter for R load with o/p voltage waveforms.	8
8	Explain the significance of free wheeling diode in controlled rectifiers.	4
9	Draw & explain single phase full bridge inverter for R-L load with o/p voltage & current waveforms	8
10	Single phase full bridge inverter is operated from 48V dc supply, it has a resistive load of $R = 2.4 \Omega$. Find its i) Output power (Po) ii) Total harmonic distortion (THD)	6
11	Explain operation of step up chopper with circuit diagram.	8
12	Explain with block schematic working of SMPS. What are its advantages over linear power supply?	6
13	A DC chopper with R-L Load is operated from 220V dc supply. The load parameters are R=5Ω, L=7.5mH and chopping frequency. Fc= 1KHz. If peak to peak load ripple current is Maximum, Calculate: 1 Peak to peak load ripple	GRatit Cohe

	current. 2] Average load current.	
14	Explain with block diagram working of online and offline UPS	6
15	Explain voltage and frequency control method of for 3-phase induction motor drive.	8
16	Explain working of electronic ballast with block diagram.	6
17	What is EMI? Explain various sources & minimizing techniques of EMI.	6
18	A single phase full wave ac voltage controller has a resistive load of R=10 Ω , and the input voltage is Vs=120V(rms), 50Hz. The delay angles of thyristors T1 and T2 are equal: $\alpha 1 = \alpha 2 = 90^{\circ}$. Determine: Vorms, Iorms, input ripple factor.	8
19	Explain operation of four quadrant chopper with circuit diagram.	8
20	Write note on 'overvoltage protection' in power electronics	6
21	Explain the terms latching current and holding current and compare them	4
22	Explain the steady state and switching characteristics of SCR	8
23	Draw and explain UJT triggering circuit for SCR	6
24	Draw and explain UJT triggering circuit using TCA785	6
25	Define and explain the concept of commutation.	6
26	Explain the inversion in single phase full converter	6
27	With the help of circuit diagrams and waveforms explain the working of single phase bridge inverter.	8
28	Explain 120 degree mode of three phase inverters with the help of waveforms	6
29	Explain different control strategies used for chopper	8
30	What are the advantages of SMPS over linear power supply?	4
31	Explain the operation of offline UPS with the help of block diagram	6
32	Explain ZCS and ZVC with circuit diagram	6
33	Explain the following SCR Ratings: 1] dv/dt 2]di/dt 3]Surge current 4]Holding current	8
34	Design UJT Trigger for SCR. Given VBB=20V, n=0.6, Ip =10μA, Iv =10mA. The frequency of oscillations is 100Hz. The trigger pulse width =50μS	10

35	Explain the operation of single phase semiconverter with waveforms for R-L Load	8
36	Explain the operation of single phase fullconverter with waveforms for R-L Load	8
37	Explain the performance parameters of AC-DC converters	8
38	Explain the operation of three phase semiconverter with waveforms for R Load	8
39	Explain the operation of three phase fullconverter with waveforms for R Load	8
40	Draw and explain the gate drive circuit for MOSFET & IGBT	8
41	Explain cooling and heat sink in power devices	8
42	What are resonant converters? Explain SLR half bridge converter.	8
43	Explain overcurrent fault conditions and overcurrent protection in power devices	8
44	Explain variable voltage & variable frequency three phase induction motor drive	8
45	Explain single phase separately excited DC motor drive	6
46	Explain in detail performance parameters of DC-DC converter	6
47	Compare 2- quadrant & 4-quadrant choppers	6
48	Explain single phase PWM inverter	6
49	Explain three phase voltage source inverter for balanced star R Load	8
50	Compare three phase semi & full converter for R Load	6

(Mr-R-L-Kazi)

Name and Sign of Faculty

Academic Coordinator

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Department of E&TC Engineering

11

Question Bank (Oral)



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Department of E&TC Engineering

Question Bank (Oral/Objectives)

A.Y.: 2018-2019

Semester: II

Class: TE

Div:

Name of Subject: Power Electronics

Question	Question	Marks
Number		
1	What is holding current in SCR?	
2	What is Latching current in SCR?	
3	What are different turn on methods of SCR?	
4	What is snubber circuit?	
5	What is hard switching of thyristor?	
6	What is firing angle?	
7	What is meant by SOA?	
8	What is the main component used for isolating the power circuits, power semiconductor from low power circuits?	
9	Name some of the current controlled devices?	
10	Name some of the voltage driven devices?	*
11	What is the duty cycle?	
12	What are the advantages of free wheeling diode in rectifier circuits?	
13	What is meant by commutation?	
14	What are the types of commutation?	Sen
15	What is natural commutation?	GOW ON IM

16	What is forced commutation?	
17	What are control strategies of chopper?	
18	Give some examples of power electronics applications from day-to- day life.	
19	How can a thyristor turned off?	
20	What is meant by phase controlled rectifier?	
21	What is meant by power factor in controlled rectifier?	
22	What are the applications of DC Chopper?	
23	What is meant by step up and step down chopper?	WCIPLE-
24	What is meant by PWM control in dc chopper?	
25	What is meant by inverter?	
26	What are the applications of the inverter?	
27	What are the advantages of PWM control?	
28	Compare Voltage Source and Current Source Inverter?	
29	What are the applications of ac voltage controller?	
30	What are the advantages of ac voltage controller?	
31	What are the disadvantages of ac voltage controller?	
32	What are the two methods of control in voltage controller?	
33	What is the difference between on-off and phase control?	
34	What are advantages of on-off control?	
35	What are disadvantages of on-off control?	

(Mr. R.L. Kazi)

Name and Sign of Faculty

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Academic Coordinator

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Assignments/Tutorials

1



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Department of E&TC Engineering

Assignments

Form No. IQAC/18a.

Class: T.E. Div: A&B Subject: PDC

Subject Incharge: M. Riyaj Kazi

Assignment No. 1 | Assignment based on Unit:I

Date of issue: 29/03/2023

- Q. 1 State and Define any four ratings of SCR
- Q.2 Explain Series & Parallel operations of SCR
- Q.3 Explain requirement of typical triggering circuits for various power devices
- Q.4 Explain the performance overview of Silicon, Silicon Carbide & GaN based MOSFET and IGBT
- Q.5 Draw the switching characteristics of SCR, MOSFET, IGBT and GTO

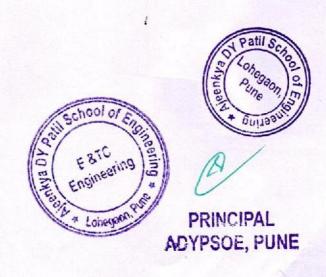
Q.6 Compare Power MOSFET & POWER IGBT

Sign of subject incharge: Mr. Riyaj Kazi

Sign of HoD: Dr. Sharan Inamdar

Chamad)

Note: Last Date of Submission: 31-03-2023.





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Department of E&TC Engineering

Assignments (

Form No. IQAC/18a.

Class: T.E. Div: A & B

Subject: PDC

Subject Incharge: M. Riyaj Kazi

Assignment No. 1 | Assignment based on Unit:II

Date of issue: 29/03/2023

- Q. 1 Explain the concept of line and forced commutation.
- Q.2 Explain Single Phase full converter with R-L Load with waveforms.
- Q.3 If a semi converter is operating from 230V/50Hz supply, driving a load resistance of 10 Ω . Find: i) Avg output power ii) RMS output power iii) Rectification efficiency
- Q.4 Explain the Significance of power factor and its improvement using PWM based techniques.
- Q.5 Explain the Three phase Full converters using SCR for R load using waveform.
- Q.6 If a single phase full converter is operating from 230V, 50 Hz Supply. The load is Find : i) Avg load voltage ii) Average load current. $R=10\Omega$, $\alpha=45^{\circ}$

Sign of subject incharge: Mr. Riyaj Kazi

Sign of HoD: Dr. Sharan Inamdar

Note: Last Date of Submission: 31-03-2023.







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Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune – 112 105.

Department of E&TC Engineering

Class: TE(E&TC)

Subject: Power Devices & Circuits

Unit IV: DC-DC converters & AC Voltage Controller

ASSIGNMENT-IV

- 1] Draw and explain Working Principle of step down chopper for highly inductive R-L load
- 2] What are the control strategies of chopper. Explain
- 3] What are the Performance Parameters of Chopper? Explain
- 4] Draw and explain Step up chopper with waveforms.
- 5] Distinguish between 2-quadrant & 4-quadrant choppers
- 6] Explain the topology: Fly back/ Half Bridge Converter
- 7] Draw and explain LM3524 based SMPS Circuit

8] Explain Single-Phase full wave AC voltage controller by using IGBT with R load

Mr. Riyaj Kazi

Sign of subject incharge

Engineering Engineering

Dr. Sharan Inamdar

HoD-E&TC



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Class: TE(E&TC)

Subject: Power Devices & Circuits

Unit V: Resonant Converters & Protection of Power Devices & Circuits

ASSIGNMENT-V

- 1] What is the Need for Resonant converters?
- 2] Explain the Concept of Zero current switching (ZCS)
- 3] Draw and Explain Zero voltage switching (ZVS) resonant converters
- 4] What is the necessity of Cooling & heat sinks
- 5] What is the over voltage conditions? Explain over voltage protection circuits
- 6] Explain metal oxide varistors as a protection device
- 7] Explain over current fault condition
- 8] Draw and explain Over current protection circuit
- 9] Explain Electromagnetic interference and its sources
- 10] What are Interference minimizing techniques? Explain.

11] Explain shielding techniques for EMI

Mr. Riyaj Kazi

Sign of subject incharge

E &TC Engineering

Dr. Sharan Inamdar

HoD-E&TC



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Class: TE(E&TC)

Subject: Power Devices & Circuits

Unit VI: Power Electronics Applications

ASSIGNMENT-VI

- 1] What are types of UPS? Differentiate between ON-line and OFF line UPS
- 2] Explain the ratings: Battery AH, back up time, battery charger rating.
- 3] Draw and explain Electronic Ballast. List the applications.
- 4] Draw and explain LED Lamp with Driver Circuit
- 5] Draw and explain Fan Regulator.
- 6] Explain with Waveforms Single phase separately excited DC motor drive
- 7] Explain stepper motor drive circuit
- 8] Explain BLDC motor drive

9] Explain Variable voltage & variable frequency three phase induction motor drive

Mr. Riyaj Kazi

Sign of subject incharge

E &Tr Engineering Engineering

Dr. Sharan Inamdar

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Ajeenkya DY Patil School of Engineering

Department of Electronics and Telecommunication Engineering

Cumulative Attendance

	Class: 1	E E&TC Div-B SEM-II					A Y 202	2-23		. Date-	01-02-202	3 to 19-0:	5-2023	
					SUBJECTS	S	AND THE		L	AB	Test of			
		Student's Name	CN	PM	PDC	NS	Total TH	CN	PDC	NS	Total Lab	Extra		
	Roll No	No. of lectures Planed>	42	40	36	35	153	10	8	11	29		Total	Percentage
		No of Lectures Conducted>	40	40	32	30	142	10	8	11	29		171	
	301	ADEPU RITHISH BHUPENDRA	5	13	- 1	2	21	2	1	3	6		27	15.79
	302	AKASH KUTE	6	17	1	0	24	0	0	2	2		26	15.20
	303	ASHISH SUDHAKAR JAGDHANE	10	21	0	2	33	1	1	2	4		37	21.64
	304	BABAR PRATIK SATISH	10	21	2	3	36	3	0	3	6		42	24.56
	305	BANSODE DEEP BALAJI	26	28	15	14	83	7	5	6	18		101	59.06
	306	BHALE ANSH RAJESH -	10	18	3	-2	33	2	- 0	4	6		39	22.81
	307	BHOGULKAR AKASH DHONDIBA	14	21	3	5	43	4	1	4	9		52	30.41
	308	CHATURE VAISHNAVI DNYANDEV	32	34	24	22	112	8	6	- 8	22	The state of	134	78,36
	309	CHIKANE SHUBHAM KALYAN	15	27	6	0	48	5	1	4	10		58	33.92
	310	CHOTALIA HARDIK HASMUKH	32	34	16	15	97	7	8	9	24		121	70.76
	311	DAFEDAR ADIL SHABBIR	8	18	3	9	38	4	2	4	10	Water land	48	28.07
	312	DESAI SWARUPANAND SANJAY	15	17	9	6	47	4	1	7	12		59	34.50
	313	DHANVE JANHAVI SANTOSH	27	29	18	14	88	6		7	18	EN LIVE	106	61.99
	314	DHAYGUDE SONALI BHARAT	25	27	21	19	92	7	8	8	23		115	67.25
	315	FATIMA GAFUR SHAIKH	0	0	0	0	0	0	0	- 0	0		0	0.00
	316	GAIKWAD SUJIT RAJARAM	25	27	12	10	74	6	5	8	19		93	54.39
	317	GANGATHADE NIKITA BHARAT	10	20	4	2	36	3	1	1	5		41	23.98
	318	GAURI POPAT MORE	8	16	1	4	29	1	0	3	4		33	19.30
	319	HASURE PRATIKSHA ANANDRAO	18	28	9	7	62	5	3	4	12		74	43.27
	320	HINGMIRE RAJ RAM	7	17	1	0	25	1	0	1	2		27	15.79
(F)	321	INGALE VAISHNAVI VIJAY	12	15	7	2	36	3	1	1	5		41	23.98
1	322	JADHAV ABHAY SUNIL	7	15	1	0	23	- 1	0	0	1		24	14.04
	323	JADHAV ABHISHEK ANIL	8	15	2	2	27	1	0	0	1		28	16.37
PRINCIPAL	324	JADHAV MRUNAL DEEPAK	21	22	18	15	76	5	4	. 6	15	1900	91	53.22
	325	JADHAV SUHAS SAMPAT	22	32	_ 7	5	66	7	6	7	20		86	50.29
ADYPSOE, PUI	326	JAGDALE VARAD VIKAS	4	16	1	0	21	. 1	0	0	1		22	12.87
	327	JAINKOP VISHAL MALLESH	2	8	0	0	10	0	0	0	0		10	5.85
	328	JASUD GAYATRI PRAVIN	27	17	10	8	62	. 7	.3	6	16		78	45.61
	329	JAWALE PRIYANKA PRAKASH	24	27	15	12	78	5	3	9	. 17	1	95	55.56
	330	KADAM ADESH SHRINIVAS	9	17	2	0	28	1.	1	2	4		32	18.71
atil School	331	KALJE OMKAR SHANKAR	17	22	14	12	65	4	3	2	9		74	43.27
133	332	KAMBLE SAKSHI PUNDLIK	18	28	6	7	59	6	3	8	17		76	44.44
(6) Obassa (m)	333	KAMBLE SUJAL VASANT	18	29	12	11	70	5	1	8	14		84	49.12
le Duragaon, 2	334	KARKE SUSHAMA BHIMASHANKAR	14	24	15	11	64	4	4	8	16		80	46.78
Tune 3	335	KHAN AARISH MAROOF	6	10	2	4	22	1	- 0	0	1		23	13:45
Collegation, Pune	336	KULKARNI VEDANT RAMESH	8	12	0	0	20	1	0	0	1	7/15/	21	12.28
* 00	337	KUMAR ANANT ARVIND	32	32	. 19	15	98	8	5	7	20		118	69.01





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338	MALI DARSHANA DATTATRAY	. 0	0	0	7 0	0	7 0	0	7 0	f o T	BH 40712402	No. 2 Sept.
339	MANISH SURESH SHINDE	11	17	2	2	32		0	8	100000000000000000000000000000000000000	0	0.00
340	MANIYAR MOHD ASIF RIYAZ AHMED	19	25	9	5	58	8	5	В	12	44	25.73
341	MANSURI GOUSMOHAMMAD INAMULLA	25	27	8	5	65		5	8	21	79	. 46.20
342	MHETRE VINOD BASAPPA	14	26	11	8	59	8	2	8	21	86	50.29
343	MOKASHE ROHINI DAGDUBA	17	25	13	10	65	4	1	3	18	77	45.03
344	MONDAL AJAY AMAR	27	29	11	6	73	7	2		8	73	42.69
345	NANAWARE VISHAL CHANDRAKANT	12	17	0	1	30	1	0	7	16	89	52.05
346	OMKAR BABU SESHADRI	26	28	6	3	63	8	5	0	1	31	18.13
347	PALLAVI PRASHANT SABLE	11	17	7	5	40	5		7	20	83	48.54
348	PANCHAL SHIVSAI SUNIL	29	22	16	12	79	8	3	8	16	56	32.75
349	PANCHMUKH DIVYA MAHESH	22	31	7	11	100000000000000000000000000000000000000	100	4	8	20	99	57.89
350	PATIL KALYANI GOPAL	12	19	1	3	71	6	1	5	12	83	48.54
351	PATIL PALLAVI SHRIKRISHNA	26	29	10	7	35	3	5	7	15	50	29.24
352	PATIL VAISHALI KADUBA	0	0	0	-	72	6	1	8	15	87	50.88
353	PAWAL AKASH DADARAO	22	24	6	0	0	0	0	0	0	0	0.00
354	PAWAR ADITEE RAMDAS	28	32		5	57	8	2	8	18	75	43.86
355	PAWAR KOMAL BHAUSAHEB	7	16	15	12	87	7	4	8	19	106	61.99
356	PRATIK GAJANAN SADAR	32		0	0	23	1	0	0	1	24	14.04
357	RAJPUT PRATIK SINGH SUBHASH SINGH	25	34	22	22	110	7	6	9	22	132	77.19
358	RATHOD AJAY SHANTIRAM	12	29	11	7	72	7	1	6	14	86	50.29
359	RAUT TUSHAR DILIP		19	2	3	36	2	0	5	7	43	25.15
360	ROKADE VAISHNAVI DIGAMBAR	27	30	14	10	81	7	3	8	18	99	57.89
361	SALALKAR MAYA RAVINDRA		14	0	0	22	1	0	0	1	23	13.45
362	SAURABH VIJAY JADHAV	15	22	10	8	55	4	2	8	14	69	40.35
363	SAYYAD ARSHAD PAIGAMBAR	8	14	0	0	22	1	0	1	2	24	14.04
364	SUSHMA SURESH SUPEKAR	26	31	11	6	74	7	3	6	16	90	52.63
365	SWAMI RUTUJA VIRBHADRA	.10	13	7	4	34	4	1	4	9	43	25.15
366	TAKALE KIRAN INDRARAJ	7	19	0	1	27	2	1	4	7	34	19,88
367	THAKKANNAVAR BHAIRVNATH BHIMANNA	13	21	5	6	45	4	3	6	13	58	33.92
368	KIRAN GANPATRAO UPASE	19	25	8	3	55	. 7	1	3	11	66	38.60
369	VAIBHAVI SUNUL NIMBALKAR	17	20	4	2	43	4	0	4	8	51	29.82
370	VAISHANAVI TULSHIRAM SAVARGAVE	24	27	14	15	80	6	4	8	18	98	57.31
371	VAISHNAVI JANARDHAN MORE	10	18	3	1	32	3	0	3	6	38	22.22
372	VISHAKHA RAOSAHEB PATIL	23	31	11	8	73	7	2	6	15	88	51.46
1		21	30	5	1	57	5	2	5	12	69	40.35
- 11	WALZADE ABHISHEK BALASAHEB	19	28	9	5	61	6	3	8	17	- 78	45.61
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Lohegaon, Pune

> Prof. Sagar D. Dhawale Class Teacher TE-B

Or. S.C.Inamadar

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13

Attendance Record

lass : TE-A	E&TC	SEM-II							-		
						AY	2022-23			Date- 1-02-2023	to 30-04-202
D. 11 M.				SUBJECTS			1		LAB		
Roll No	No. of lectures Planed>		PM 27	PDC 28	NS 28	Total TH	CN	PDC	NS	Total Lab	
204	No of Lectures Conducted →	26	24	27	27	104	7	7	8 7	15	7et
	JATTI MONALI NANDLAL AJAY PARITOSH DAS	8	4	7	4	23	5	3	5	13	28.5
-	AMBEKAR ATHARVA ATUL	5	6	8	0	10	4	3	4	- 11	16.6
	ANSARI ASIM JAHANGIR	4	4	3	1	19	1	1	0	2	16.6
-	ASHISH PANDA	9	5	13	6	33	4	5	1 4	13	36.5
	BAGAL MAYUR HANUMANT	16	11	14	9	50	4	7	2	13	50.0
0.000	BHERDE RUTUJA MANISH BHOPLE SUMEET PRALHAD	12	6	11	5	36	4	4	3	11	37.3
	BONDLA TARUN	0	3	0	0	32	5	6	0	15	37.3
	CHASKAR AVISHKAR BALU	2	5	0	0	7	0	0	0	0	5.56
	CHATE BHAKTI TUKARAM	14	9	7	12	42	5	6	4	15	45.2
	CHAVAN AISHWARYA ASHOK CHAVAN PRAJAKTA PRATAP	10	5	10	5	30	3	1	2	6	28.5
C	CHOUTEWAR VAISHNAVI			The same	3	26	4	5	3	12	30.1
-1-	BALAPRASAD DEOKAR VIRAJ SANDEEP	10	3	24	19	80	- 6	9	4	19	78.5
_	DESHPANDE ABHISHEK GANE	11	7	9	6	33	5	7	4	16	34.9
317 D	DIVYA YUVARAJ SALUNKE	11	10	9	9	39	3	4	3	10	33.33
	OOLSE GAYATRI DEEPAK SAIKWAD SHUBHAM SANJAY	5	3	3	0	11	1	0	0	1	9.52
	SAIKWAD SHUBHAM SANJAY SHEGADE SHAMBHAVI SHIRIS	5 8	6	2	0	12	3	3	0	6	14.25
321 G	SIRDE PRANAY SATYAWAN	9	7	7	6	18 29	3	5	3	4	15.87
	SORE PRANAV DIPAK	4	0	6	3	13	3	5	2	10	31.75
	GOVIND CHOUDHARY IIPPARAGI HARDIK BASAVARA	5	2	1 0	0	2	1	0	0	1	2.38
325 IN	NGALE OMKAR JANARDHAN	6	2	5	1 1	17	3 *	3	3	8	19.84
	ADHAV ANKITA ARJUN	4	2	6	6	18	3	4	3	10	18.25
	ADHAV TEJAS SANJAY ADHAV VAIBHAV TATERAO	17	9	12	12	50	6	7	5	18	53.97
JA	AGTAP SHRAVANKUMAR		0	2	0	3	1	0	0	1	3.17
	ATTATRAY ALGAONKAR HAMID NIYAZALI	7	1	2	1	8	3	3	3	,	13.49
	ADAM POOJA PRAMOD	7	0	2	0	9	3	2	3	8	23.81
2000	ALE VAISHNAVI RAJKUMAR	0	0	2	0	2	2 2	0	0	2	8.73
	AMBLE AJINKYA PRADEEP ATE ADITI NAMDEV	19	11	11	14	55	7	7	7	21	3.17 60.32
	ATORE ROHAN VILAS	13	7 2	7	10	37	6	6	6	18	43.65
	HUMKAR ANUJA ANIL	11	3	10	3	27	3	3	3	1	7.14
	ONDAWALE PUNAM BHASKA	14	7	9	6	36	6	6	5	18	28.57 42.86
	ORHALE MAYUR VITTHAL ALI ANKITA SUNIL	0	0	7	0	13	3	3	3	9	17.46
M	ANE DESHMUKH	V		0	0	0	1	0	1	2	1.59
	GVUAYSINH UDAYSINH ANISH KUMAR MISHRA	3	2	2	1	9	3	3	3	9	14.29
	MKAR DATTATRAY KALBHOR	5	2	4	0	6	1	1	1	3	7.14
	ABALKAR VAISHNAVI KIRAN	9	5	3	4	21	3	3	3	9	8.73
	ARATE PIYUSH DEEPAKRAO	5	6	- 0	0	11	1	0	1	1	23.81 10.32
	ATIL CHETANA ALIAS VARSH. ATIL VAISHNAVI ASHOK	21 14	17	18	18	74	6	6	6	18	73.02
347 PA	AWANE VANDANA VIJAY	13	8	4	12	39	3	3	3	5	34.92
	NGAT ADVAIT VINAYAK	0	2	0	0	2	3	0	3	6	36.51 6.35
	RINCE SINGH RIYOTKARSH DWIVEDI	15	14	13	11	53	5	4	4	13	52.38
351 PU	IRI ASHUTOSH NARAYAN	6	4	13	10	17	3	1	2	5	42.06
	JRNIMA DHURWEY	14	9	11	10	44	5	3	3 5	13	18.25
	U RANJAN MPUERE VISHWESH LINGRA	4	7	7	6	31	2	2	2	6	45.24 29.37
	PATWAR SHANTANU PRADI	2	1	3	0	9	2	2	2	6	11.90
	DHIT RANA	14	12	9	6	3 41	2	1	2		5.56
	KME SRUSHTI GOVIND HANE RUTURAJ PRAMOD	11	4	9	8	32	2	4	2	5 8	36.51 31.75
	ROLKAR GARGI SANJAY	7	7	8	0	11	3	0	0	3	11.11
360 SAT	TOTE JAYESH SANJIV	8	6	0	6	32	2 2	0	2	5	29.37
	AIKH FAHED AKIL	14	10	10	10	44	5	5	2	12	12.70
	AIKH SAMEER KHAJAMIYA INDE NUTAN BALASAHEB	10	3 5	5	3	- 18	2	2	2	6	19.05
364 SHI	INDE RUTUJA PRAVIN	12	8	7	10	30 41	3	3	3	9	30.95
	NDE SANIL SANJAY	13	8	7	7	35	5	3 2	3	9	39.68
	NDE SNEHA ABASAHEB NWANE ANIKET VILAS	9	7	4	5	24	3	2	3	8	35.71 25.40
368 SUE	BODH GAJANAN DHOKE	10	7	2	5 2	25 21	3	0	3	6	24.60
369 SUF	RYAWANSHI MAYURI SATIS	15	12	15	13	56	5	2	3	8	23.02
	TAR TUSHAR RAJENDRA	2	1	0	0	3	1	0	0	12	3.17
	THE BHAGYASHREE SOMNA DRAT SAHIL SHIVAJI	1 11	8	5	0	2	0	0	0	0	1.59
373 VAH	HILE MONDIP SURESH	2	1	2	5	30 5	2	0	2	7	29.37
	GHMARE SHUBHAM SURES	4	1	2	3		ol de	1	0 4	9	6.35
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Pro	Shital Patil					1 0 No	9307 SA	1		nad)	
	ss Teacher		lita	School	120	1/2/10	Sansi)	11	Dr. Sharan HoD-F	Inamdar	
			1	E &TC	emgineering	RINCIPA PSOE, P		nkya D	HO TC Eng	D ineering hool of Eng	jineeri



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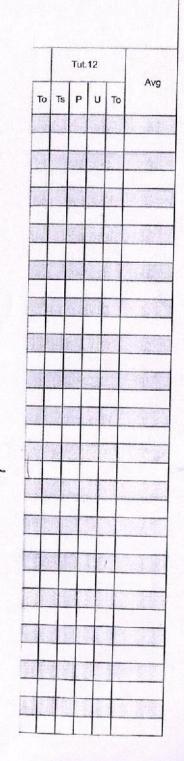
Department of E&TC Engineering

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Continuous Assessment Record

PRACTICAL RECORD B1 Batch Attendance Name of the Student Roll No. 1 2 3 4 5 7 8 9 10 11 12 13 6 ADEPU Rithish B Akash Kide Ashish S. Jagdhane Baber Batik & 4 Barrock Deep R. 5 PPPP Bhale Anch R Bhogulkas Akarb D Charline Vaistnavi D Chikane Shubham Kalyoun Chotalia Hardik H 10 Dafetar Adil Shabbir Derai Swarupand 8 Ohanve Janhavi S 13 Staygude Sonal B. fahma Gafur S P 15 Gaikwad Swit Rajasam Grangathack Nikita B Gausi Popat more Habure Pratiks ha A PP 20 Hingmine Ray R Ingale Vaishmui V-22 Jadhau Abhay 3 Indhan Abhishek A. 23 Abhister Mrupy O Jadhav Jadhar Suhas Sum Patil So 25 Signature of Faculty PRINCIPAL ADYPSOE, PUNE

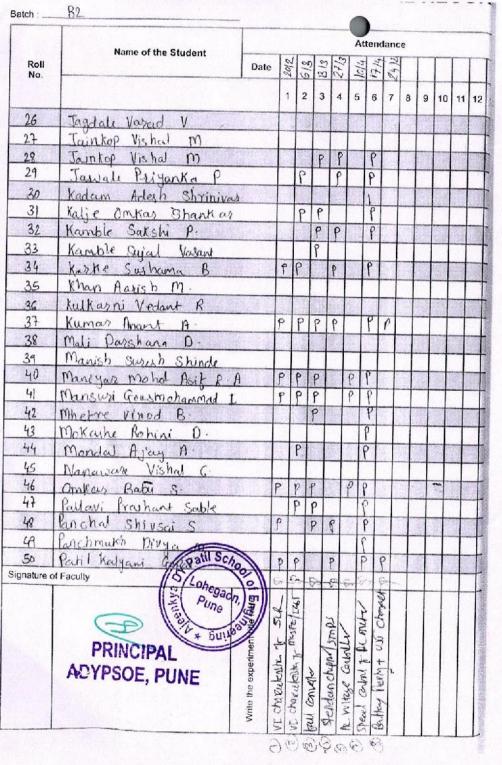
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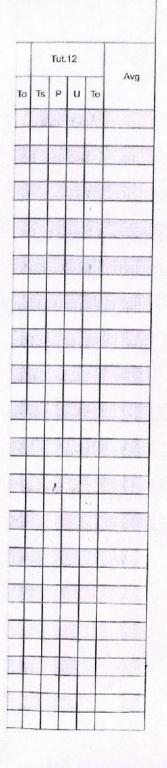


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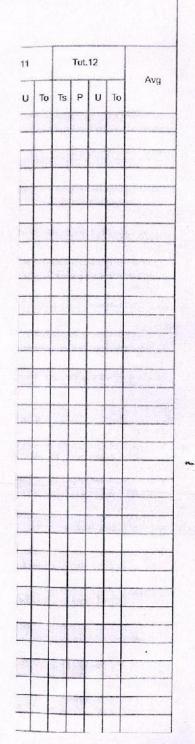


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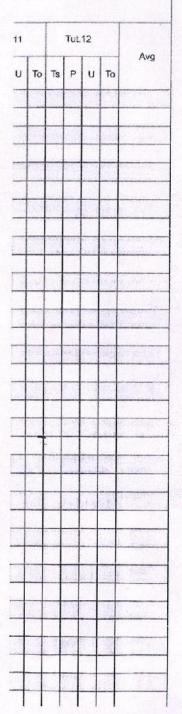
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"Empowerment through quality technical education"

AJEENKYA DY Patil School of Engineering

Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohgaon, Pune – 412 105

Department of E&TC Engineering

15

Unit Test I: Question Paper, Attendance Record, Result



Ajeenkya D Y PATIL SCHOOL OF ENGINEERING

Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune – 412 105.

Department of E&TC Engineering

Unit Test I Question Paper

A.Y.: 2022-2023

Semester: II

Class: T.E.

Div:- A

Name of Subject: Power Devices & Circuits

Date of Examination: 15/03/2023

Total Marks: 30

Duration: 1 hour

Instructions:

- 1. Answer Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6, Q.7 or Q.8, Q.9 or Q.10.
- 2. Neat diagram must be drawn whenever necessary.
- 3. Assume suitable data whenever necessary.
- Q.1 Draw and explain the static V-I Characteristics of SCR

6M

OR

Q.2 If a semi converter is operating from 230V/50Hz supply, driving a load resistance of 10 Ω .

Find: i) Avg output power ii) RMS output power iii) Rectification efficiency

6M

Q.3 Draw and explain the static V-I Characteristics of GTO

6M.

OR

Q.4 If a single phase full converter is operating from 230V, 50 Hz Supply. The load is Find: i) Avg load voltage ii) Average load current. $R=10\Omega$, $\alpha=45^{\circ}$

Q.5 Explain the importance of series and parallel connection of SCR

6M

OR

Q.6 Explain the concept of line and forced commutation.

6M

OR

Q.7 Distinguish between Power MOSFET & Power IGBT

6M

OR

Q. 8 Draw & explain single phase semi converter with waveforms

6M

Q.9 Draw & explain three phase full converter with R Load &

6M

OR

Q. 10 Write a short note on effect of source inductance on single phase full converter. 6M

011.2



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Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune – 412 105.

Department of E&TC Engineering

Unit Test I Question Paper

A.Y.: 2022-2023

Semester: II

Class: T.E.

Div:- B

Name of Subject: Power Devices & Circuits

Date of Examination: 14/03/2023

Total Marks: 30

Duration: 1 hour

Instructions:

1. Answer Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6, Q.7 or Q.8, Q.9 or Q.10.

2. Neat diagram must be drawn whenever necessary.

3. Assume suitable data whenever necessary.

Q.1 Draw and explain the static V-I Characteristics of IGBT

6M

OR

Q.2 If a semi converter is operating from 230V/50Hz supply, driving a load resistance of 10 Ω .

Find: i) Avg output power ii) RMS output power iii) Rectification efficiency

6M

Q.3 Draw and explain the static V-I Characteristics of Power MOSFET.

6M

OR

Q.4 If a single phase full converter is operating from 230V, 50 Hz Supply. The load is Find: i) Avg load voltage ii) Average load current. $R=10^{\circ}\Omega$, $\alpha=45^{\circ}$

Q.5 Define Latching Current, Holding Current, di/dt & dv/dt ratings of SCR

6 M

OR

Q.6 Explain the requirement of a typical triggering / driver (such as opto isolator) circuits for various power devices

OR

Q.7 Distinguish between SCR & Power MOSFET

6M

OR

Q. 8 Draw & explain single phase full converter with R-L Load & waveforms

6M

Q.9 Draw & explain three phase full converter with R Load & warfel

6M

OR

Q. 10 Write a note on rectification & inversion made of two quadrant converted

6M

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Dr. D. Y. Patil Group of Institutions' Technical Campus AJINKYA D. Y. PATIL SCHOOL OF ENGINEERING

Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune – 412 105.

Department of E&TC Engineering

A.Y.: 2022-2023 Subject Name:

Semester: II

Class: TE (E & TC)

PSOE, PUNE

Div.: - A

Unit Test 1 Attendance

Roll Number	Student Name	Sign
1	JATTI MONALI NANDLAL	Marale.
2	AJAY PARITOSH DAS	AS
3	AMBEKAR ATHARVA ATUL	Athorns
4	ANSARI ASIM JAHANGIR	Down
5	ASHISH PANDA	Qo-
6	BAGAL MAYUR HANUMANT	1 Bogod
7	BHERDE RUTUJA MANISH	Jutya
8	BHOPLE SUMEET PRALHAD	Sumer
9	BONDLA TARUN	Ars
10	CHASKAR AVISHKAR BALU	Ar
11	CHATE BHAKTI TUKARAM	allvole
12	CHAVAN AISHWARYA ASHOK	
13	CHAVAN PRAJAKTA PRATAP	Pavan
9 2-9	CHOUTEWAR VAISHNAVI	
14	BALAPRASAD	aatshal
15	DEOKAR VIRAJ SANDEEP	VAD
16	DESHPANDE ABHISHEK GANESH	AR
17	DIVYA YUVARAJ SALUNKE	Chimes.
18	DOLSE GAYATRI DEEPAK	
19	GAIKWAD SHUBHAM SANJAY	AB
20	GHEGADE SHAMBHAVI SHIRISH	AL AL
21	GIRDE PRANAY SATYAWAN	
22	GORE PRANAV DIPAK	Diancin
23	GOVIND CHOUDHARY	AB
24	HIPPARAGI HARDIK BASAVARAJ	Must.
25	INGALE OMKAR JANARDHAN	(met)
26	JADHAV ANKITA ARJUN	TO KITE A-5
27	JADHAV TEJAS SANJAY	
28	JADHAV VAIBHAV TATERAO	Harriston 's'
	JAGTAP SHRAVANKUMAR	Codeman
29	DATTATRAY	- Sales
30	JALGAONKAR HAMID NIYAZALI	W C/O
31	KADAM POOJA PRAMOD	Porte
32	KALE VAISHNAVI RAJKUMAR	AB
33	KAMBLE AJINKYA PRADEEP	Mkambe
34	KATE ADITI NAMDEV	Quet school
35	KATORE ROHAN VILAS	STAR AND

36	KHUMKAR ANUJA ANIL	A.A. khumkar
37	KONDAWALE PUNAM BHASKAR	पी.कांडवल
38	KORHALE MAYUR VITTHAL	Major.
39	MALI ANKITA SUNIL	AB
	MANE DESHMUKH DIGVIJAYSINH	TANKS.
40	UDAYSINH	
41	MANISH KUMAR MISHRA	Mauri
42	OMKAR DATTATRAY KALBHOR	(Oukas
43	PABALKAR VAISHNAVI KIRAN	N CUE
44	PARATE PIYUSH DEEPAKRAO	AB
45	PATIL CHETANA ALIAS VARSHA	-2400-
46	PATIL VAISHNAVI ASHOK	Vaishnavi.
47	PAWANE VANDANA VIJAY	Pawane V.V
48	PINGAT ADVAIT VINAYAK	AB
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53	RAJ RANJAN	AB
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55	RAPATWAR SHANTANU PRADEEP	AB.
56	ROHIT RANA	Pers
57	RUKME SRUSHTI GOVIND	Siday'
58	SAHANE RUTURAJ PRAMOD	AA AA
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70	SUTAR TUSHAR RAJENDRA	10
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72	THORAT SAHIL SHIVAJI	AD AD
73	VAHILE MONDIP SURESH	MA
74	WAGHMARE SHUBHAM SURESH	MITS.
14	WAGIIWAKE SHUBHAM SUKESH	AB



Prof. Ryy Kon Subject Incharge

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Lohegaon, Pune



Dr. D. Y. Patil Group of Institutions' Technical Campus AJINKYA D. Y. PATIL SCHOOL OF ENGINEERING

Dr. D. Y. Patil Knowledge Çity, Charholi Bk., Via. Lohegaon, Pune – 412 105.

Department of E&TC Engineering

A.Y.: 2022-2023

Semester: II

Class: TE (E & TC)

Div.: - B

Subject Name:

Unit Test 1 Attendance

Roll Number	Student Name	Sign
1	ADEPU RITHISH BHUPENDRA	Kethich
2	AKASH KUTE	A
3	ASHISH SUDHAKAR JAGDHANE	elush .
4	BABAR PRATIK SATISH	(Part) Company
5	BANSODE DEEP BALAJI	ance
6	BHALE ANSH RAJESH	AL.
7	BHOGULKAR AKASH DHONDIBA	1 Kall
8	CHATURE VAISHNAVI DNYANDEV	Mature
9	CHIKANE SHUBHAM KALYAN	-8KC
10	CHOTALIA HARDIK HASMUKH	Hardik.
11	DAFEDAR ADIL SHABBIR	A dit.
12	DESAI SWARUPANAND SANJAY	261
13	DHANVE JANHAVI SANTOSH	Jan
14	DHAYGUDE SONALI BHARAT	0~4
15	FATIMA GAFUR SHAIKH	Al
16	GAIKWAD SUJIT RAJARAM	1.500
17	GANGATHADE NIKITA BHARAT	M
18	GAURI POPAT MORE	AN AM
19	HASURE PRATIKSHA ANANDRAO	Jan.
20	HINGMIRE RAJ RAM	A
21	INGALE VAISHNAVI VIJAY	A
22	JADHAV ABHAY SUNIL	
23		・レ質
24	JADHAV ABHISHEK ANIL	Jadharas.
25	JADHAY MRUNAL DEEPAK	Fadhal.
26	JADHAV SUHAS SAMPAT	· Janhas
27	JAGDALE VARAD VIKAS	Wadale
28	JAINKOP VISHAL MALLESH	AT AT
29	JASUD GAYATRI PRAVIN	fasting
30	JAWALE PRIYANKA PRAKASH	Prowale
	KADAM ADESH SHRINIVAS	JASA
31	KALJE OMKAR SHANKAR	Skoy's -
32	KAMBLE SAKSHI PUNDLIK	Ry
33	KAMBLE SUJAL VASANT	dual
34	KARKE SUSHAMA BHIMASHANKAR	
35	KHAN AARISH MAROOF	sushma.
36	KULKARNI VEDANT RAMESH	
37	KUMAR ANANT ARVIND	St. AB
38	MALI DARSHANA DATTATRAV	
39	MANISH SURESH SHINDE	04 m 14
40	MANIYAR MOHD ASIF RIYAZ AHMED	Maray Pattison

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41	INAMULLA	(C)	
42	MHETRE VINOD BASAPPA	(ROOD)	
43	MOKASHE ROHINI DAGDUBA	(D)	
44	MONDAL AJAY AMAR	AIT	
45	NANAWARE VISHAL CHANDRAKANT	AS	
46	OMKAR BABU SESHADRI	000	
47	PALLAVI PRASHANT SABLE	17.11	
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50	PATIL KALYANI GOPAL	1000	M
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52	PATIL VAISHALI KADUBA		AD
53	PAWAL AKASH DADARAO	Del -	-As
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55	PAWAR KOMAL BHAUSAHEB	Brawc .	AN
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72	VISHAKHA RAOSAHEB PATIL	Galshnow	
73	WALZADE ABHISHEK BALASAHEB	ORBA_	AD



Prof. Ryay Kan Subject Incharge

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Dr. D. Y. Patil Group of Institutions' Technical Campus AJINKYA D. Y. PATIL SCHOOL OF ENGINEERING Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune – 412 105. Department of E&TC Engineering

From No. IQAC/23 (E)

A.Y.: 2022-23

Class: TE 🚜

Department of Electronics and Telecommunication Engineering

Result Analysis of Unit Test - 1

Sr.No	Roll No	Name of Student	Power Devices & Circuits			
Srivo	Kell No	Name of Student	Unit- 1 Marks (Q.1/Q.2)	Unit- 2 Marks (Q.3/Q.4)	Total Mark	
1 -	301	JATTI MONALI NANDLAL	8	6	- 14	
2	302	AJAY PARITOSH DAS	AB	AB	AB	
3	303	AMBEKAR ATHARVA ATUL	9	. 7	16	
4	304	ANSARI ASIM JAHANGIR	8	6	14	
5	305	ASHISH PANDA	9	5	14	
6	306	BAGAL MAYUR HANUMANT	6	5	11	
7	307	BHERDE RUTUJA MANISH .	10	8	18	
8	308	BHOPLE SUMEET PRALHAD	11	7	18	
	309	BONDLA TARUN	AB	AB	AB	
10	310	CHASKAR AVISHKAR BALU	AB 🖚	AB	AB	
11	311	CHATE BHAKTI TUKARAM	9	7	16	
12	312	CHAVAN AISHWARYA ASHCK	10	- 7	17	
13	313	CHAVAN PRAJAKTA PRATAP CHOUTEVVAR VAISHNAVI	. 5	4	9	
14	314	HALADDAGAD.	11	9	20	
15	315	DEOKAR VIRAJ SANDEEP	9	6	15	
16	316	DESHPANDE ABHISHEK GANESH	AB	AB	AB	
17	317	DIVYA YUVARAJ SALUNKE	7	7	14	
18	318	DOLSE GAVATRI DEEPAK	9	10	19	
19	319	GAIKWAD SHUBHAM SANJAY	AB	AB	AB	
20	320	GHEGADE SHAMBHAVI SHIRISH	AB	A8 -	AB	
21	321	GIRDE PRANAY SATYAWAN	7	6	13	
22	322	GORE PRANAV DIPAK	8	6	14	
23	323	GOVIND CHOUDHARY	AB	AB	AB	
24	324	HIPPARAGI HARDIK BASAVARAJ	8	5	13	
25	325	INGALE OMKAR JANARDHAN	9	7	16	
26	326	JADHAV ANKITA ARJUN	9	5	14	
27	327	JADHAV TEJAS SANJAY	10	8	18	
28	328	JADHAV VAIBHAV TATERAC	9	6	15	
29	329	DATTATRAY	8	6	14	
30	330	JALGAONKAR HAMID NIYAZALI	10	9	19	
31	331	KADAM POOJA PRAMOD	11	9	20	
32	332	KALE VAISHNAVI RAJKUMAR	A3	AB		
33	333	KAMBLE AJINKYA PRADEEP	7		AB	
34	334	KATE ADITI NAMDEV	9	5	12	
35	335	KATORE ROHAN VILAS	AB	7	16	
36	336	KHUMKAR ANUJA ANIL		AB	AB	
37	337	KONDAWALE PUNAM BHASKAR	10	. 8	18	
38	338	KORHALE MAYUR VITTHAL	9	9	18	
39	339	Ankita Sunii Mali	10	8	. 18	
40	340	MANE DESTINATION DIGITIZATION	AB	A8	AB	
41	341	MANISH KUMAR MISHRA	11 -	9	20	
42	342	OMKAR DATTATRAY KALBHOR	8	5 .	13	
43	343	PABALKAR VAISHNAV: KIRAN	7	5	12	
44	344	PARATE PIYUSH DEEPAKRAD	9	6	15	
45	345		AB	AB	AB	
46		PATIL CHETANA ALIAS VARSHA	10	9	19	
47	346	PATIL VAISHNAVI ASHOK	- 11	13	24	
-	347	PAWANE VANDANA VIJAY	8	10	18	
48	348	PINGAT ADVAIT VINAYAK	AB	AB	AB	
50	170000000000000000000000000000000000000	PRINCE SINGH	8	5	13	
	350	PRIYOTKARSH DWIVEDI	- 6	6	12	
51	351	PURI ASHUTOSH NARAYAN	9	5	15	
52		PURNIMA DHURWEY	10	7	17	
53		RAJ RANJAN	AB	AB	AB	
54		RAMPUERE VISHWESH LINGRAJ	9	7	16	
55	31123.32	RAPATWAR SHANTANU PRADEEP	AB	AB	AB	
56		ROHIT RANA	8	8	16	
57	-	RUKME SRUSHTI GOVIND	11	12	23	
58		SAHANE RUTURAJ PRAMOD	AB	- AB	AB	
59		SAROLKAR GARGI SANJAY	12	11	. 23	
60	360	SATOTE JAYESH SANJIV	AB -	AB	AB	
61	361	SHAIKH FAHED AKIL	10	11	21	
62	362	SHAIKH SAMEER KHAJAMIYA	11	10	21	
63		SHINDE NUTAN BALASAHEB	12	10	22	
64		SHINDE RUTUJA PRAVIN	11	10	21	
65		SHINDE SANIL SANJAY	9	9	18	
66		SHINDE SNEHA ABASAHEB	11	10	21	
67		SONWANE ANIKET VILAS .	10			
68		SUBODH GAJANAN DHOKE	9	9	19	
69		GURYAWANSHI MAYURI SATISH	10	6	15	
70		SUTAR TUSHAR RAJENDRA		10	20	
71		ATHE BHAGYASHREE SOMNATH	A8 -	AB	AB	
72			AB	AB	AB	
73		HORAT SAHL SHIVAJI	9	- 10	19	
74		AHILE MONDIP SURESH .	7	11	18	
		VAGHMARE SHUBHAM SURESH	AB	AB	AB	

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Approved Ajeenkya DY Patil School of Engineering
Lohegaon, Pune

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From No. IQAC/23 (E)

A.Y.: 2022-23

Result Analysis of Unit Test - 1

Department of Electronics and Telecommunication Engineering

			Power	Devices & Circu	its
Sr.No	Rall No	Name of Student	Unit- 1 Marks (Q.1/Q.2)	Unit- 2 Marks (Q.3/Q.4)	Total Marks
1	1-	ADEPU RITHISH BHUPENDRA	11	10	21
2	2	AKASH KUTE	AB	AB	AB
3	4	ASHISH SUDHAKAR JAGDHANE	10	12	- 22
5	5	BABAR PRATIK SATISH BANSODE DEEP BALAJI	13	9 11	16 24
6	6	BHALE ANSH RAJESH	10	9	19
7	7	BHOGULKAR AKASH DHONDIBA	- 6	5	11
8	8	CHATURE VAISHNAVI DNYANDEV	8	11	19
9	9	CHIKANE SHUBHAM KALYAN	10	8	18
10	10	CHOTALIA HARDIK HASMUKH	11	10	21
11	11	DAFEDAR ADIL SHABBIR	7	7	- 14
12	12	DESAI SWARUPANAND SANJAY	10	. 8	18
13	13	DHANVE JANHAVI SANTOSH	7 -	11	18
14	14	DHAYGUDE SONALI BHARAT	9	10	19
16	15	FATIMA GAFUR SHAIKH	AB	AB	AB
17	17	GAIKWAD SUJIT RAJARAM	6	5	11
18	18	GANGATHADE NIKITA BHARAT GAURI POPAT MORE	AB	AB	AB
19	19	HASURE PRATIKSHA ANANDRAO	6 AB	4	10
20	20	HINGMIRE RAJ RAM	AB	AB AB	AB AB
21	- 21	INGALE VAISHNAVI VIJAY	9	10	19
22	22	JADHAV ABHAY SUNIL	11	9	20
23	23	JADHAV ABHISHEK ANIL	10	12	22
24	24	JADHAV MRUNAL DEEPAK	9	10	19
25	25	JADHAV SUHAS SAMPAT	10	7	17
26	26	JAGDALE VARAD VIKAS	11	10	21 -
27	27	JAINKOP VISHAL MALLESH	AB	AB	AB
28	28	JASUD GAYATRI PRAVIN	10	7	17
29	29	JAWALE PRIYANKA PRAKASH	9	11	20
30	30	KADAM ADESH SHRINIVAS	6	S	11
31	31	KALJE OMKAR SHANKAR	7	6	13
32	32	KAMBLE SAKSHI PUNDLIK	8	10	18
33	33	KAMBLE SUJAL VASANT	13	11	24
35	35	KARKE SUSHAMA BHIMASHANKAR	8	10	18
36	36	KHAN AARISH MAROOF KULKARNI VEDANT RAMESH	10	10	20
37	37		AB	AB	AB
38	38	KUMAR ANANT ARVIND MALI DARSHANA DATTATRAY	12	11	23
39	39	MANISH SURESH SHINDE	AB	AB	AB
40	40	MANIYAR MOHD ASIF RIYAZ AHMED	11	10	21
41	41	MANSURI GOUSMOHAMMAD INAMULLA	10	11	21
42	42	MHETRE VINOD BASAPPA	12	11	23
43	43	MOKASHE ROHINI DAGDUBA	9	6	15
44	. 44	MONDAL AJAY AMAR	13	10	23
45	45	NANAWARE VISHAL CHANDRAKANT	AB	AB	AB
46	46	OMKAR BABU SESHADRI	9	7	16
47	47	PALLAVI PRASHANT SABLE	14	10	24
48	48	PANCHAL SHIVSAI SUNIL	11	13	24
49	49	PANCHMUKH DIVYA MAHESH	12	11	23
50	50	PATIL KALYANI GOPAL	AB	AB	AB
51	51	PATIL PALLAVI SHRIKRISHNA	AB	AB	AB
52	52	PATIL VAISHALI KADUBA	AB	AB	AB
53	53	PAWAL AKASH DADARAO	13	10	23
55	54	PAWAR ADITEE RAMDAS	11	13	24
56	55	PAWAR KOMAL BHAUSAHEB	AB	AB	AB
57	57	PRATIK GAJANAN SADAR	10	. 8	18
58	58	RAJPUT PRATIK SINGH SUBHASH SINGH RATHOD AJAY SHANTIRAM	11 AB	10	21
59	59	RAUT TUSHAR DILIP	12	AB 10	AB
60	60	ROKADE VAISHNAVI DIGAMBAR	12	9	22
61	61	SALALKAR MAYA RAVINDRA	13	9	22
62	62	SAURABH VIJAY JADHAV	6	5	11
63	63	SAYYAD ARSHAD PAIGAMBAR	11	9	- 20
64	64	SUSHMA SURESH SUPEKAR	10	6	16
65	65	SWAMI RUTUJA VIRBHADRA	10	8	18
66	66	TAKALE KIRAN INDRARAJ	9 .	9	18
67	67	THAKKANNAVAR BHAIRVNATH BHIMANN	7	5	12
68	68	KIRAN GANPATRAD UPASE	AB	AB .	AB
69	69	VAIBHAVI SUNUL NIMBALKAR	- 12	11	23
70 1	70	VAISHANAVI TULSHIRAM SAVARGAVE	AB	AB	AB
70					
71	71	VAISHNAVI JANARDHAN MORE	11	12	23
	71 72 73	VAISHNAVI JANARDHAN MORE VISHAKHA RAOSAHEB PATIL WALZADE ABHISHEK BALASAHEB	11 AB T 8	12 AB 7	23 AB 15



Dr. S.C.Inamadar HOD
HODE & TC Engineering
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Lohegaon, Pune

Lohegaon,



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Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohgaon, Pune – 412 105

Department of E&TC Engineering

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Unit Test II: Question Paper, Attendance Record, Result



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Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune - 412 105.

Department of E&TC Engineering

Unit Test (II) Question Paper

Form No. IQAC/18b.

A.Y.: 2022-2023

Semester: II

Class: TE-A (E&TC)

Name of Subject: PDC

Date of Examination: 03/05/2023

Total Marks: 30 Marks

Duration: 1 hours

Instructions (if any):

1) Solve Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6

- 2) Figures to right indicate full marks.
- 3) Assume suitable data if necessary.
- Q.1 Explain with waveforms Single phase full bridge square wave inverter for R load using MOSFET

OR

Q.2 Draw & explain step down chopper with R-L Load & waveforms

(10)

Q.3 What are the control strategies of Inverter? Explain any two.

(10)

OR

Q.4 List the SMPS Topologies. Explain any two.

(10)

Q.5 Compare 180° and 120° conduction mode of Inverter

(10)

OR

Q.6 Write short note on:

i) LM3524 based chopper

ii) Concept of maximum power point tracking (MPPT).

(10)

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Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune - 412 105.

Department of E&TC Engineering

Unit Test (II) Question Paper

Form No. IQAC/18b.

A.Y.: 2022-2023

Semester: II

Class: TE-B (E&TC)

Name of Subject: PDC

Date of Examination: 03/05/2023

Total Marks: 30 Marks

Duration: 1 hours

Instructions (if any):

1) Solve Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6

2) Figures to the right indicate full marks.

3) Assume suitable data if necessary.

Q.1 Explain with waveforms Single phase full bridge square wave inverter for R-L load using IGBT (10)

OR

Q.2 Draw & explain step down chopper with R Load & waveforms

(10)

Q.3 What are the control strategies of the Inverter? Explain any two.

(10)

OR

Q.4 List the SMPS Topologies. Explain any two.

(10)

Q.5 Compare 180° and 120° conduction modes of Inverter

(10)

OR

Q.6 Write short note on:

i) LM3524 based chopper

ii) Concept of maximum power point tracking (MPPT).

T.E. (ESTC) PDC Unit-Test-II

3\5\2023 - T.E.A

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1)	368	Subodh Ohoke	
2)	340	Digrijaysinh Mane Deshmakt	THE STATE OF THE S
3)	338	Mayur Vitthal Korhale	
4)	342	Omkor Dattatray Kalbho	
57	361	Fahed AKI Shorks	for
67	336	Anuja khumkar	A-A-Khumkan
7)	357	south Rukme	Sexul.
8]	303	Athatva Ambekaz	House
97	324	Harolik Hipparagi	But.
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(1)	361	Asherosh Piri	Africa .
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13)	329	Jagtap Shravankuman	A STORE
147	374	Maghmare Shubham	-Sef
15	353	Ray Ranjan	Ray Regin
16	367	Aniket V- Sonware	compusue
17	352	Purnima Dhumey	Juline
18	365	Purnima Dhumey Shirde Sanil	Linde
019	333	Kamble + Alinkya	- Smbka
20	327	Tejas Sodhau	
ع١.	341	Manish Human Mishra	Manish
22.	321	Preanay Gürde	
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2420	345	Chetana Patil	-46
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29	363	Engineering Shinde Mutan PR	Ajinde
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32	369	Mayuri sunganlansh	194



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PEZ	Roll no.	Name	Sign	^
33.	349	Prince Singh	Roy	-(1
344	356	de politicana	Denne	(0
35.	305	Ashish fanda	Ross	(2
36	316	Abhishek deshpande	Abh	4.1
37	315	Deokor beviraj	100	Te de
38	337	Panam Kondawale	tino	ML.
39	326	Ankita radhav	(3) L	The state of the s
40)	308	Sumeet Bhople	Sun	uel5
41)	1354	Vishwesh Rampure	Pah	
420	372	Sahil Thorat	fred	8.
43)	1322	CHOSE BROWN	D-coor	uf,
44)	348	Vaishnavi Pabalkax	(Vay)	<u>(*)</u>
45)	313	Prajakta Chavan	Trave	un-
.)	359	Crange sanolicais	- garaj	. Co1
1	346	Vaishnavi Patil	Walder War	15
48)	312	Aishwarga chavan	-Aietanos	31
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	T.E.(B) PDC Unit-Test-II 4/05/2023
	ATTENDANCE
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	Sr Roll Mo. Name Sign
V.	1 14 sonali B. Dhaygude II
	2 10 60 Wolles Rokade Naishnaur & Rakade
	3. 47 Pallari Sable Pallari
3	4. 34 Sushma Karke Sushma
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	8. 17 Nikita Gangathade Ditita
	9. 65 suami Rutuja (Putula)
	10. 58 ATAY, Rathod tuy.
	11 12 Swarupanoine Desal Spart
	12 21 Ingale Vaishnavi destre
	13 41 Mansovi Goosmahammad Cha
	14 63 Asshad P Sayrad Sayrad A.P.
	15 42 MHETRE VINOD : B
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	20 Och Bouteld Baiser Butie.
	21. 37 Steatil School Anant kumar
	22. 28 Engineurne Gayatri Jasud Company
	23° 59 Property De Rout
	24 48 Shivsai Panchal Shivsai
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5 44	Sr. MO	Roll Mo	Mane	Sign
3-8	26	61	Maya Ravindra Solutor	THEYO.
	27	49	Divya. M. Panchmukh	Hiza.
	08	69	Vaibhari S. Nimbalkar	# Nimbas
	29	50	Pratik. Gr. Sadoz.	R
1.0	30	08	Vaishnavi Chature	Thatur
	31	51	Patti Pallavi	Satis
	32	13	Janhari Dhanve	ferris
	33 0106	02	Akash kuje	(Mar)
	84	26	Varad Jagdale	yadale
	35	15	Suhal Jouthar	3 .
	36	01	Rithish Adepu	Kithish
	37	67	Thakkonnavar. B.T.	
	38	53	Akash D. Pawai	gay,
	39	16	Sujit Gaikwad	Smit ,
	40	19	Hasyre Prafiksha	Hasirel
= N	41	39	Manish shinde	Medias
	42	72	Vishakha Patil	Paril
9	43/102	54	Pawoe Adilee	Mur.
	44-11	57	Rayput Pratiksjogh	Pout PK.
	45	05	Deep Bomsode	Dich
	46	68	Kiean Upase	and I
	47	44	Ajay Mondal and	(A) hy
	48	06	Ansh. Bhale .	W.
	99	24	mrunal Jadhar Josii Scro	Fadhay.
	50	10	Hardik Chotalia	raila
	51	3	Ashish Jagdhane Engineer.	3 mildi
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Dr. D.Y. Patil Group of Institutions' Technical Campus AJINKYA D. Y. PATIL SCHOOL OF ENGINEERING Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune - 412 105. Department of E&TC Engineering

From No. IQAC/23 (E)

Department of Electronics and Telecommunication Engineering 022-23 Class: TE • A

A.Y.: 2022-23

Result Analysis of Unit Test - II

Sr.No	Rell No	Name of Student	Power Devices & Circuits		
			Unit- 1 Marks (Q.1/Q.2)	Unit- 2 Marks (Q.3/Q.4)	Total Mark
1	301	JATTI MONALI NANDLAL	14	20	34
3	302	AJAY PARITOSH DAS	AB	AB	AB
4	303 304	AMBEKAR ATHARVA ATUL ANSARI ASIM JAHANGIR	16	22	38
5.	305	ASHISH PANDA	14	AB	14
6	306	BAGAL MAYUR HANUMANT	11	16	30 27
7	307	BHERDE RUTUJA MANISH	18	AB	18
8	308	BHOPLE SUMEET PRALHAD	18	15	33
9	309	BONDLA TARUN	AB	AB	AB
10	310	CHASKAR AVISHKAR BALU	AB	AB	AB
11	311	CHATE BHAKTI TUKARAM	16	AB	16
12	312	CHAVAN AISHWARYA ASHOK	17	20	37
13	313	CHAVAN PRAJAKTA PRATAP	9	16	25
14	314	BALADRASAD	20	25	45
15	315	DECKAR VIRAJ SANDEEP	15	20	35
16	316	DESHPANDE ABHISHEK GANESH	AB T	19	19
17	317	DIVYA YUVARAJ SALUNKE	14	18	32
18	318	DOLSE GAYATRI DEEPAK	19	24	43
19	319	GAIKWAD SHUBHAM SANJAY	AB	AB	AB
20	320	GHEGADE SHAMBHAVI SHIRISH	AB	AB	AB
21	321	GIRDE PRANAY SATYAWAN	13	17	30
22	322	GORE PRANAV DIPAK	14	. 16	30
23	323	GOVIND CHOUDHARY	AB	AB	AB
24	324	HIPPARAGI HARDIK BASAVARAJ	13	16	29
25	325	INGALE OMKAR JANARDHAN	16	16	32
26	326	JADHAV ANKITA ARJUN	14	16	30
27	327	JADHAV TEJAS SANJAY	18	23	41
28	328	JADHAV VAIBHAV TATERAO JAGTAP SHRAVANKUMAK	15	AB	15
30	329	DATTATRAY	14	16	30
31	330	JALGAONKAR HAMID NIYAZALI	19	A8	19
32	331	KADAM POCJA PRAMOD	20	AB	- 20
33	332	KALE VAISHNAVI RAJKUMAR	AB	AB	AB
34	333	KAMBLE AJINKYA PRADEEP	12	20	32
35	334	KATE ADITI NAMDEV	16	21	37
36	335	KATORE ROHAN VILAS KHUMKAR ANUJA ANIL	AB	AB	AB
37	337	KONDAWALE PUNAM BHASKAR	18	24	42
38	338	KORHALE MAYUR VITTHAL	18	18	36
39	339	Ankita Sunii Meli	AB	20	38
40	340	MANE DESHMUKH DIGVIJAYSINH	20	20	AB 40
41	341	MANISH KUMAR MISHRA	13	22	35
42	342	OMKAR DATTATRAY KALBHOR	12	24	36
43	343	PABALKAR VAISHNAVI KIRAN-	15	17	32
44	344	PARATE PIYUSH DEEPAKRAD	AB	AB	AB
45	345	PATIL CHETANA ALIAS VARSHA	19	27	46
46	346	PATIL VAISHNAVI ASHOK	24	27	51
47	347	PAWANE VANDANA VIJAY	18	AB ·	18
48	348	PINGAT ADVAIT VINAYAK	AB	* AB	AB
49	349	PRINCE SINGH	13	AB	13
50	350	PRIYOTKARSH OWIVEDI	12	AB	12
51	351	PURI ASHUTOSH NARAYAN	15	23	38
52	352	PURNIMA CHURWEY	17	. 24	41
53	353	RAJ RANJAN	AB	16	16
54	354	RAMPUERE VISHWESH LINGRAJ	16	21	37
55	355	RAPATWAR SHANTANU PRADEEP	16	AB	16
56	356	RCHIT RANA	23	15	38
57	357	RUKME SRUSHTI GOVIND	23	24	47
58	358	SAHANE RUTURAJ PRAMOD	- AB	AB	AB
59	359	SAROLKAR GARGI SANJAY	23	25	48
60	360	SATOTE JAYESH SANJIV .	AB	AB	AB
61	351	SHAIKH FAHED AKIL	21	26	47
62	362	SHAIKH SAMEER KHAJAMIYA	21	A8	21
63	363	SHINDE NUTAN BALASAHEB	- 22	23	45
64	364	SHINDE RUTUJA PRAVIN	21	19	40
65	365	SHINDE SANIL SANJAY	18	18	36
66	366	SHINDE SNEHA ABASAHEB	21	22	43
67	367	SONWANE ANIKET VILAS	19	19	38
68	358	SUBODH GAJANAN DHOKE	15	19	34
69	369	SURYAWANSHI MAYURI SATISH	20	24	44
70	370	SUTAR TUSHAR RAJENDRA	AB	AB	AB
71	371	TATHE BHAGYASHREE SOMNATH	AB	AB	AB
72	372	THORAT SAHIL SHIVAJI	19	20	39
73	373	VAHILE MONDIP SURESH -	16	AB	16
74	374	WAGHMARE SHUBHAM SURESH	AB	25	25

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Ajeenkya DY Patil School of Engineering
Lohegaon, Pune

PRINCIPAL ADYPSOE, PUNE

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Dr. D. Y. Patil Group of Institutions' Technical Campus AJINNYA D. Y. PATIL SCHOOL OF ENGINEERING Dr. D. Y. Patil Knowledge City, Charholi Ek, V.a. Lohegaon, Pune - 412 105. Department of E&TC Engineering

From No. IQAC/23 (E)

A.Y.: 2022-23

Div: B

Result Analysis of Unit Test - II

Department of Electronics and Telecommunication Engineering

C- N		INO Non-FOLL	Power Devices & Circuits		
Sr.No	Roll No	Name of Student	Unit- 1 Marks (Q.1/Q.2)	Unit- 2 Marks (Q.3/Q.4)	Total Marks
1	1	ADEPU RITHISH BHUPENDRA	21	25	46
3	3	AKASH KUTE	AB	13	13
4	4	ASHISH SUDHAKAR JAGDHANE BABAR PRATIK SATISH	22 16	21 AB	43
5	5	BANSODE DEEP BALAJI	24	25	16
6	6	BHALE ANSH RAJESH	19	26	45
7	1	BHOGULKAR AKASH DHONDIBA	11	AB	11
8	8	CHATURE VAISHNAVI DNYANDEV	19	27	45
9	9	CHIKANE SHUBHAM KALYAN	18	14	32
10	10	CHOTALIA HARDIK HASMUKH	21	22	43
12	11	DAFEDAR ADIL SHABBIR DESAI SWARUPANAND SANJAY	14	AB	14
13	13	DHANVE JANHAVI SANTOSH	18	14	32
14	14	DHAYGUDE SONALI BHARAT	19	26	45
15	15	FATIMA GAFUR SHAIKH	AB	AB	AB
16	16	GAIKWAD SUJIT RAJARAM	11	17	28
17	17	GANGATHADE NIKITA BHARAT	AB	24	24
18	18	GAURI POPAT MORE	10	A8	10
19	19	HASURE PRATIKSHA ANANDRAO	AB	22	22
20	20	HINGMIRE RAJ RAM	AB	AB -	AB
21	21	INGALE VAISHNAVI VIJAY	19	24	43
22	22	JADHAV ABHAY SUNIL	20	AB	20
24	24	JADHAV ABHISHEK ANIL JADHAV MRUNAL DEEPAK	22	AB	22
25	25	JADHAV MRUNAL DEEPAK JADHAV SUHAS SAMPAT	19	18 25	37
26	26	JAGDALE VARAD VIKAS	21	16	43
27	27	JAINKOP VISHAL MALLESH	A8	AB	AB
28	28	JASUD GAYATRI PRAVIN	17	23	40
29	29	JAWALE PRIYANKA PRAKASH	20	AB	20
30	30	KADAM ADESH SHRINIVAS	11	A8	- 11
31	31	KALJE OMKAR SHANKAR	13	7	20
32	32	KAMBLE SAKSHI PUNDLIK	18	15	33
34	33	KAMBLE SUIAL VASANT	24	25	49
35	35	KARKE SUSHAMA BHIMASHANKAR KHAN AARISH MAROOF	18	19	37
36	36	KULKARNI VEDANT RAMESH	20	AB	20
37	37	KUMAR ANANT ARVIND	AB 23	AB 24	AB 47
38	38	MALI DARSHANA DATTATRAY	AB	AB	AB
39	39	MANISH SURESH SHINDE	21	AB	21
40	40	MANIYAR MOHD ASIF RIYAZ AHMED	22	AB	22
41	41	MANSURI GOUSMOHAMMAD INAMULLA	21	23	44
42	42	MHETRE VINOD BASAPPA	23	17	40
43	43	MOKASHE ROHINI DAGDUBA	15	13	28
45	44	MONDAL AJAY AMAR.	23	16	39
46	45 46	NANAWARE VISHAL CHANDRAKANT	AB	AB	AB
47	47	OMKAR BABU SESHADRI PALLAVI PRASHANT SABLE	23	AB	23
48	48	PANCHAL SHIVSAI SUNIL	16	AB	16
49	49	PANCHMUKH DIVYA MAHESH	24	26	50
50	50	PATIL KALYANI GOPAL	AB AB	25 18	48
51	51	PATIL PALLAVI SHRIKRISHNA	AB.	25	26
52	52	PATIL VAISHALI KADUBA	AB	- AB	AB
53	53	PAWAL AKASH DADARAO	23	22	45
54	54	PAWAR ADITEE RAMDAS	24	24	48
55	55	PAWAR KOMAL BHAUSAHEB	AB	AB	AB
55	56 57	PRATIK GAJANAN SADAR	18	25	43
58	58	RAJPUT PRATIK SINGH SUBHASH SINGH RATHOD AJAY SHANTIRAM	21	24	45
59	59	RAUT TUSHAR DILIP	AB	18	18
60	60	ROKADE VAISHNAVI DIGAMBAR	22	16	38
61	61	SALALKAR MAYA RAVINDRA	20	23	43
62	62	SAURABH VIJAY JADHAV	11	AB	50
63	63	SAYYAD ARSHAD PAIGAMBAR	20	16	36
64	64	SUSHMA SURESH SUPEKAR	16	12	28
65	65	SWAMI RUTUJA VIRBHADRA	18	22	40
66	66	TAKALE KIRAN INDRARAJ	18	AB	- 18
67		THAKKANNAVAR BHAIRVNATH BHIMANN	12	24	36
68 69	68	KIRAN GANPATRAO UPASE	AB	24	24
70	69 70	VAIBHAVI SUNUL NIMBALKAR	23	27	50
71	72.5	VAISHANAVI TULSHIRAM SAVARGAVE	AB ·	25	25
72	72	VAISHNAVI JANARDHAN MORE VISHAKHA RAOSAHEB PATIL	23	AB	23
73		WALZADE ABHISHEK BALASAHEB	A8	24	- 24
74		YADAY ANUSHKA ANIL	15 16	AB AB	15



Dr. S.C.Inamadar
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HoD E.& TC
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Approve, Pune

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Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohgaon, Pune – 412 105

Department of E&TC Engineering

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Subject Result



Dr. D. Y. Patil Technical Campus Dr. D. Y. Patil School of Engineering

Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohegaon, Pune – 412 105 Website: www.dypic.inContact No.:020-6707 7926

Form No:IQAC/6

RESULT ANALYSIS

SPPU Examination: AY 2021-22

Department: E&TC

Date of declaration of result: 12 sept 2022

Overall Result Analysis

Semester: II Class: TE-E&TC

Sr. No.	Details	No. of Students	
1	No. of students appeared	54	
2	No. of students passed(All clear)	41	
3	No. of students failed	13	
4	All clear passing Percentage	75.92	
5	All clear passing Percentage(University)	-	
6	Percentage of passing with ATKT	7.40%	
7	Percentage of passing with ATKT(University)		
8	No. of students passed with Distinction	10	
9	No. of students passed with First Class	11	
10	No. of students passed with Higher Second Class	13	
11	No. of students passed with Second Class	03	
12	No. of students passed with Pass Class	04	
13	No. of students failed in 1 Th. Sub	05	
14	No. of students failed in 2 Th. Subs	03	
15	No. of students failed in 3 Th. Subs	04	
16	No. of students failed in more than 3 Th. Subs	09	
17		School 06	
18	No. of students failed in 2 Pr/Or	9aon, 9 02	
19	No. of students failed in 3 Pr/Or	วันเลืองว่า 01	

A. Subject Wise Result Analysis:

Sr. No.	Name of the Subject	TH/P R	Name of the Staff Member	No. of Students Appeared	No. of Students Passed	% of Passing
1	DIGITAL IMAGE PROCESSING	TH	Dr. S.M.Koli	54	33	61.11
2	POWER DEVICES & CIRCUITS	TH	Prof. Riyaj Kazi	54	34	62.96
3	CELLULAR NETWORKS	ТН	Prof. S.B. Chougale	54	41	75.92
4	PROJECT MANAGEMENT	TH	Dr. Saniya Ansari	54	45	83.33

Sr.		No. of		No. of students obtaining marks in the range						
No.	Subject	students appeared	66 to 100	60 to 65	55 to 59	54 to 40	Name of Topper	Marks Obtained		
1	DIGITAL IMAGE PROCESSING	54	05	02	07	18	VIKESH BHARTI GOSWAMI	73		
2	POWER DEVICES & CIRCUITS	54	08	06	06	17	HANNAMSHETTY ABHIJEET	79		
3	CELLULAR NETWORKS	54	03	07	11	19	CHIRIVELLA LAKSHMIMANASA	73		
4	PROJECT MANAGEMENT	54	09	06	06	23	SHRINIVAS KANKATE VAISHNAVI PRITAM	81		

B. Toppers:

Rank	Examination Seat Number	Name of students	Total Marks out of 700	SGPA	Class Obtained
1	T190883044	SAURAV SUMAN	532	9.36	FCWD
2	T190883024	KAPADNE PRADNYA KISHOR	507	9.19	FCWD
3	T190883029	LAWANDE SEJAL ROHIDAS	500	9.14	FOUDOO

Dr. S.M. Koli

HOD

Head

Department of E&TC Engg.

Dr. D. Y. Patil School of Engg.

Charholi (Bk), Via Longaon, Pune



Dr. F. B. Sayyad
Principal
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Department of E&TC Engineering

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Lab Manual

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Ajeenkya DY Patil School of Engineering, Lohegaon

Department of Electronics & Telecommunication Engg.

LAB MANUAL

Power Devices & Circuits



Department of Electronics & Telecommunication Engineering, A DYPSOE, Lohegaon



Ajeenkya DY Patil School of Engineering, Lohegaon

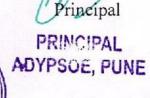
CERTIFICATE

This is to certify that

Mr. / Ms			
Of Class TE		Has completed all	the practical
work in the subje	ct Power Devices &	&Circuits Satisfacto	orily in the
De_I	partment of E&TC	Engineering	
as prescribed by	University of Pune	, in the academic ye	ear 20

Staff In-charge

Head of the Department



Pail School of Fatt.

Engineering Pure

Department of Electronics & Telecommunication Engineering, A DYPSOE, Lohegaon

Power Devices and Circuits

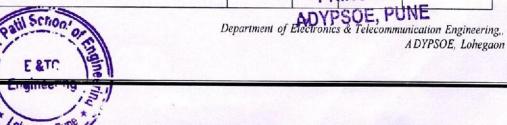


Ajeenkya DY Patil School of Engineering, Lohegaon

INDEX

Name:	Roll No
Class:	Subject:

Sr. No.	Title of Experiment & Assignment		Page No.	Remark	Sign of Teacher
1.	VI Characteristics of SCR i) Plot output V-I characteristics to measure IH, IL				
2.	Drain & Transfer Characteristics of Power MOSFET & IGBT				
3.	To Study UJT Triggering Circuit for SCR	ı			
4.	A single phase semi converter with R & RL Load				•
5.	Single phase Full Converter using IGBT / SCR with R & R-L load				
6.	Step down / Step up chopper using power MOSFET / IGBT				
7.	SMPS /UPS Performance Evaluation i) find load & line regulation characteristics				
8.	Single phase AC voltage controller using IGBT/SCR for R and RL load				Engineering
9.	To study speed control of DC / single phase AC motor				ohegao
10.	To Study Automatic Voltage Regulator and UPS	1		a/	TOPE A A DENT
11.	To study battery testing, safety and maintenance of batteries		PR	INCIPAL	•



Experiment No.: 01

Date:

V-I CHARACTERISTICS OF SCR

AIM: To plot V-I Characteristics & measure IL, IH of SCR

EQUIPMENTS & COMPONENTS:

- 1. Trainer kit
- 2. VARIAC
- 3. 3 Multimeters (2 as ammeters & 1 as a voltmeter)
- 4. 15W or 25W load lamp (230V)
- 5. CRO
- 6. Connectors, etc.

THEORY:

The SCR is a four layer device with three terminals, namely the anode, cathode and gate. When the anode is made positive w.r.t. cathode, junction J2 is reversed biased and only the leakage current will flow through the device. SCR is then said to be in forward biased blocking state or OFF state. When the cathode is made positive w.r.t. anode junction J1 and J3 reverse biased and a small reverse current will flow through SCR. This is the reverse blocking state of the device. When the anode to cathode voltage is increased, the reverse biased junction J2 will breakdown due to large voltage gradient across the depletion layers. This is the avalanche breakdown. Since the other junctions J1 and J3 are forward biased, there will be free carrier movement across the three junctions resulting in a large anode to cathode forward current. The voltage drop across the device will be ohmic drop in the four layers and device is in conduction state or ON state.

If anode to cathode voltage is now reduced, since the original depletion layer and reversed biased J2 no longer remains in conducting state or ON state when the forward current will flow below the level of the holding current IH. The depletion layer will begin to establish across the junction J2 due to the reduced number of carriers and the device will go into the blocking state. Similarly, when the SCR is switched ON, the resulting forward current has to be more than the latching current IL, holding current is usually lower but very close to the latching current, and its magnitude is of the order of mA.

When SCR is reversed biased, the device will behave in the same manner as two diodes connected in series with the reversed voltage applied across them.



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PROCEDURE:

(A) V-I Characteristics:

- Connect a VARIAC at zero position to given terminals (ac input) on trainer kit.
- Connect a 230V, 15W lamp as a load at given terminals.
- 3. Connect one ammeter for anode current I_a and another ammeter for gate current I_g at given terminals.
- 4. Ensuring that both VARIAC & V_g potentiometer are at 0 position, switch on the trainer kit & VARIAC.
- 5. Now increase VARIAC voltage gradually to V_s (V_{AK}) = 100V or 200V, then increase I_g gradually till load activation (glowing of lamp land) & measure load current (I_a). Note these readings as I_{g1} = ---, V_{AKI} =100V / 200V, I_{a1} = --
- Reset the system, Now keeping V_{AK} zero increase I_{g2}=I_{g1}+0.03mA / 0.05mA and then increase V_{AK} gradually till SCR conduction. Note these readings as I_{g2}, V_{AK2} & I_{g2}.
- Repeat the step6 for different values of gate currents Ig3, Ig4, etc.
- Tabulate the readings and plot the I_A verses V_{AK}.

** Note / Precaution: <u>Do not use lamp load more than 25W other wise kit will gets damaged.</u>

(B) Measurement of latching current: IL

- 1. Apply V_{AK} =50V 60V, turn on the SCR by increasing sufficient I_g .
- 2. After conduction of SCR make gate current zero, SCR will remain in ON state which is called latching condition.
- 3. Now reduce VAK gradually by observing IA on ammeter. IA will reduce w.r.t. VAK
- 4. At one stage I_A becomes zero form a non zero current value. Note this non zero I_A as a latching current.

(C) Measurement of holding current: l_H

- 1. Apply V_{AK} =50V 60V, turn on the SCR by increasing sufficient I_g .
- After conduction of SCR keep gate current as it is, (do not make Ig zero as in latching current case) SCR will remain in ON state
- 3. Now reduce VAK gradually by observing IA on ammeter. IA will reduce w.r.t. VAK
- 4. At one stage I_A becomes zero form a non zero current value. Note this non zero I_A as a holding current.

(D) Characteristics curve on CRO:

- 1) Keep CRO in X-Y mode
- 2) Connect anode to X (channel-1), connect cathode to Y (channel-2)

3) Vary the potentiometer, observe the effect of gate current on characteristics curve on CRO.

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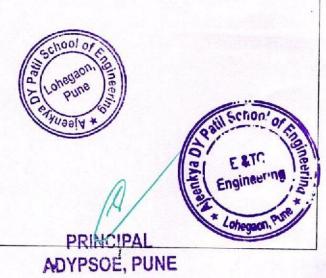
OBSERVATIONS:

I _{Gl} = m	A	I _{G2} =	mA .
V _{AKI} (V)	I _{A1} (A)	V _{AK2} (V)	I _{A2} (A)
10			1
20		A STEEL BY	
30		Electrical land	
			11/1/19
200			-

$I_{G3} = mA$		I _{G4} =	m A
V _{AK3} (V)	I _{A3} (A)	V _{AK4} (V)	I _{A4} (A)
			2

Latching current $(I_L) = ---- mA$ Holding current $(I_H) = ---- mA$

CONCLUSIONS:



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3

Experiment No.:02 Class: T.E. (E&TC) Subject: Power Electronics Date:

DRAIN & TRANSFER CHARACTERISTICS OF POWER MOSFET & IGBT

<u>AIM:</u> To study V - I & <u>Transfer</u> characteristics of power MOSFET / IGBT

EQUIPMENTS & COMPONENTS:

- 1. Trainer kit
- 2. Dual power supply (does not require in case of inbuilt power supply)
- 3. Multimeters (3)
- 4. Connectors, etc.

THEORY:

1) MOSFET:

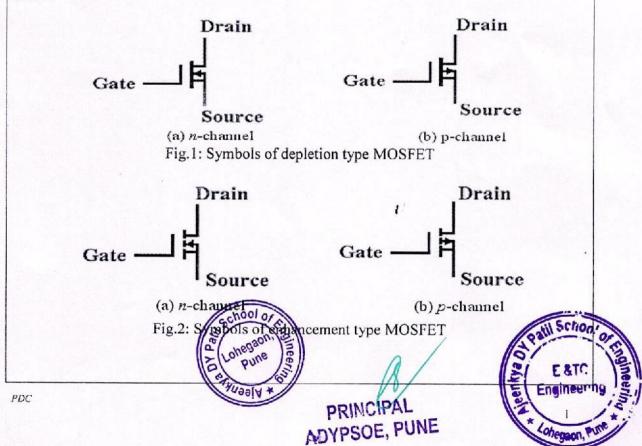
The MOSFET is a Metal oxide silicon di-oxide field effect transistor. It is a three terminal device which are gate, drain and source. The MOSFET is a voltage controlled device where as BJT is a current controlled device.

There are two types of MOSFETs: i) Depletion type MOSFET and

ii) Enhancement type MOSFET

The channel in the center is absent for enhancement type MOSFET but the channel is present in depletion type MOSFET. The gate voltage can either be positive or negative in depletion type MOSFET, but enhancement MOSFET responds only for positive gate voltage.

The n- drift region increases the onstage drop of MOSFET and also the thickness of this region determines the breakdown voltage of MOSFET. The different symbols are as below:



2) IGBT:

The <u>Insulated Gate Bipolar Transistor</u>, (IGBT) uses the insulated gate (hence the first part of its name) technology of the MOSFET with the output performance characteristics of a conventional bipolar transistor, (hence the second part of its name). The result of this hybrid combination is that the "IGBT Transistor" has the output switching and conduction characteristics of a bipolar transistor but is voltage-controlled like a MOSFET.

The insulated gate bipolar transistor or IGBT is a three-terminal power device, noted for high efficiency and fast switching. The structure is very similar to that of a vertically diffused MOSFET featuring a double diffusion of a p-type region and an n-type region. An inversion layer can be formed under the gate by applying the correct voltage to the gate contact as with a MOSFET. The main difference is the use of a p+ substrate layer for the drain. The effect is to change this into a bipolar device as this p-type region injects holes into the n-type drift region.

The main advantages of IGBT over a Power MOSFET and a BJT are:

- 1.It has a very low on-state voltage drop due to conductivity modulation and has superior on-state current density. So smaller chip size is possible and the cost can be reduced.
- Low driving power and a simple drive circuit due to the input MOS gate structure. It can be easily controlled as compared to current controlled devices (thyristor, BJT) in high voltage and high current applications.
- 3. Wide SOA. It has superior current conduction capability compared with the bipolar transistor. It also has excellent forward and reverse blocking capabilities.

The main drawbacks are:

- 1. Switching speed is inferior to that of a Power MOSFET and superior to that of a BJT. The collector current tailing due to the minority carrier causes the turn-off speed to be slow.
- 2. There is a possibility of latchup due to the in ternal PNPN thyristor structure

PROCEDURE:

A) V-I Characteristics:

- 1. Make the connection as per the diagram.
- 2. Keep the gate source voltage constant at $0V (V_{GS} = 0V)$.
- 3. Apply the $V_{DS} = 1V$, still drain current I_D will be zero
- 4. Now increase V_{GS} gradually till I_D becomes nonzero, (200 μ A 300 μ A) note this V_{GS} as V_{GS1}
- 5. Now Vary the V_{DS} from 0 to 12V as per the steps mention in the observation table.
- 6. Note down the corresponding readings of I_D.
- 7. Repeat the above procedure for different $V_{GS2} = V_{GS1} + 0.2V$, $V_{GS3} = V_{GS2} + 0.2V$, $V_{GS4} = V_{GS3} + 0.2V$,
- 8. Plot the drain characteristics V_{DS} Vs I_D for various values of V_{GS} on graph paper

B) Transfer Characteristics:

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1. Make the connection as per the diagram.

G Touchage

- 2. Keep the gate source voltage constant at V_{GS1} as above part (A) and increase V_{DS} to 5V, note down corresponding I_D
- 3. Now increase gate source voltage at constant V_{DS} in step of 0.1V and note down corresponding I_D values
- 4. Plot the transfer characteristics V_{GS} vs I_D for constant value of V_{DS}

*** Repeat same procedure for IGBT

OBSERVATION TABLE:

(A) V - I Characteristics:

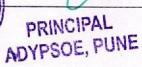
Sr	V _{DS}		I _D (mA)	
No	(volts)	$V_{GS1}=$	V _{GS2} =	V _{GS3} =	V _{GS4} =
1	0.2				
2	0.4			1 T-1 1 No.	
3	0.6				
4	0.8				
5	1.0				
6	2.0				
7	3.0				
8	4.0				
9	5.0				
10	6.0				
11	7.0				
12	8.0				
13	9.0				
14	10.0				
15	11.0				
16	12.0				

(ii) Transfer Characteristics:

Sr	$V_{DS} = 5V$			
No	V _{GS} (V)	I _D (mA)		
1				
2		,		
3				
4				
5 6				
6				
7				

CONCLUSIONS:

PDC





EXPT. No. 03

Experiment No.:

Date:

2.

AIM: To study UJT Triggering Circuit for SCR

APPRATUS:

- 1. Dual Trace SCR
- 2. Digital Multimeter
- Trainer kit
- 4. Load resistors, Connectors, etc.

THEORY:

This training system is designed to explain working principles of UJT triggering circuit.

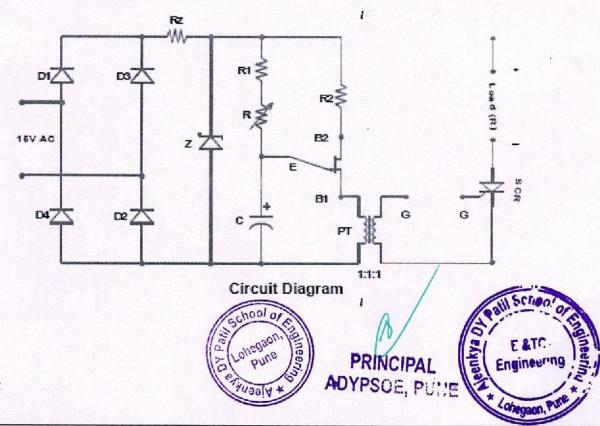
UJT exhibits negative resistance characteristics hence it is operating under relaxation mode.

Basically R & R-C triggering circuits provide long duration gate pulses which increases gate losses so that sharp & narrow pulses are preferred for SCR which can be obtained from UJT relaxation oscillator.

Here a line synchronized UJT triggering circuit is used which consists of a bridge rectifier (without filter), a zener regulator & an UJT relaxation oscillator.

Circuit Operation:

UJT Triggering Circuit for SCR (Line Synchronized)



AC input applied to bridge rectifier provides pulsated full wave do voltage. The o/p of bridge rectifier is regulated by a zener regulator to a predetermined value. Resistor Rz is current limiting resistor through zener. The regulated o/p Vz is given to UJT relaxation oscillator as V_{BB} .

When supply is switched ON capacitor starts charging towards Vz through R1 & R. When capacitor voltage reaches to Vp, junction E-B1 breaks & UJT starts conducting. As UJT conducts, capacitor C discharges through UJT and a sharp & narrow current pulse is obtained across secondary of pulse transformer which is given to gate of SCR.

PROCEDURE:

- 1. Ensuring all pots at minimum position switch on the trainer kit.
- Observe waveforms at o/p of bridge rectifier, across zener.
- Observe waveforms across C (Charging & discharging)
- Now rotate 'pot R' on kit gradually and observe the change in waveform on CRO.
- Also Observe waveforms (pulse) across primary of pulse transformer.
- Now connect gate pulse to gate of SCR, connect J1 of bridge to J1 of SCR.
- 7. Connect a load of 220Ω or 330Ω, observe different waveforms across load by varying 'pot R' at different firing angles!
- Also note down amb & amax
- Sketch the waveforms across load & SCR at α_{min} & α_{max}
- τα Tabulate the readings.

OBSERVATION TABLE:

Sr. No.	Firing Angle	V ₀
1	a _{max} =	
2		
3		
4		
5	a _{min} =	

Graph: Firing Angle Vs Speed

Conclusion:

Corport of the Control of the Contro



Experiment No.: 04

Date:

SINGLE PHASE HALF CONTROLLED CONVERTER (SEMICONVERTER)

AIM: To study Single Phase Half controlled Rectifier (Semi converter) for R / R-L load

EQUIPMENTS & COMPONENTS:

- 1. Trainer kit
- 2. Unearthed Dual Trace CRO/ Power scope
- 3. Multimeters
- 4. Load lamp, 15 25W
- 5. Connectors, etc.

THEORY:

- This training system is designed to explain working principle of semiconverter.
- Here the Phase Angle Control technique is used using UJT triggering circuit...
- The position of firing angle may be from 10° to 170° on half cycle to obtain maximum to minimum voltage across the load.

CIRCUIT OPERATION:

(i) Control Circuit:

Control circuit consists of a line synchronized UJT relaxation oscillator. Bridge rectifier converts 15V ac input to pulsating dc voltage o/p. During +ve half cycle of input diodes D1-D2 conduct, while during -ve half cycle of i/p diodes D3-D4 conduct. The output of this bridge is regulated by a zener diode. Resistor Rz controls current through zener.

When circuit is switched ON, capacitor C starts charging through R & R1 towards source voltage. When capacitor voltage reaches to peak voltage of UJT i.e. Vp, emitter-base junction (E-B1) of UJT breaks and UJT conducts. During UJT conduction capacitor C discharges through UJT and a pulse is obtained across primary of pulse transformer which is used as a gate pulse. Here R is a potentiometer which controls charging rate of capacitor. Pulse transformer has 1:1:1 turn ratio, therefore pulses of same rating will appear across both secondary windings. Pulse transformer also provides isolation between triggering circuit & power converter

circuit. Pulses at first secondary are given to SCR T1 as G1-K1, similarly Pulses at next secondary are given to SCR T2 as G2-K2.

Advantages: 1. UJT triggering circuit provides sharp & narrow gate pulses which reduces the gate losses.

- 2. It provides isolation between triggering circuit & power circuit.
- 3. Firing angle range is 0 180°
- 4. It exhibits repositions characteristics hence used under relaxation Mode.

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(ii) Power Converter Circuit:

Power converter circuit consists of two SCRs & two diodes connected in symmetrical configuration. During + ve half cycle T1 & D2 become forward biased, after gating gate pulse to T1, both T1 & D2 conduct in one direction and load voltage is positive. During –ve half cycle T2 & D1 become forward biased, after gating gate pulse to T2, both T2 & D1 conduct in the same direction and hence load voltage remains positive. In this way by controlling firing angle by pot R in control circuit dc voltage gets controlled.

During firing angle duration there is inherent freewheeling by either T1-D1 or T2-D2 when R-L load is connected.

PROCEDURE:

- Observe & sketch different waveforms in control circuit i.e. o/p of bridge, voltage across zener, voltage across capacitor C and voltage across primary of pulse transformer.
- 2. Now connect G1 of control circuit to G1 of power converter, similarly connect K1 to K1, G2 to G2 & K2 to K2.
- 3. Connect a lamp load (R load) of 25W / 40W in given bulb socket.
- 4. AC input can be connected either from VARIAC or from 0-36V (rms), 50Hz transformer...
- 5. Connect a power scope / CRO & a multimeter across the load.
- Observe load voltage waveform on CRO, measure corresponding o/p voltage & firing angle.
- 7. Now vary the firing angle by pot, observe different waveforms across load, measure corresponding o/p voltages & firing angles.
- 8. Tabulate the readings, compare with theoretical o/p, plot the graph a vs Vo
- 9. Repeat the all procedure for R L load.
- 10. Repeat the all procedure for R L load with freewheeling diode

OBSERVATION TABLE:

Formulae: 1) For R load

$$V_0 = \frac{V_m}{\pi} (1 + \cos \alpha)$$

2) For R-L load

$$V_O = \frac{V_m}{\pi} (1 + \cos \alpha)$$

3) For R-L load with freewheeling diode

$$V_O = \frac{V_m}{\pi} \left(1 + \cos \alpha \right)$$



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1.

Sr. No.	Firing Angle	V0 (Practical)	V0 (Theoretical)
1	30		
2	45		
3	65		
4	90		
5			
6			
7			

2) For R-L load:-

1

Sr. No.	Firing Angle	V0 (Practical)	V0 (Theoretical)
1			
2			
3			
4			
5			
6			
7			

3) For R-L load with freewheeling diode:-

Sr. No.	Firing Angle	V0 (Practical)	V0 (Theoretical)
1			(= = = = = = = = = = = = = = = = = = =
2			
3			
4			
5	tion = 1		
6		in the second of the	
7			

CONCLUSIONS:



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Experiment No.: 05

Date:

SINGLE PHASE FULL CONTROLLED CONVERTER

AIM: To Study Single Phase Full converter for R / R-L load

EQUIPMENTS & COMPONENTS:

- 1. Trainer kit
- 2. Unearthed Dual Trace CRO/ Power scope
- 3. Multimeters
- 4. Load lamp, 15 25W
- 5. Connectors, etc.

THEORY:

- This training system is designed to explain working principle of full converter
- Here the Phase Angle Control technique is used using UJT triggering circuit..
- The position of firing angle may be from 10° to 170° on half cycle to obtain maximum to minimum voltage across the load.

CIRCUIT OPERATION:

(i) Control Circuit:

Control circuit consists of a line synchronized UJT relaxation oscillator. Bridge rectifier converts 15V ac input to pulsating dc voltage o/p. During +ve half cycle of input diodes D1-D2 conduct, while during -ve half cycle of i/p diodes D3-D4 conduct. The output of this bridge is regulated by a zener diode. Resistor Rz controls current through zener.

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When circuit is switched ON, capacitor C starts charging through R & R1 towards source voltage. When capacitor voltage reaches to peak voltage of UJT i.e. Vp, emitter-base junction (E-B1) of UJT breaks and UJT conducts. During UJT conduction capacitor C discharges through UJT and a pulse is obtained across primary of pulse transformer which is used as a gate pulse. Here R is a potentiometer which controls charging rate of capacitor. Pulse transformer has 1:1:1 turn ratio, therefore pulses of same rating will appear across both secondary windings. Pulse transformer also provides isolation between triggering circuit & power converter

circuit. Pulses at first secondary are given to SCR T1 as G1-K1, similarly Pulses at next secondary are given to SCR T2 as G2-K2.

Advantages: 1. UJT triggering circuit provides sharp & narrow gate pulses which reduces the gate losses.

2. It provides isolation between triggering circuit & power circuit.

3. Firing angle range 1800 180

4. It exhibits -ve resistance characteristics hence used under relaxation Mode.

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(ii) Power Converter Circuit:

Power converter circuit consists of four SCRs connected in symmetrical configuration. During + ve half cycle T1 & T2 become forward biased, after gating pulse both T1 & T2 conduct in one direction and load voltage is positive. During -ve half cycle T3 & T4 become forward biased, after gating gate pulse T3& T4, both conduct in the same direction and hence load voltage remains positive. In this way by controlling firing angle by pot R in control circuit dc voltage gets controlled.

There is no inherent freewheeling action in full converter

PROCEDURE:

- 1. Observe & sketch different waveforms in control circuit i.e. o/p of bridge, voltage across zener, voltage across capacitor C and voltage across primary of pulse transformer.
- 2. Now connect G1 of control circuit to G1 of power converter, similarly connect K1 to K1, G2 to G2 & K2 to K2.
- 3. Connect a lamp load (R load) of 25W / 40W in given bulb socket.
- 4. AC input can be connected either from VARIAC or from 0-36V (rms), 50Hz transformer...
- 5. Connect a power scope / CRO & a multimeter across the load.
- 6. Observe load voltage waveform on CRO, measure corresponding o/p voltage & firing angle.
- 7. Now vary the firing angle by pot, observe different waveforms across load, measure corresponding o/p voltages & firing angles.
- 8. Tabulate the readings, compare with theoretical o/p, plot the graph α vs Vo
- Repeat the all procedure for R L load.
- 10. Repeat the all procedure for R L load with freewheeling diode

OBSERVATION TABLE:

Formulae: 1) For R load

$$V_0 = \frac{v_m}{\pi} \left(1 + \cos \alpha \right)$$

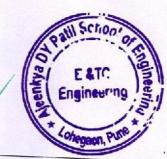
2) For R-L load

$$Vo=[2Vm/\pi] \times Cos\alpha$$

3) For R-L load with freewheeling diode

$$V_0 = [2V_m/\pi] \times Cos\alpha$$





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1) For R load:-

Sr. No.	Firing Angle	V0 (Practical)	V0 (Theoretical)
1			
2			
3			
4			
5			
6			
7			

2) For R-L load:-

Sr. No.	Firing Angle	V0 (Practical)	V0 (Theoretical)
1			
2			
3			
4			
5			
6			
7			1

3) For R-L load with freewheeling diode:-

Sr. No.	Firing Angle	V0 (Practical)	V0 (Theoretical)
1			
2			
3			
4			
5			
6			
7			

CONCLUSIONS:



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EXPERIMENT NO: 06

Experiment No:

Date:

AIM: To study DC Chopper (step down) using MOSFET.

APPRATUS:

- 1. Dual trace CRO
- 2. Digital Multimeter
- 3. Trainer kit
- 4. Power supply.
- 5. Connectors, etc.

THEORY:

- i) This training system is designed to explain working principle of step down DC chopper. A chopper is a circuit which converts fixed dc voltage into Variable dc voltage i.e. dc dc converter. It consists of a gate drive circuit & a MOSFET as a switch. By making switch ON / OFF o/p voltage is made variable form 0 Vs.
- ii) Here the time ratio Control (TRC) technique is used using a IC555 based gate drive circuit.
- iii) The ON time can be varied by RA, OFF time can be varied by RB, hence duty cycle / frequency can be change.

PROCEDURE:

- Observe waveform at o/p of astable multivibrator using IC555.
- Connect above o/p to i/p of totem pole circuit (base of Q1 & Q2) and observe its output.
- Apply 12V 24V dc as an input to chopper.
- Now connect o/p of totem pole to gate of MOSFET.
- Connect a CRO across load which is internally connected.
- Observe load voltage waveform on CRO, measure T_{ON}, T_{OFF} on CRO and measure corresponding o/p voltage.
- Now vary the T_{CN} or T_{OFF} by R_A or R_B respectively and measure corresponding output voltages.

8. Tabulate the readings, constant

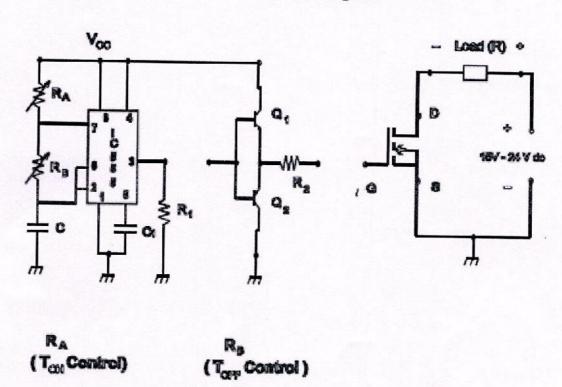
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theoretical o/p.



EXPERIMENT NO: 06

Circuit Diagram







EXPERIMENT NO: 06

OBSERVATION TABLE:

Sr. No.	Ton	Toff	T	Duty cycle	V0 (Practical)	V0 (Theoretical)
1					11.1000001	(mooretical)
2		-				
3						
4						
5						
6						
7						
8						
9			775			
10						

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CONCLUSIONS:





Experiment No.: 07

Date:

LOAD & LINE REGULATIONS OF SMPS

AIM: To find load & line regulations of Switch Mode Power Supply (SMPS)

EQUIPMENTS & COMPONENTS:

- 1. Trainer kit
- 2. Multimeters
- 3. VARIAC
- 4. Different loads / Rheostat, Connectors etc.

THEORY:

- This training system is designed to explain working principle of Switch Mode Power Supply (SMPS)
- TOP-Switch is a single CMOS chip which integrates a power MOSFET, PWM control & protection circuitry on a single chip.
- This design delivers 10W at 12V. It is operated from 120V 265V ac supply.

Pin Functional Description of TOP-Switch:

DRAIN (D) Pin:

High voltage power MOSFET drain output. The internal startup bias current is drawn from this pin through a switched high voltage current source. Internal current limit sense point for drain current.

CONTROL (C) Pin:

Error amplifier and feedback current input pin for duty cycle control. Internal shunt regulator connection to provide internal bias current during normal operation. It is also used as the connection point for the supply bypass and auto-restart/ compensation capacitor.

LINE-SENSE (L) Pin: (Y or R package only)

Input pin for OV, UV, line feed forward with DCMAX reduction, remote ON/OFF and synchronization. A connection to SOURCE pin disables all functions on this pin.

EXTERNAL CURRENT LIMIT (X) Pin: (Y or R package only)

Input pin for external current limit adjustment, remote ON/OFF, and synchronization. A connection to SOURCE pin disables all functions on this pin.

MULTI-FUNCTION (M) Pin: (P or G package only)

This pin combines the functions of the LINE-SENSE (L) and EXTERNAL CURRENT LIMIT (X) pins of the Y package into one pin. Input pin for OV, UV, line feed forward with DCMAX reduction, external current limit adjustment, remote ON/OFF and synchronization. A connection to SOURCE pin disables all

functions on this pin and makes TOP SWILL O

woperate in simple three terminal mode (like

TOPSwitch-II).

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FREQUENCY (F) Pin: (Y or R package only)

Input pin for selecting switching frequency: 132 kHz if connected to SOURCE pin and 66 kHz if connected to CONTROL pin. The switching frequency is internally set for fixed 132 kHz operation in P and G packages.

SOURCE (S) Pin:

Output MOSFET source connection for high voltage power return. Primary side control circuit common and reference point.

Circuit Diagram





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PROCEDURE:

(A) For Load Regulation:

- 1. Connect VARIAC to ac input terminals, ensuring at zero position switch ON it.
- 2. Now apply 230V ac from VARIAC, measure o/p at given terminals, it will be of 12V dc which is no load voltage i.e. $V_{NL} = 12V$.
- 3. Connect a load or (rheostat) having $100 K\Omega$ to $220 K\Omega$ at o/p, measure o/p across load.
- 4. Vary the load, from 220Ω to $2K\Omega$ measure corresponding load voltages.
- 5. Tabulate the readings, find load regulation.

%Load Regulation =
$$\frac{V_{NL}-V_{FL}}{V_{FL}}$$
 * 100

(B) For Line Regulation:

- 1. Connect VARIAC to ac input terminals, ensuring at zero position switch ON it.
- 2. Now apply 250V ac from VARIAC, connect a load resistance of $1K\Omega$, measure o/p at given terminals, it will be of 12V dc.
- 3. Decrease the ac i/p from VARIAC in step of 10V, measure o/p.
- 4. Repeat step 3 for various i/p voltages up to 150V.
- 5. Tabulate the readings, find line regulation.

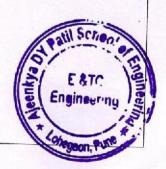
%Line Regulation =
$$\begin{array}{c} \Delta V0 \\ ----- \\ \Delta Vin \end{array}$$
 * 100

OBSERVATION TABLE:

$$V_{NL} = 12V$$

(A) For Load Regulation:

Sr. No.	R_L	V _{FL} (V)	Load Reg. (%)
1	220Ω	177.57	
2	330Ω		
3	470Ω		
4	1KQati	150	
5	PULL PULL	School	1



(B) Line Regulation:

Sr. No.	Variac voltage	Vo (V)	LineReg.
1	250V		
2	240V		
3	230V		
4	220V		
5	210V		
6	200V		
7	190V		1
8	180V		
9	170V		
10	160V		
	?	A'a'	

CONCLUSIONS:





Experiment No.: 08

Date:

SINGLE PHASE AC VOLTAGE CONTROLLER

AIM: To study single phase AC voltage controller for R load.

EQUIPMENTS & COMPONENTS:

- 1. Trainer kit
- 2. Power scope / Unearthed dual trace CRO
- 3. Load lamp (40W, 230V)
- 4. Multimeters
- 5. Connectors, etc.

THEORY:

- This training system is designed to explain working principle and method of obtaining the variable AC from fixed frequency AC supply.
- Here the Phase Angle Control technique is used using UJT triggering circuit..
- In order to control AC power we may use a Triac or SCRs connected in anti-parallel fashion. In our Trainer System we have used two SCRs in anti-parallel connection (full wave).
- The position of firing angle may be from 10° to 170° on half cycle to obtain maximum to minimum voltage across the load.

Control circuit consists of a line synchronized UJT relaxation oscillator. Bridge rectifier converts 15V ac input to pulsating dc voltage o/p. During +ve half cycle of input diodes D1-D2 conduct, while during –ve half cycle of i/p diodes D3-D4 conduct. The output of this bridge is regulated by a zener diode. Resistor Rz controls current through zener.

When circuit is switched ON, capacitor C starts charging through R & R1 towards source voltage. When capacitor voltage reaches to peak voltage of UJT i.e. Vp, emitter-base junction (E-B1) of UJT breaks and UJT conducts. During UJT conduction capacitor C discharges through UJT and a pulse is obtained across primary of pulse transformer which is used as a gate pulse. Here R is a potentiometer which controls charging rate of capacitor. Pulse transformer has 1:1:1 turn ratio, therefore pulses of same rating will appear across both secondary windings. Pulse transformer also provides isolation between triggering circuit & power converter circuit.

As per power converter circuit, during + ve half cycle T1 becomes forward biased, after gating gate pulse T1 conducts in one direction and load voltage is positive. During -ve half cycle T2 becomes forward biased, after gating gate pulse T2 conducts in opposite direction and hence load voltage becomes negative. In this way by controlling firing angle AC voltage gets varied from fixed AC voltage.

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PROCEDURE:

- 1. Observe & sketch different waveforms in control circuit i.e. o/p of bridge, voltage across zener, voltage across capacitor C and voltage across primary of pulse transformer.
- 2. Now connect G1 of control circuit to G1 of power converter, similarly connect K1 to K1, G2 to G2 & K2 to K2.
- 3. Connect a lamp load of >15W to load terminals.
- 4. AC input can be connected either from VARIAC or from 0-36V (rms), 50Hz transformer.
- 5. Connect a power scope / CRO & a multimeter across the load.
- Observe load voltage waveform on power scope / CRO, measure corresponding o/p voltage & firing angle.
- 7. Now vary the firing angle, observe different waveforms across load, measure corresponding o/p voltages & firing angles.
- 8. Tabulate the readings, compare with theoretical o/p, plot the graph α vs Vorms

Circuit Diagram

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OBSERVATION TABLE:

Sr. No.	Firing Angle	Vorms (Practical)	Vorms (Theoretical)
1			
2			
3			
4			
5			,
6			
7			
8			
9			
10			

Formulae:

$$V_{ORMS} = \frac{1}{\sqrt{2\pi}} \sqrt{\left[\pi - \alpha + \frac{\sin 2\alpha}{2}\right]}$$

CONCLUSIONS:



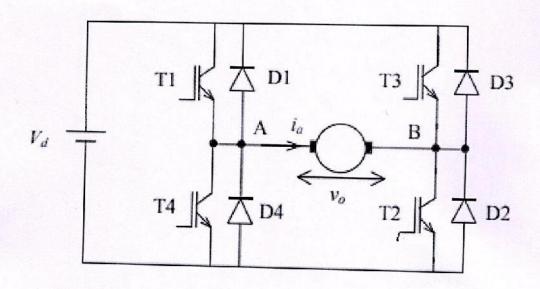
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EXPERIMENT NO. 9

Date:

Title: Speed control of DC motor using Semi/full Converter

Measure RPM and plot graph of speed versus armature voltage and field current



A single phase semiconverter/full converter is used to control the speed of DC Motor. The firing Angle control method is used to vary the load voltage across the motor.

Firing Circuit:

UJT Relaxation Oscillator is used to generate firing pulses for semiconverter/full converter. By varying the firing angle, Load voltage is varied. Higher is the firing angle, lesser will be the load voltage.

Power Circuit:

A full converter circuit is made up of four thyristors whose DC output voltage is

controlled by a firing angle.

Speed Control:

The Speed of the DC Motor is controlled by varying the load voltage of a full converter. In firing angle control method, as the firing angle increases, the load voltage decreases and therefore the speed of the DC motor reduces.

Procedure:

- 1] Make the connections as per the circuit Diagram.
- 2] Observe the converter output on Power Scope for different firing angle
- 3] Connect the DC Motor across the load for a certain firing angle.
- 4] Measure the speed of motor using tachometer.
- 5] Take minimum six reading of firing angle and corresponding output voltage and speed of the motor.
- 6] Plot a graph of firing angle Versus Speed.

Observation Table:

Sr. No.	Firing Angle	Output Voltage	Speed in RPM
1			Speed in Rei W
2			
3			
4			
5			
6			
7			1
8			
9			
10			

Graph: Firing Angle Vs Speed

Conclusion:





EXPERIMENT NO. 10

Date:

Title: Demonstration/Study of Automatic Voltage Regulator and Uninterruptable Power Supply(UPS)

1] Automatic Voltage Regulator (AVR)

To study the working of automatic voltage Regulator and calculate the line and load regulation.

- A) Device Identification
- B) Design Steps
- C) Implementation
- D) Demonstration
- E) Applications

2] Uninterruptable Power Supply (UPS)

To study the working UPS and calculate the back up time.

- A) Device Identification
- B) Design Steps
- C) Implementation
- D) Demonstration
- E) Applications
- F) Specifications

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Experiment No. 11

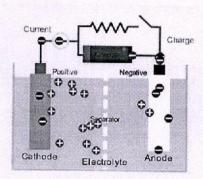
<u>AIM</u>: To study battery testing, safety and maintenance of batteries <u>EOUIPMENTS</u> & <u>COMPONENTS</u>:

- 1. Battery 12V / ---- AH
- 2. Digital Multimeter
- 3. Charger /Rectifier/UPS
- 4. Load lamp, Connectors, etc.

THEORY:

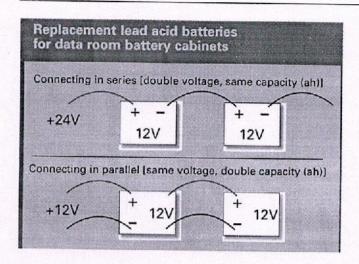
Rechargeable Batteries

A rechargeable battery is a type of electrical battery that is comprised of one or more electrochemical cells. It is known as a secondary cell because its electrochemical reactions are electrically reversible. In other words, after the stored charge has been drained, the battery's chemical reactions can occur again, in reverse, to store a new charge.



As with all batteries, rechargeable batteries consist of an anode, a cathode, and an electrolyte. During charging, the anode material is oxidized, producing electrons, and the cathode is reduced, consuming electrons. These electrons constitute the current flow in the external circuit. The electrolyte may serve as a simple buffer for internal ion flow between the electrodes, as in lithium-ion and nickel-cadmium cells, or it may be an active participant in the electrochemical reaction, as in lead-acid cells.





Types of Rechargeable Batteries: Several different combinations of chemicals are commonly used in rechargeable batteries. Different types include lead-acid, nickel-cadmium (NiCd), nickel-metal hydride (NiMH), lithium-ion (Li-ion), lithium-ion polymer (LiPo), and rechargeable alkaline batteries.

Lead-Acid Batteries

Lead-acid batteries, invented in 1859 by French physicist Gaston Planté, are the oldest type of rechargeable battery. Their ability to supply high-surge currents means that the cells maintain a relatively large power-to-weight ratio. These features, along with their low cost, make them attractive for use in motor vehicles, which require high currents.

$$Pb + PbO_2 + 2H_2SO_4 \leftrightarrow 2PbSO_4 + 2H_2O$$

In the lead acid cells the negative plates have a spongy lead as their active material, whilst the positive plates have an active material of lead dioxide. The plates are immersed in an electrolyte of dilute sulphuric acid. The sulphuric acid combines with the lead and the lead oxide to produce lead sulphate and water, electrical energy being released during the process. The overall reaction is:

Battery Sizing for UPS:

Battery manufacturers provide various types of information for sizing batteries. All battery sizing calculations in this paper have assumed a standard room temperature of 77° (25°C). Batteries which will be operated at higher or lower temperatures continuously should be calculated specifically for that temperature. Your UPS or battery supplier can assist you with these calculations. Generally, this information is supplied in one of three ways:

1. Kilowatts per cell 2. Kilowatts per bank 3. Ampere per cell

Normally information supplied for lead acid batteries designed for short discharge times (5-120 minutes) is in the form of kilowatts per cell tabulated for various back-up times. Therefore, it is important to first calculate the total kilowatts required to operate the particular UPS at a given load (usually assuming full load).

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Nickel-Metal Hydride Batteries

A nickel-metal hydride battery, abbreviated NiMH or Ni-MH, is very similar to the nickel-cadmium cell (NiCd). NiMH batteries use positive electrodes of nickel oxyhydroxide (NiOOH), as does the NiCd, but the negative electrodes use a hydrogen-absorbing alloy instead of cadmium. A NiMH battery can have two to three times the capacity of a NiCd battery of equivalent size, and its energy density approaches that of a lithium-ion cell.

Lithium-Ion Batteries

Small size and light weight are the primary benefits, but additional features like built-in battery management (not just monitoring), make lithium-ion an attractive alternative to traditional batteries.

In addition, their high cycle-count (charge-discharge cycles), and faster recharge times compared with lead batteries allows their use in non-traditional UPS applications, like grid sharing, peak shaving, and industrial or process control support. The battery management system is deployed in each battery, as well as in a system level master controller. It manages charge current, voltage, and cell voltage balance, while making adjustments as necessary to eliminate any chance of overtemperature.

If temperatures rise above safe levels, the management system will independently disconnect the battery or string via multiple different disconnection means, and notify the user via the battery cabinet monitor, and an alarm on the UPS.

Lithium-Ion Polymer Batteries

Lithium-ion polymer (LiPo) batteries are usually composed of several identical secondary cells in parallel to increase the discharge-current capability. They are often available in series "packs" to increase the total available voltage. Their primary distinction from lithium-ion batteries is that their lithium salt electrolyte is not held in an organic solvent. Instead, it is in a solid polymer composite, such as polyethylene oxide or polyacrylonitrile. The advantages of LiPo over the lithium-ion design include potentially lower cost of manufacture, adaptability to a wide variety of packaging shapes, reliability, and ruggedness. Their major disadvantage is that they hold less charge.

Alkaline Batteries

There are also rechargeable forms of alkaline batteries, which are a type of primary battery dependent upon the reaction between zinc (Zn) and manganese dioxide (MnO2). They are manufactured fully charged and have the ability to carry their charge for years, longer than most NiCd and NiMH batteries, which self-discharge. Rechargeable alkaline batteries can also have a high recharging efficiency and have less environmental impact than disposable cells.

The key properties and specifications for Ni-Cd, Ni-MH, and Li-Ion: The gravimetric energy density of a battery is a measure of how much energy a battery contains in comparison to its weight, and is typically expressed in Watt-hours/kilogram (W-hr/kg). The volumetric energy density of a battery is a measure of how much energy a battery contains in comparison to its volume, and is typically expressed in Watt-hours/kilogram (W-hr/l).

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CELL TYPE	NI-MH	NI-CD	LI-ION
GRAVIMETRIC DENSITY (W-HR/KG)	55	50	90
VOLUMETRIC DENSITY (W-HR/L)	180	140	210

The maximum current that a battery can deliver is directly dependent on the internal equivalent series resistance (ESR) of the battery. The current flowing out of the battery must pass through the ESR, which will reduce the battery terminal voltage by an amount equal to the ESR multiplied times the load current ($V = I \times R$).

CHARACTERISTICS OF RECHARGEABLE BATTERIES:

- A cell is an electro-chemical device capable of supplying the energy that results from an internal chemical reaction to an external electric circuit.
- A battery is composed of one or more cells, either parallel or series connected to obtain required current/voltage capability (batteries comprised of series connected cells are by far the most common).
- ESR (Equivalent Series Resistance) is the internal resistance present in any cell that
- limits the amount of peak current it can deliver.
- The Amp-hour capacity of a battery (or cell) is its most important figure of merit: it is
- defined as the amount of current that a battery can deliver for 1 hour before the battery voltage reaches the end-of-life point.
- The "c" rate is a current that is numerically equal to the A-hr rating of the cell. Charge and discharge currents are typically expressed in fractions or multiples of the c rate.
- The MPV (mid-point voltage) is the nominal voltage of the cell, and is the voltage that
- measured when the battery has discharged 50% of its total energy.
- The measured cell voltage at the end of its operating life is called the EODV, which
- stands for End of Discharge Voltage (some manufacturers refer to this as EOL or End of Life voltage).
- The gravimetric energy density of a battery is a measure of how much energy a battery contains in comparison to its weight.
- The volumetric energy density of a battery is a measure of how much energy a battery contains in comparison to its volume.
- A constant-voltage charger is a circuit that recharges a battery by sourcing only enough current to force the battery voltage to a fixed value.
- A constant-current charger is a circuit that charges a battery by sourcing a fixed current into the battery, regardless of battery voltage.

Maintenance of batteries:

Battery service and maintenance are critical to UPS reliability. A gradual decrease in battery life can be monitored and evaluated through voltage checks, load testing or monitoring. Periodic preventive maintenance extends battery string life by preventing loose connections, removing corrosion and identifying machine testing before they can affect the rest of the string.

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What can go wrong with batteries?

Condition	Cause
Plate separation	Repeated cycling (charging and discharging), damage during handling and shipping, and overcharging
Grid corrosion	Normal aging, operating in an acidic environment and high temperatures
Internal short circuit	Heat (plates expand causing shorts), separator failure, handling and shipping, and grid corrosion
External short circuit	Human error (shorting terminals) and leaks
Sulfation of plates	Sitting discharged for an extended period, not on charge or being undercharged, such as battery shel life being exceeded past manufacturer's guidelines
Excessive gassing	Often due to high temperatures or overcharging; electrolyte volume is decreased
Drying out	Excessive gassing, high temperatures or overcharging, resulting in too little electrolyte for battery to function and provide full backup time

PROCEDURE:

- 1. Observe all connections in UPS, note battery rating mentioned on case
- 2. Focus on battery terminals and charger
- 3. Measure Battery voltage and charging current without load while charger in ON
- Measure Battery voltage and discharge current while lamp load is connected and charger is OFF

6.

*** Pracaution Note:

- Do not charge by higher current or higher voltage than specified.
 Doing so may generate gas inside the battery, resulting in swelling, fire, heat generation or bursting.
- Do not heat, disassemble, nor dispose of in fire.
 Doing so damages the insulation materials and may cause fire, heat generation, leakage or bursting.
- Do not solder directly to the battery. If soldering is performed directly to the battery, the battery is heated up, consequently causing leakage, explosion or fire due to overheating from internal short-circuit.
- Do not short.
 If the (+) and (-) come into contact with metal materials, short-circuit occurs. As a result, fire, heat generation, leakage or bursting may occur.
- Keep batteries out of the reach of children. It is dangerous if children swallow the battery. Keep batteries which are considered swallowable out of the reach of children. When designing mechanical hardware around the battery, make sure that the battery cannot be removed by children. Swallowing may lead to burns performing of soft tissue, and death. Severe burns can

What can go wrong with batteries?

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External short circuit	Human error (shorting terminals) and leaks				
Sulfation of plates	Sitting discharged for an extended period, not on charge or being undercharged, such as battery shell life being exceeded past manufacturer's guidelines				
Excessive gassing	Often due to high temperatures or overcharging: electrolyte volume is decreased				
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- Do not solder directly to the battery.
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- Do
 If the (+) and (-) come into contact with metal materials, short-circuit occurs. As a

 Keep hatteries
- Keep batteries out of the reach of children. It is dangerous if children swallow the battery. Keep batteries which are considered swallowable out of the reach of children. When designing mechanical hardware around the battery, make sure that the battery cannot be removed by children. Swallowing may lead to burns, perforation of soft tissue, and death. Severe burns can

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occur within 2 hours of ingestion. In case of ingestion of a battery, seek medical attention immediately.

- Do not reverse placement of (+) and (-). If the (+) and (-) side of the battery is reverse inserted, it may cause a short-circuit or over discharge of the battery on some equipment and it may induce overheating, explosion or fire.
- Do not discharge by force. If the battery is discharged by direct connection to an external power supply etc., voltage of the battery will decline lower than 0 volt (electrical reversal) and will cause the battery case to expand, overheat, leak, explode or burn.
- In case of leakage or a strange smell, keep away from fire to prevent ignition of any leaked electrolyte.
- Do not weld terminals to the battery.
 The heat by welding may cause fire, heat generation, leakage or bursting. We weld standard terminals under strictly controlled conditions. If you need to weld terminals to the battery, please consult us in advance.
- In case of disposal, insulate between (+) and (-) of battery by an insulating material.
 Jumbling batteries or with other metal materials cause short-circuit. As a result, fire, heat generation, leakage or bursting may occur.

OBSERVATION TABLE:

Battery	Battery Type	Battery Voltage		Battery Charge	ging	Battery Discharging parameters		
	1)pc	Actual	Rated	Current (A)	Voltage (V)	Current (A)	Voltage (V)	
1							(1)	
2		200						
3								
4								
5	Take 1							
6								

ONCLUSIONS:			

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CO-PO Mapping & Attainments



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	Bloom's Taxonomy Levels	Form No. IQAC/36						
Level	Particulars							
1-Remember	1-Remember Recall facts and basic Concepts (define, duplicate, list, memorize, repeat state)							
2-Understand	Explain ideas or concepts (classify, describe, disculocate, recognize, report, select, translate)	uss, explain, identify,						
3-Apply	Use information in new situations (execute, imple demonstrate, interpret, operate, schedule, sketch)	ement, solve, use,						
4-Analyze	Draw connection among ideas (Differentiate, orga contrast, distinguish, examine, experiment, question							
5-Evaluate	Justify a stand or decision (Appraise, argue, defend value, critique, weigh)	d, judge, select, support,						
o Greate	Produce new or original work (Design, assemble, develop, formulate, author, investigate)	construct, conjecture,						



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Course Outcomes (COs):

Form No. IQAC/36

Semester:II

Academic Year.: 2021-22

Class: TE

Div: A

Subject: PDC

Name of Subject Teacher: Prof. Riyaj L. Kazi

CO No.	BT level	Students will be able to
CO-1	1-Remember	To differentiate based on the characteristic parameters among SCR, GTO, MOSFET & IGBT and identify suitability of the power device for certain applications and understand the significance of device ratings.
CO-2	6-Create	To design triggering / driver circuits for various power devices.
CO-3	4-Analyze	To evaluate and analyze various performance parameters of the different converters and its topologies.
CO-4	2-Understand	To understand significance and design of various protections circuits for power devices.
CO-5	5- Evaluate	To evaluate the performance of uninterruptible power supplies, switch mode power supplies and battery.
CO-6	2-Understand	To understand case studies of power electronics in applications like electric vehicles, solar systems etc.

In case if the syllabus dosen't contain 6 COs, the teacher can deside CO. The CO must highlight "What the student will be able to do a completion of Unit"



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	Program Outcomes (POs):	Form No. IQAC/36
ngine	ering Graduates will be able to:	
1	Engineering knowledge: Apply the knowledge of mathematics, science, engineering fundamental specialization to the solution of complex engineering problems.	als, and an engineering
2	Problem analysis: Identify, formulate, review research literature, and analyze complex engineering conclusions using first principles of mathematics, natural sciences, and engineering sciences.	g problems reaching substantiated
3	Design/development of solutions: Design solutions for complex engineering problems and desprocesses that meet the specified needs with appropriate consideration for the public health and safety environmental considerations.	
4	Conduct investigations of complex problems: Use research-based knowledge and research experiments, analysis and interpretation of data, and synthesis of the information to provide valid conductions.	
5	Modern tool usage: Create, select, and apply appropriate techniques, resources, and modern engine prediction and modeling to complex engineering activities with an understanding of the limitations.	neering and IT tools including
6	The engineer and society: Apply reasoning informed by the contextual knowledge to assess soc cultural issues and the consequent responsibilities relevant to the professional engineering practice.	ietal, health, safety, legal and
7	Environment and sustainability. Understand the impact of the professional engineering solution contexts, and demonstrate the knowledge of and need for sustainable development.	ions in societal and environmenta

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	Program Specific Outcomes (PSOs):	Form No. IQAC/36
	A graduate of the program will	
PSO-1	Understand the fundamentals of designing electronics systems for applications like communication systemented & VLSI systems and automation	ms, signal processing,
PSO-2	Strengthen the ability to use open source tools for modeling and simulation to solve technical problems	
PSO-3	Inculcate the skills to manage and lead a team contributing to development of prototype, application and technologies.	product of next generation







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				100			CO-I	O-PS	O Ma	pping			Fe	rın No. B	QAC/36	
Subject:	ic Year:2021- PDC Subject Teac		rof. Ri	yaj Ka	zi								Class	Semeste I'E	P:II DiviA	
PO CO	RT LEVEL	PO1	161	POS	104	POS	PCHS	PO?	PLIS	1902	1405.7	14011	11012	2801	1/9032	150
CO-1	1- Remember	3				100							2	1		
CO-2	5-Create	3	2	1	t								2	1	19.77	1
CO-3	4-Analyze	3	2	2	2								2	2		
CO-4	Understan	1	2	1	2		14						2	1		
CO-5	5. Evaluate	1											2	1-	1	
CO-6	Understan	3				2							2	3		
Average	1070	2.33	2.00	1.33	1.67	2.00	-	25	4.0	100	- 1	100	2.00	1.50	1.00	1.90
Rounde		3	2	2	2	2	E W		172	734		HI.	2	2	1	1

(Strength of Correlation): 3-Strong, 2-Medium, 1-Weak, Keep Blank-if No Corellation Justification for CO-PO Mapping.

CO No.	PO/PSO Mapped	Level	Justification of Mapping
	PO1	3	Strongly having the Knowledge of the fundamental concepts of power devices and its application that helps is solving complex engineering problems.
CO-1	P012	2	Moderately the student will become aware of the need for lifetong learning and the continued appracing of technical knowledge of power device characteristics.
	P501	1	Slightly the student will study power device eatings and its suitability for particular applications
582	P01	3	strengly having the Knowledge of the fundamental concepts of trigering methods that helps in solving complex engineering problems.
	POZ	2	Moderately the student will know Principles of drive circuits and methods of isolation
	POS	1	Slightly the student using the knowledge of series & parallel connections of power devices
CO-2	P04	1	Slightly having knowledge of trigerring circuits to conduct experiments in real life problems to provide valid conclusions
	PO12	2	Moderately the student will become aware of the need for lifelong learning and the continued upgrading of
	P501	1	technical knowledge of designing the trigorning circuits for a particular applications. Slightly the student will study of fundamental concepts of drive circuits.
	7503	,	Slightly the student will find the requirements of trigering circuits and understand the design steps.
	P01	1	Strongly having the Knowledge of the fundamental concepts of performance garameters of various converter that helps in solving complex engages ing problems
CO-3	P07	2	Moderately the student will know analysis of converter performance parameters
	POS	1	Slightly the student using the knowledge of topologies of performance parameters of converter helps in determining the converter performance and efficiency.
	PG4	1	Slightly having Knowledge of converter performance can be used to conduct experiments in real life problem to provide valid conclusions.
	PO12	2	Moderately the student will become aware of the need for kilolong learning and the continued upgrading of technical knowledge of performance of converters.
98	PS01	1	Slightly the student will study the converter parameters for analysing its performance
	P01	3	Strongly having the Knowledge of the fundamental concepts of protection circuits that helps in solving complex engineering problems.
	102	1	Moderately the student will know types of protections for power devices.
	FO3	1	Slightly the student using the knowledge of suitable protection circuit for a specific application of power converter
0.1	FO4	1	Slightly having Knowledge of selection of devices in protection circuit that can be used to conduct experiment in real life problems to provide valid conductions.
	PO17	2	Moderately the student will become aware of the need for lifelong learning and the continued upgrading of technical knowledge of design of protection circuits.
	P\$01	1	Slightly the student will study of fundamental concepts of device protections against various parameters
	PO1	3	Strongly having the Knowledge of the fundamental concepts of UPS, SMPS and Battery
	P012	2	Moderately the student will become aware of the need for filelong learning and the continued upgrading of technical knowledge of battery requirements and its design calculations.
CO-5	PSO1		Slightly the student will understand the fundamental concepts of swithed mode power supply and its specifications.
	P502	1	Slightly the student will be able to apply the concepts of power supply performance parameters for suitable applications
	POI	_	Strongly having the Knowledge of the fundamental concepts of Power Electronic Applications
	PD5		Moderately the student will become aware of the need for lifelong learning and the continued upgrading of technical knowledge on power electronic applications in electric vehicle.
CD-6	Pot2	2	Moderately the student will become aware of the need for lifelong learning and the continued upgrading of technical knowledge on power electronic applications in solar systems.
-	2501		Strongly students will able to apply the applications of power electronics in real life applications in electric





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		Guidelines for Unit Tests Form No. IQAC/36
Test No.	Syllabus	Particulars Particulars
Summative Test	Unit-I (10 marks)	Question should cover CO-1
UT-1	Unit-II (10 marks)	Question should cover CO-2
01-1	Unit-III (10 marks)	Question should cover CO-3
	Unit-IV (10 marks)	Question should cover CO-4
UT-2	Unit-V (10 marks)	Question should cover CO-5
	Unit-VI (10 marks)	Question should cover CO-6

Lohegaon,



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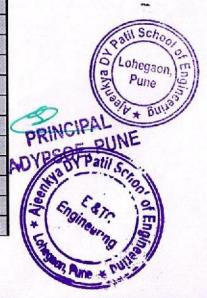
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Summ	ative Test Marks [Direct Assesment-In	ternal]	For	rm No. IQAC/36
Summative T	est			Date:
Academic Ye	ar:2021-22		Semester:II	
Subject:PDC				Max. Marks:30
Name of Subj	ect Teacher:Mr. Riyaj Kazi	Class:	TE	Div:A
	CO Addressed		(CO1
	Marks	20	10	Mapping level
Roll No.	Name of student	Marks (Obtained	
1	ABHINAY KUMAR	14	7	3
2	AFAAN IDRISI	13	7	3
3	BANODIYA NEEL MANOJ	16	8	3
4	BATTULA NITIN VENKATESH	16	8	3 1 4
5	BHOODATT PRATIK RAMESH	14	7	3
6	BISHT VIJAYSINGH NIDHI	16	8	3
7	CHEERALA KONA NAVEEN	15	8	3
8	CHIRIVELLA LAKSHMIMANASA S	14	7	3
9	DABHADE TEJAS PANDHARINATH	6	3	0
10	DEEPAK KUMAR THAKUR	18	9	3
11	DISHA HEGDE	17	9	3
12	DUBE NIHARIKA PRAMOD	13	7	3
13	GANVIR SANGHASEN PRAMOD	14	7	3
14	GARPAL ATHARV VINOD	13	7	3
15	GHUNDIYAL HARSH RAVINDRAKUMAR	3	2	0
16	GOPANI YASH JAGDISH	15	8 /	3
17	GOPATHI SUPRIYA	16	8	3
19	GOVIND KADAM	AB	NA	NA
			THE APPRECIATION OF THE PERSON NAMED	



	THANKAMONETTY A DUNIET 14			
20	HANNAMSHETTY ABHIJEET M	14	7	3
21	KADBHANE ASHUTOSH GANESH	15	8	3
22	KANKATE VAISHNAVI PRITAM	14	7	3
23	KAPADNE PRADNYA KISHOR	14	7	3
24	KAWADE AKASH RAVASAHEB	AB	NA	NA
25	KOKATE AJAY AUDUMBAR	12	6	3
27	KURHADE ABHISHEK SHYAM	14	7	3
28	LAWANDE SEJAL ROHIDAS	20	10	3
29	MAYANK RAJPUROOHIT	AB	NA	NA
30	MEHETRE SUMIT SATISH	16	8	3
33	PANDIT SAYALI MADHUKAR	AB	NA	NA
34	PRANALI MAMADAPURE	15	8	3
35	PRATIK JAGDISH DIYEWAR	13	7	3
36	RAGHAVENDRA SINGH RATHORE	15	8	3
37	SALUNKHE GAURI RAHUL	AB	NA	NA
38	SAURAV SUMAN	16	8	3
39	SHASHWAT BHOYAR	16	8	3
40	SHARMA SHIVANI HARISH	15	8	3
41	SHARMA SHUBHAM KIRAN	15	8	3
42	SHUBHAM PANDURANG KALAMKAR	15	8	3
43	SINGH DEVESH DINESH	13	7	3
44	SIRSAT VAISHNAVI DNYANESHWAR	AB	NA	NA
45	SWAROOP BANDU BADWE	3	2~	0
46	VIKESH BHARTI GOSWAMI	AB	NA	NA
47	WADHAI SAKSHI CHANDRAKANT	16	8	3
48	WANKHADE SANKALP SANJAY	AB	NA	NA
49	WANKHEDE HIMANSHU DHANANJAY	13	7	3
50	YASH SOLANKE	AB	NA	NA
51	PRATIK PATIL	AB	NA	NA
52	VIVEK MAHAJAN	AB	NA	NA
53	BRIJESH POKAR	14	7	3
54	PRIYA GUTTE	15	8	3 1 4
55	YASH KASHIKAR	13	7	3
56	PAYAL KAMBLE	13	7	3 = 1



ROHAN PATRE	13	7	3	
RITHIK DEHURIA	12	6	3	
	15	8	3	
	44			
al no of students securing more than 60%			41	
al no of students securing more than 50%			0	
al no of students securing more than 40%			0	
		93.18		
CO Addressed			O-1	
	RITHIK DEHURIA SAYALI PATIL al no of students attempted the question al no of students securing more than 60% al no of students securing more than 50% al no of students securing more than 40% Percentage	RITHIK DEHURIA SAYALI PATIL al no of students attempted the question al no of students securing more than 60% al no of students securing more than 50% al no of students securing more than 40% Percentage	RITHIK DEHURIA RITHIK DEHURIA SAYALI PATIL al no of students attempted the question al no of students securing more than 60% al no of students securing more than 50% al no of students securing more than 40% Percentage 93	

Criteria (marks Obtained)	Mapping Level
>=60%	3
>=50%	2
>=40%	1

CO Adderessed	Avg	Mapping
COI	93.18	3
CO2		
CO3		
CO4	NO	T Addressed
CO5		
CO6		

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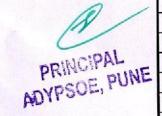
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	Test Marks [Direct Assesment-Into	ernal]	Form No	o. IQAC/36		
Unit Test	Number: I			Date:		
Academic				Semester	II	
Subject:P				Max. Ma	rks:30	
	Subject Teacher: Mr. Riyaj Kazi	Class: T.I	Ε.	Div: A		
	Question Number	Q.1	Or Q.2	A STATE OF THE PARTY OF THE PAR)r Q.4	
	CO Addressed		CO2	CO3		
	Marks		Mapping level	10	Mappin g level	
Roll No.	Name of the Student	Marks	Obtained	Marks Obtained		
1 1	ABHINAY KUMAR	5	2	4	1	
2	AFAAN IDRISI	AB	NA	AB	NA	
3	BANODIYA NEEL MANOJ	6	12.13	6	3	
4	BATTULA NITIN VENKATESH	9	3	10	3	
5	BHOODATT PRATIK RAMESH	8	3 -	7	3	
6	BISHT VIJAYSINGH NIDHI	AB	NA	AB	NA	
7	CHEERALA KONA NAVEEN	6	3.3	6	3	
8	CHIRIVELLA LAKSHMIMANASA S	8	3	6	3	
9	DABHADE TEJAS PANDHARINATH	AB	NA	AB	NA	
10	DEEPAK KUMAR THAKUR	6	3	5	2	
11	DISHA HEGDE	10	3	9	3	
12	DUBE NIHARIKA PRAMOD	AB	NA	AB	NA	
13	GANVIR SANGHASEN PRAMOD	8	3	9	3	
14	GARPAL ATHARV VINOD	7	3	9	3	
15	GHUNDIYAL HARSH RAVINDRAKUMA	AB	NA	AB	NA	
16	GOPANI YASH JAGDISH	7	3	7	3	







17	GOPATHI SUPRIYA	8	3	8	3
19	GOVIND KADAM	AB	NA -	AB	NA
20	HANNAMSHETTY ABHIJEET M	10	3	10	3
21	KADBHANE ASHUTOSH GANESH	8	3	8	3
22	KANKATE VAISHNAVI PRITAM	9	3	9	3
23	KAPADNE PRADNYA KISHOR	5	2	4	1
24	KAWADE AKASH RAVASAHEB	8	3	8	3
25	KOKATE AJAY AUDUMBAR	4	1 1	5	2
27	KURHADE ABHISHEK SHYAM	AB	NA	AB	NA
28	LAWANDE SEJAL ROHIDAS	10	3	10	3
29	MAYANK RAJPUROOHIT	AB	NA		NA
30	MEHETRE SUMIT SATISH	5	2	4	1
33	PANDIT SAYALI MADHUKAR	7	3	7	3
34	PRANALI MAMADAPURE	8	3	7	3
35	PRATIK JAGDISH DIYEWAR	4	1	5	2
36	RAGHAVENDRA SINGH RATHORE	AB	NA	AB	NA
37	SALUNKHE GAURI RAHUL	9	3	9	3
38	SAURAV SUMAN	10	3	10	3
39	SHASHWAT BHOYAR	7	3	7	3
40	SHARMA SHIVANI HARISH	6	3	5	2
41	SHARMA SHUBHAM KIRAN	8	3	8	3
42	SHUBHAM PANDURANG KALAMKAR	7	3	8	3
43-	SINGH DEVESH DINESH	AB	NA	AB	NA
44	SIRSAT VAISHNAVI DNYANESHWAR	AB	NA	AB	NA
45	SWAROOP BANDU BADWE	9	3	10	3
46	VIKESH BHARTI GOSWAMI	7	13	7	3.
47	WADHAI SAKSHI CHANDRAKANT	AB	NA	AB	NA
48	WANKHADE SANKALP SANJAY	8	3	6	3
49	WANKHEDE HIMANSHU DHANANJAY	AB	NA	AB	NA
50	YASH SOLANKE	9	3	10	3
51	PRATIK PATIL	AB	NA	AB	NA
52	VIVEK MAHAJAN	AB	NA	AB	NA
VE 53	BRIJESH POKAR	AB	NA	AB	NA
54	PRIYA GUTTE	AB	NA	AB	NA



	전 경기 마음 다른 경기 내가 있었다. 이 전 경기 있는 사람들이 되었다. 그런 얼마를 먹게 되었다.			A THE PROPERTY OF THE PARTY OF	AND THE RESERVE AND THE PARTY OF THE PARTY O
55	YASH KASHIKAR	5	2	3	0
56	PAYAL KAMBLE	AB	NA	AB	NA
57	ROHAN PATRE	10	3	5	2
58	RITHIK DEHURIA	7	3	10	3
59	SAYALI PATIL	10	3	7	3
	tal no of students attempted the question	37		37	
Tot	tal no of students securing more than 60%		15		14
To	tal no of students securing more than 50%		2		2
To	tal no of students securing more than 40%		1		2
	Percentage	40	0.54	37	.84
	CO Addressed	C	O-2	C	0-3

Criteria (marks Obtained	Mapping Level
>=60%	3
>=50%	2
>=40%	1

CO Adderessed	Avg	Mapping
CO1	NOT A	ddressed
CO2	40.54	1
CO3	37.84	-1
CO4		
CO5	NOT A	ddressed
CO6		





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Website: https://dypsoe.in/

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	내내가 있는데 선명하게 되는 때 사용하다면서 살아보다 보고 있다.	OI LO	of C Engine	cring			
	Test Marks [D	irect A	ssesment- I	nternal]	Fo	orm No. IQ	AC/36
Unit Test	Number: II					Date:	
	Year:2021-22					Semester:	:II
Subject:P						Max. Ma	rks:30
	Subject Teacher: Mr. Riyaj Kazi			Class:TE		Div:A	
	Question Number	Q.1 (Or Q.2	Q.3	Or Q.4	Q.5 (Or Q.6
	CO Addressed	(CO4	(CO5	CO6	
	Marks	10	Mapping level	10	Mapping level	10	Mappin g level
Roll No.	Name of the Student	Marks	Obtained	Marks	Obtained	Marks (Obtained
1	ABHINAY KUMAR	7	3	8	3	8	3
2	AFAAN IDRISI	7		7		7	
3	BANODIYA NEEL MANOJ	AB		AB		AB	
4	BATTULA NITIN VENKATESH	7		5		8	
5 .	BHOODATT PRATIK RAMESH ~	8		8		5	
Cho di	BISHT VIJAYSINGH NIDHI	AB		AB		AB	
School S	CHEERALA KONA NAVEEN	8		8		7	
On, 3	CHIRIVELLA LAKSHMIMANASA S	7		8		8	
1841	DABHADE TEJAS PANDHARINATH	AB		AB		AB	3000
10	DEEPAK KUMAR THAKUR	7		8		7	
11	DISHA HEGDE	8		8		8	
12	DUBE NIHARIKA PRAMOD	7		7		7	
13	GANVIR SANGHASEN PRAMOD	8		8		5	
14	GARPAL ATHARV VINOD	AB		AB		AB	
15	GHUNDIYAL HARSH RAVINDRAKUMA	AB		AB		AB	
VE 16	GOPANI YASH JAGDISH	AB		AB		AB	

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				-	THE STATE OF THE S		
17	GOPATHI SUPRIYA	9		8		6	
19	GOVIND KADAM	AB		AB		AB	
20	HANNAMSHETTY ABHIJEET M	8		9		6	
21	KADBHANE ASHUTOSH GANESH	6		8		5	
22	KANKATE VAISHNAVI PRITAM	5		7		7	
23	KAPADNE PRADNYA KISHOR	8		8		6	
24	KAWADE AKASH RAVASAHEB	AB	ALTERNATION OF THE PROPERTY.	AB		AB	\$20,000
25	KOKATE AJAY AUDUMBAR	AB		AB		AB	
27	KURHADE ABHISHEK SHYAM	7		7		5	
28	LAWANDE SEJAL ROHIDAS	AB	700 v.24 and	AB		AB	Company of the
29	MAYANK RAJPUROOHIT	AB		AB		AB	
30	MEHETRE SUMIT SATISH	AB		AB		AB	
33	PANDIT SAYALI MADHUKAR	7		7		7	
34	PRANALI MAMADAPURE	7	- Online dis	8		8	This was a
35	PRATIK JAGDISH DIYEWAR	7		8		7	
36	RAGHAVENDRA SINGH RATHORE	8		8		6	
37	SALUNKHE GAURI RAHUL	8		8		8	
38	SAURAV SUMAN	6		7		7	
39	SHASHWAT BHOYAR	9	2.146	9		8	
40	SHARMA SHIVANI HARISH	7		7		9	
41	SHARMA SHUBHAM KIRAN	8		6		5	
42	SHUBHAM PANDURANG KALAMKAR	7		7		7	
	SINGH DEVESH-DINESH	8	-	7		7	- *
43	SIRSAT VAISHNAVI DNYANESHWAR	AB		AB		AB	
1311	SWAROOP BANDU BADWE	AB		AB		AB	
37 46	VIKESH BHARTI GOSWAMI	7		6		6	
46	WADHAI SAKSHI CHANDRAKANT	AB		AB		AB	
48	WANKHADE SANKALP SANJAY	8		8		9	
49	WANKHEDE HIMANSHU DHANANJAY	AB		AB		AB	
50	YASH SOLANKE	AB		AB		AB	
51	PRATIK PATIL	AB		AB		AB	
52	VIVEK MAHAJAN	7		7		7	
	BRIJESH POKAR	8		8		8	
UNE53	PRIYA GUTTE	AB		AB		AB	

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55	YASH KASHIKAR	AB		AB		AB	
56	PAYAL KAMBLE	7		8		8	
57	ROHAN PATRE	5		10		5	
58	RITHIK DEHURIA	7		7		7	
59	SAYALI PATIL	7		8		7	
To	tal no of students attempted the question	35		35		35	
To	tal no of students securing more than 60%	And the second	1		1		1
To	tal no of students securing more than 50%		0		0		0
To	tal no of students securing more than 40%	William Street	0		0		0
	Percentage	2.8	36		.86	2.8	
mile of	CO Addressed	CO)-4	C	0-5	CO)-6

Criteria	Mapping Level
>=60%	3
>=50%	2
>=40%	1

CO Adderessed	Avg Mapping					
CO1						
CO2	NOT A	ddressed				
CO3						
CO4	2.86	1				
CO5	2.86	1				
CO6	2.86	1				







Empowerment Through Quality Technical Education

Dr. D. Y. Patil School of Engineering
Dr. D. Y. Patil Knowledge City,
Charholi (Bk), Lohegaon, Pune – 412 105 Website: https://dypsoe.in/

Department of E&TC Engineering

Acaden	nic Year: 2021-22	Se	mester:II
Subject		Di	v:A
	f Subject Teacher:Mr. Riyaj Kazi	The Air Co	
Roll No.		CA	(out of 25)
1	ABHINAY KUMAR		22
2	AFAAN IDRISI		21
3	BANODIYA NEEL MANOJ		19
4	BATTULA NITIN VENKATESH		18
5	BHOODATT PRATIK RAMESH	A Secretary	22
6	BISHT VIJAYSINGH NIDHI		20
7	CHEERALA KONA NAVEEN		19
8	CHIRIVELLA LAKSHMIMANASA S	TAVIA HE	19
9	DABHADE TEJAS PANDHARINATH		17
10	DEEPAK KUMAR THAKUR		18
11	DISHA HEGDE		19
12	DUBE NIHARIKA PRAMOD		21
13	GANVIR SANGHASEN PRAMOD	period the	22
14	GARPAL ATHARV VINOD		23
15	GHUNDIYAL HARSH RAVINDRAKUMAR		20
16	GOPANI YASH JAGDISH		19
17	GOPATHI SUPRIYA		21
19	GOVIND KADAM		21
20	HANNAMSHETTY ABHIJEET M	-	18
21	KADBHANE ASHUTOSH GANESH	of the second	19
22	KANKATE VAISHNAVI PRITAM		20
23	KAPADNE PRADNYA KISHOR		20
24	KAWADE AKASH RAVASAHEB		21
25	KOKATE AJAY AUDUMBAR		23
27	KURHADE ABHISHEK SHYAM		22
28	LAWANDE SEJAL ROHIDAS		21







29	MAYANK RAJPUROOHIT	18
30	MEHETRE SUMIT SATISH	19
33	PANDIT SAYALI MADHUKAR	20
34	PRANALI MAMADAPURE	17
35	PRATIK JAGDISH DIYEWAR	21
36	RAGHAVENDRA SINGH RATHORE	22
37	SALUNKHE GAURI RAHUL	19
38	SAURAV SUMAN	18
39	SHASHWAT BHOYAR	20
40	SHARMA SHIVANI HARISH	22
41	SHARMA SHUBHAM KIRAN	21
42	SHUBHAM PANDURANG KALAMKAR	20
43	SINGH DEVESH DINESH	19
44	SIRSAT VAISHNAVI DNYANESHWAR	20
45	SWAROOP BANDU BADWE	18
46	VIKESH BHARTI GOSWAMI	17
47	WADHAI SAKSHI CHANDRAKANT	21
48	WANKHADE SANKALP SANJAY	22
49	WANKHEDE HIMANSHU DHANANJAY	20
50	YASH SOLANKE	21
51	PRATIK PATIL	22
52	VIVEK MAHAJAN	23
53	BRIJESH POKAR	21
54	PRIYA GUTTE	22
55	YASH KASHIKAR	20
56	PAYAL KAMBLE	21
57	ROHAN PATRE	19
58	RITHIK DEHURIA	18
59	SAYALI PATIL	22
	TOTAL COUNT OF STUDENTS	55
	TOTAL MARKS OF STUDENTS	1108
	AVERAGE MARKS	20.15
建筑线	% ASSESSMENT OF AVERAGE MARKS (out of 25)	80.58

Note: Department/Subject teacher has flexibility to decide criterias of CA i.e.quizes, assignments, open book tests, presentations etc...









Empowerment Through Quality Technical Education

Dr. D. Y. Patil School of Engineering

Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohegaon, Pune – 412 105

Website: https://dypsoe.in/

Department

	of	Engineering
Guide	lines for Course Exit Survey [Indirect Assesment]	Form No. IQAC/36
1	Frame one question based on each CO "students will be able to understand the mathematical description systems", mathematical description of continuous and discrete time signal.	Frame question as:"Have you understood the
2	Responses shall be on the scale of 1 to 3 as 1: Slightly Understood 2: Moderately Understood 3: Clearly Understood	





PRINCIPAL ADYPSOE, PUNE



Academic Year:

Empowerment Through Quality Technical Education

Dr. D. Y. Patil School of Engineering

Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohegaon, Pune – 412 105 Website: https://dypsoe.in/

Department of

Engineering

Course Exit Survey [Indirect Assesment] Form No. IQAC/36

Semester:

Class: Div.

Subject:	Subject Teacher:			Class:		Div:	
Roll No.	Name of student	CO-1	CO-2	CO-3	CO-4	CO-5	CO-6
1	ABHINAY KUMAR	1	3	3	3	3	3
2	AFAAN IDRISI	3	3	3	3	3	3
3	BANODIYA NEEL MANOJ	2	1	3	2	2	2
4	BATTULA NITIN VENKATESH	3	3	2	3	3	3
5	BHOODATT PRATIK RAMESH	3	3	3	3	2	3
6	BISHT VIJAYSINGH NIDHI	3	3	3	3	3	3
7	CHEERALA KONA NAVEEN	3	3	3	3	3	3
8	CHIRIVELLA LAKSHMIMANASA S	3	3	1	3	3	3
9	DABHADE TEJAS PANDHARINATH	2	3	3	2	2	3
10	DEEPAK KUMAR THAKUR	3	3	3	3	3	2
11	DISHA HEGDE	3	3	2	3	3	3
12	DUBE NIHARIKA PRAMOD	3	3	3	3	3	3
13	GANVIR SANGHASEN PRAMOD	.1	1	1	3	3	2
14	GARPAL ATHARY VINOD	2	3	- 3	2	1	1
15	GHUNDIYAL HARSH RAVINDRAKUM.	~- 1	3	3	3	3	3,
16	GOPANI YASH JAGDISH	3	3	3	3	3	3
17	GOPATHI SUPRIYA	2	1	3	2	2	2
19	GOVIND KADAM	3	3	2	3	3	3
20	HANNAMSHETTY ABHIJEET M	3	3	3	3	2	3
21	KADBHANE ASHUTOSH GANESH	3	3	3	3	3	3
22	KANKATE VAISHNAVI PRITAM	3	3	3	3	3	3
23	KAPADNE PRADNYA KISHOR	3	3	1	3	3	3
24	KAWADE AKASH RAYASAHEB	2	3	3	2	2	3
25	KOKATE AJAY AUDUMBARCA	3	3	3	3	3	2

PRINCIPAL ADYPSOE, PUNE



	Course Outcome	COI	CO2	CO3	CO4	CO5	CO6
	%	83.64	90.30	85.45	93.33	89.09	89.09
	Total count of students 55	3	3	3	3	3	3
59	SAYALI PATIL	1	1	1	3	3	2
58	RITHIK DEHURIA	3	3	3	3	3	3
57	ROHAN PATRE	3	3	2	3	3	3
56	PAYAL KAMBLE	3	3	3	3	3	2
55	YASH KASHIKAR	2	3	3	2	_ 2	3
54	PRIYA GUTTE	3	3	1	3	3	3
53	BRIJESH POKAR	3	3	3	3	3	3
52	VIVEK MAHAJAN	3	3	3	3	3	3
51	PRATIK PATIL	3	3	3	3	2	3
50	YASH SOLANKE	3	3	2	3	3	3
49	WANKHEDE HIMANSHU DHANANJAY	2	1	3	2	2	2
48	WANKHADE SANKALP SANJAY	3	3	3	3	3	3
47	WADHAI SAKSHI CHANDRAKANT	1	3	3	3	3	1
46	VIKESH BHARTI GOSWAMI	2	3	3	3	3	2
45	SWAROOP BANDU BADWE	1	1	1	3	3	3
44	SIRSAT VAISHNAVI DNYANESHWAR	3	3	3	3	3	3
43	SINGH DEVESH DINESH	3	3	3	3	3	1
42	SHUBHAM PANDURANG KALAMKAR	3	3	3	2	2	
41	SHARMA SHUBHAM KIRAN	2	3	1	3	3	
40	SHARMA SHIVANI HARISH	3		3	3	3	
39	SHASHWAT BHOYAR	3	3	3	3	3	
38	SAURAV SUMAN	3	3	3	3	2	
37	SALUNKHE GAURI RAHUL	3	3	2	3	3	
36	RAGHAVENDRA SINGH RATHORE	2	1	3	2	2	TANK.
35	PRATIK JAGDISH DIYEWAR	3	3	3	3	3	
34	PRANALI MAMADAPURE	1	3	3	3	3	
33	PANDIT SAYALI MADHUKAR	2	3	3	2	1	243
30	MEHETRE SUMIT SATISH	1	1	1	3	3	17 200
29	MAYANK RAJPUROOHIT	3	3	3	3	3	F PER
28	KURHADE ABHISHEK SHYAM LAWANDE SEJAL ROHIDAS	3	3	2	3	3	

PRINCIPAL PUNE + BUNDAN





Academic Year:2021-22

Subject: PDC

Name of Subject Teacher: Mr. Riyaj Kazi

		Blooms Taxonomy				nent (Inter	mal) (30°	%)	Direc	t Assesmen Univers	t (Extern ity Exam		Direct Assessment (DA)	Indirect	со	
				Unit To (20%				CA (10%)		Subject Result (60%)		0%)		0	Attainment	
CO No	Statement of COS		Summ ative Test	UTI	UT 2	Mapping	CA	Mapping	% result of Sub.	Mapping	% Result of PR / OR / TW	Mapping	Mapping of (20% Internal tests+10% Continous Assessment +60% Univ result(TH)+10% Univ oral result	Course Exit Survey	Mapping	Weightage (80% DA+ 20% IDA)
CO-1	To differentiate based on the characteristic parameters among SCR, GTO, MOSFET & IGBT and identify enitability of the power device for cartain	1-Remember	93.18			3.0	80.58	3	94,16	3.0	100.00	3.0	3,00	83.64	3.0	3.00
CO-2	To design triggering / driver circuits for various power devices.	6-Create		40.54	NA.	1,0	80.58	3	94.16	3,0	100.00	3.0	2.6	90_30	3.0	2.68
CO-3	To evaluate and analyze various performance parameters of the different converters and its topologies.	4-Analyze	7	37,84		0,0	80.58	3	94.16	3.0	100.00	3.0	- 2.4	85.45	3,0	2.52
CO-4	To understand significance and design of various protections circuits for power devices.	2-Understand			2.86	0,0	80.58	3	94.16	3.0	100.00	3.0	2.4	93,33	3,0	2.52
	To evaluate the performance of uninterruptible power supplies, switch mode power supplies and pattery.	5- Evaluate			2.86	0,0	80.58	3	94,16	3.0	100.00	3.0	2,4	89.09	3,0	2.52
20-6	To understand case studies of power electronics in applications like electric vehicles, solar systems etc.	2-Understand			2,86	0.0	80.58	3	94.16	3.0	100,00	3.0	2.4	89,09	3.0	2.52

PRINCIPAL ADYPSOE, PUNE Empowerment Through Quality Technical Education Dr. D. Y. Patil School of Engineering Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohegaon, Punc – 412 105 Website: https://dypsce.in/

Department of E&TC Engineering

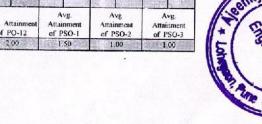
CO-PO-PSO Attainment

Form No. IQAC/36

Semester:II

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ADYPSOE, PUNE



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"Empowerment through quality technical education"

AJEENKYA DY Patil School of Engineering

Dr. D. Y. Patil Knowledge City, Charholi (Bk), Lohgaon, Pune – 412 105

Department of E&TC Engineering

20

Faculty Feedback



"Empowerment through quality technical education" AJEENKYA DY PATIL SCHOOL OF ENGINEERING

Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune - 412 105. Department of **E&TC** Engineering

Form No. IQAC/19

Faculty Feedback Form-(I)

23/03/2023

Academic Year:2022-23

Semester - II

Feedback- I

Class & Department: TE E&TC

Div:B

To,

Prof. Riyaj Kazi

Designation: Professor

Subject: Regarding your teaching feedback given by students: Excellent

Dear Sir/Madam,

We would like to inform you that your students feedback score is 91.12 %

Your performance is measured on the basis of students feedback taken for the Subject Power Devices and Circuits (TH)

The average score of various attributes of teaching learning process are considered in the performance assessment.

The benchmark set for feedback is as follows:

- 1. Below 75%- Unsatisfactory and need to give a written Explanation to us
- 2. 75 to 85% Satisfactory
- 3. 86 to 95%- Excellent
- 4. Above 95%- Outstanding

Based on above feedback:

☐ We appreciate your efforts towards improving the teaching-learning process.

anking You,

Corrective actions (If applicable)

1. Nil

Copy to:

Concerned Staff iv)

V) HoD

vi) Personal File

Principal

PRINCIPAL ADYPSC PUNE



"Empowerment through quality technical education" AJEENKYA DY PATIL SCHOOL OF ENGINEERING

Dr. D. Y. Patil Knowledge City, Charholi Bk., Via. Lohegaon, Pune - 412 105. Department of **E&TC** Engineering

Form No. IQAC/19

Faculty Feedback Form-(I)

23/03/2023

Academic Year: 2022-23

Semester - II

Feedback- I

Class & Department: TE E&TC

Div:A

To,

Prof. Riyaj Kazi

Designation: Professor

Subject: Regarding your teaching feedback given by students: Excellent

Pear Sir/Madam,

We would like to inform you that your students feedback score is 88.24 %

Your performance is measured on the basis of students feedback taken for the Subject Power Devices and Circuits

The average score of various attributes of teaching learning process are considered in the performance assessment.

The benchmark set for feedback is as follows:

- 1. Below 75%- Unsatisfactory and need to give a written Explanation to us
- 2. 75 to 85% Satisfactory
- 3. 86 to 95%- Excellent
- 4. Above 95%- Outstanding

Based on above feedback:

☐ We appreciate your efforts towards improving the teaching-learning process.

anking You,

Corrective actions (If applicable)

1. Nil

Copy to:

i) Concerned Staff

ii) HoD

iii) Personal File

Principal

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